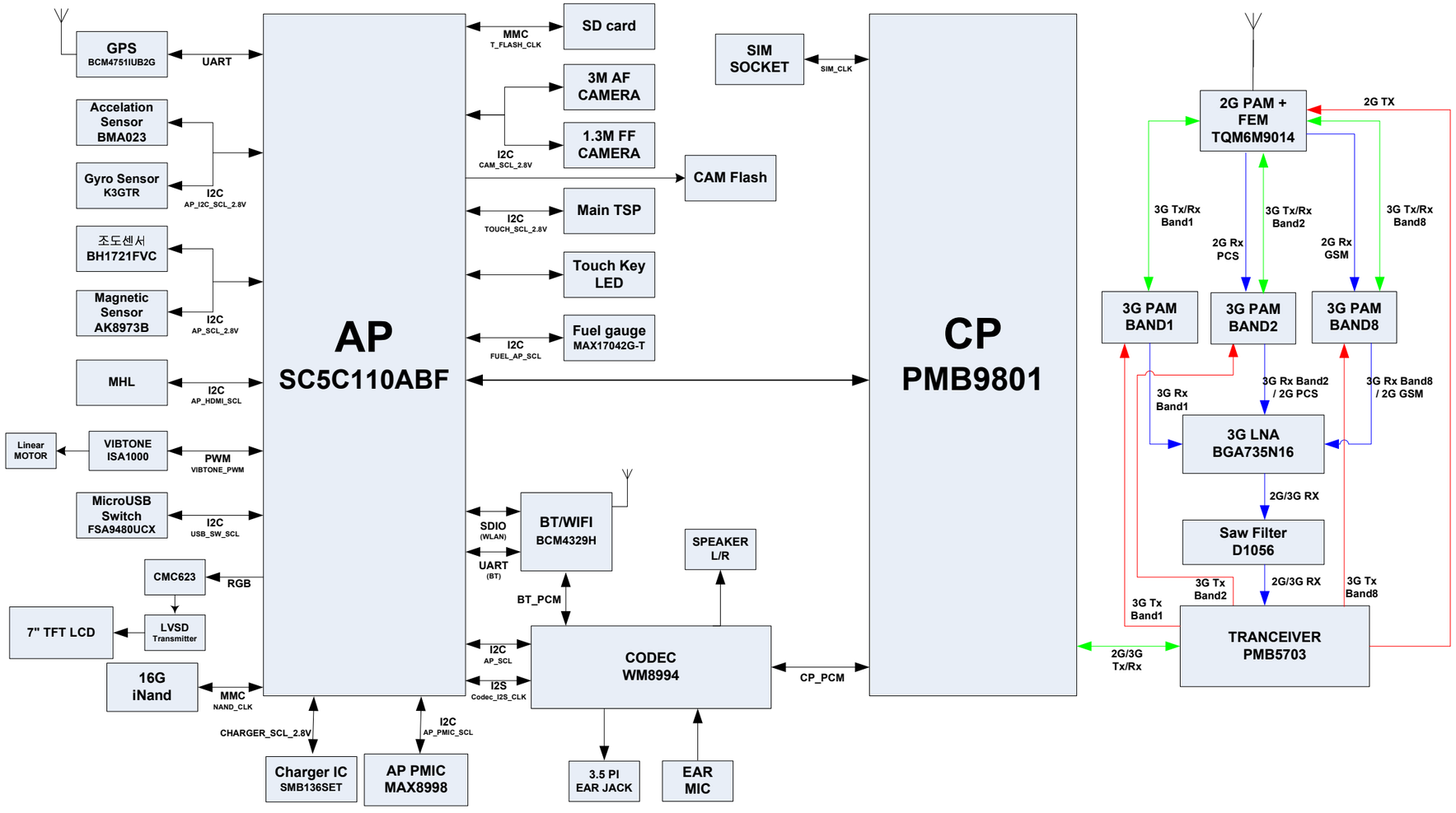
8. Level 3 Repair

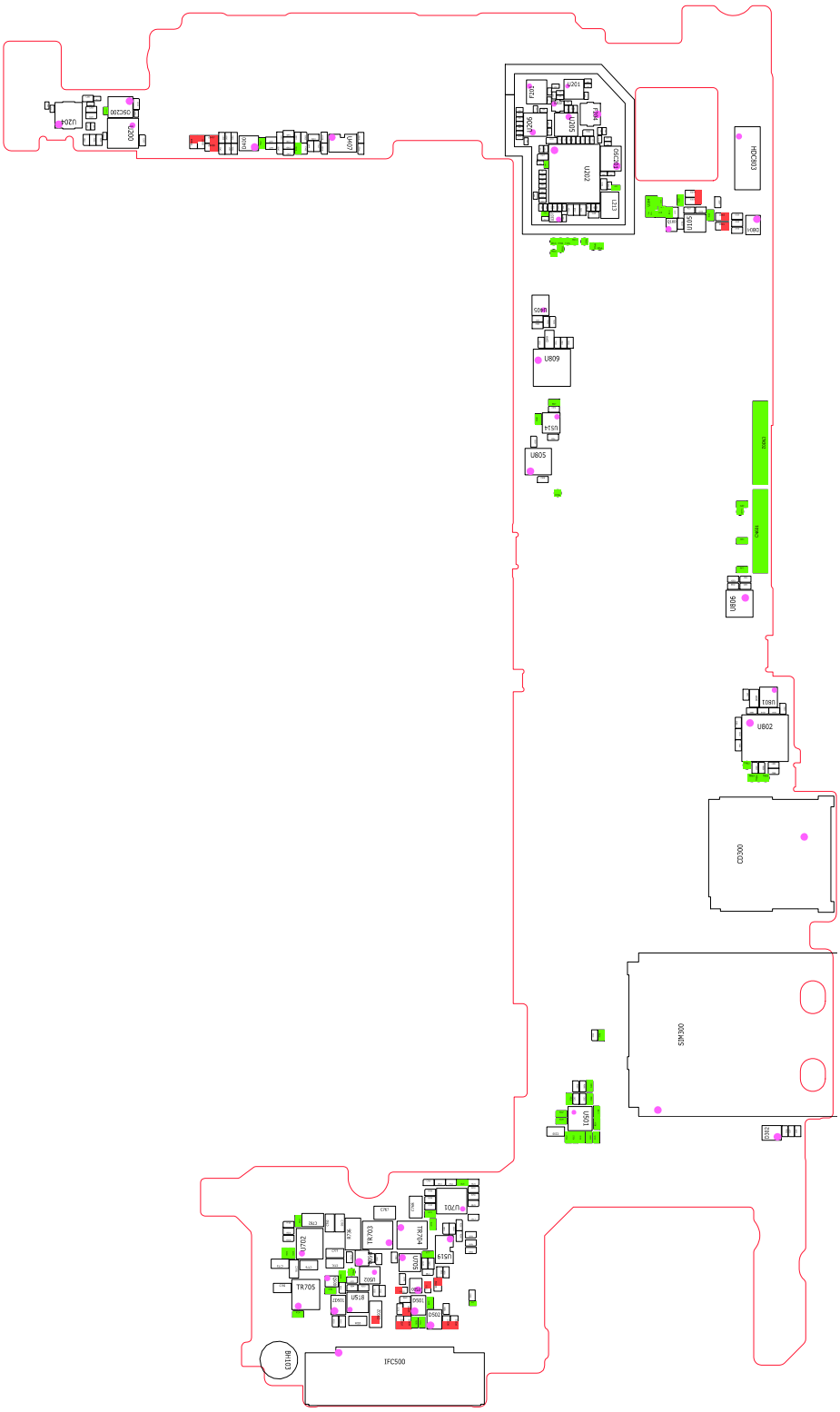
8-1. Block Diagram



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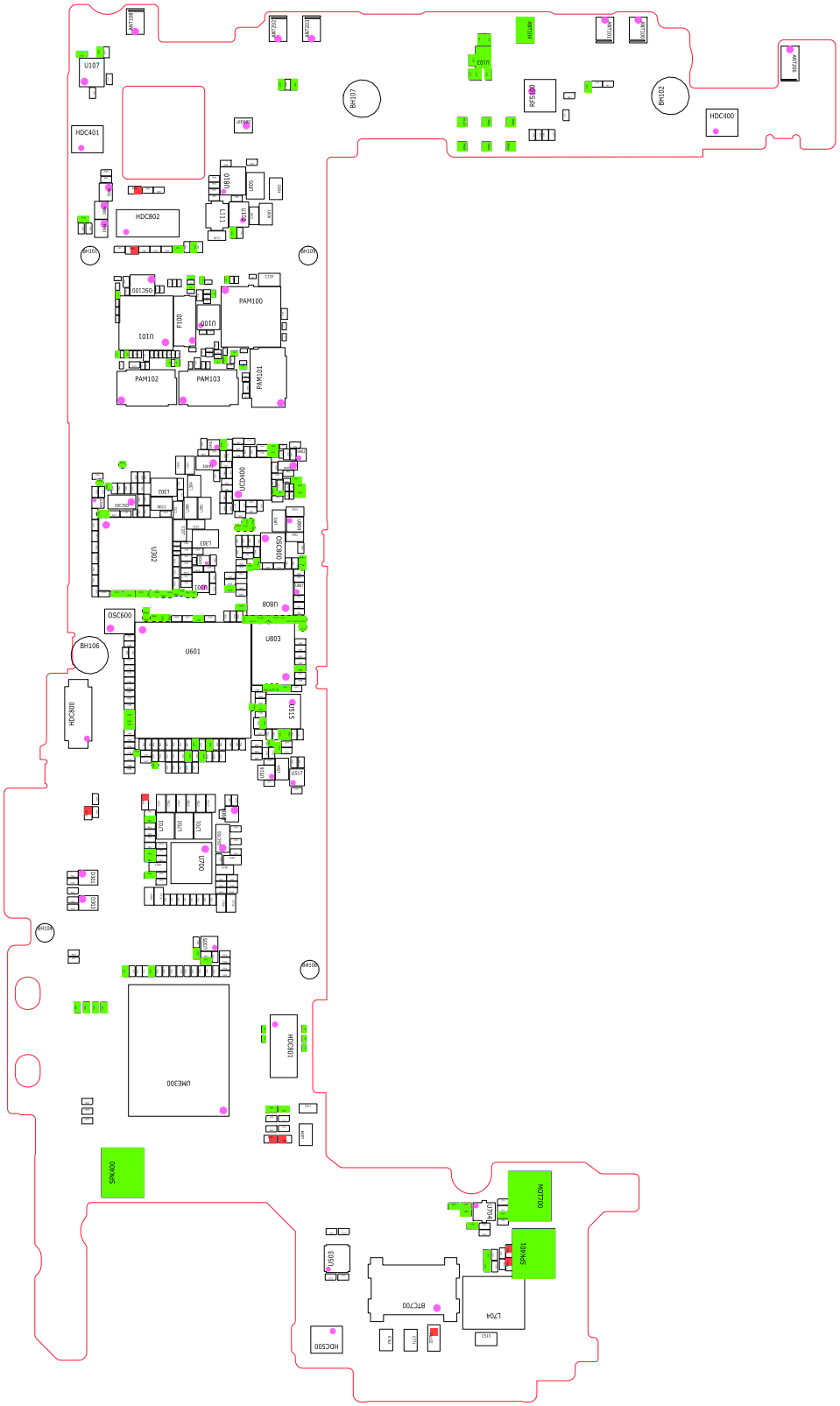
8-2. PCB Diagrams

8-2-1. Top



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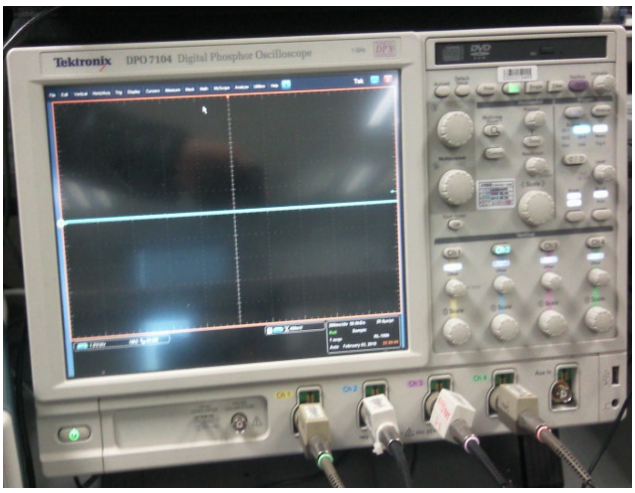
8-2-2. Bottom



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8-3. Flow Chart of Troubleshooting

Equipments



ㄱ Oscilloscope



ㄱ Digital Multimeter



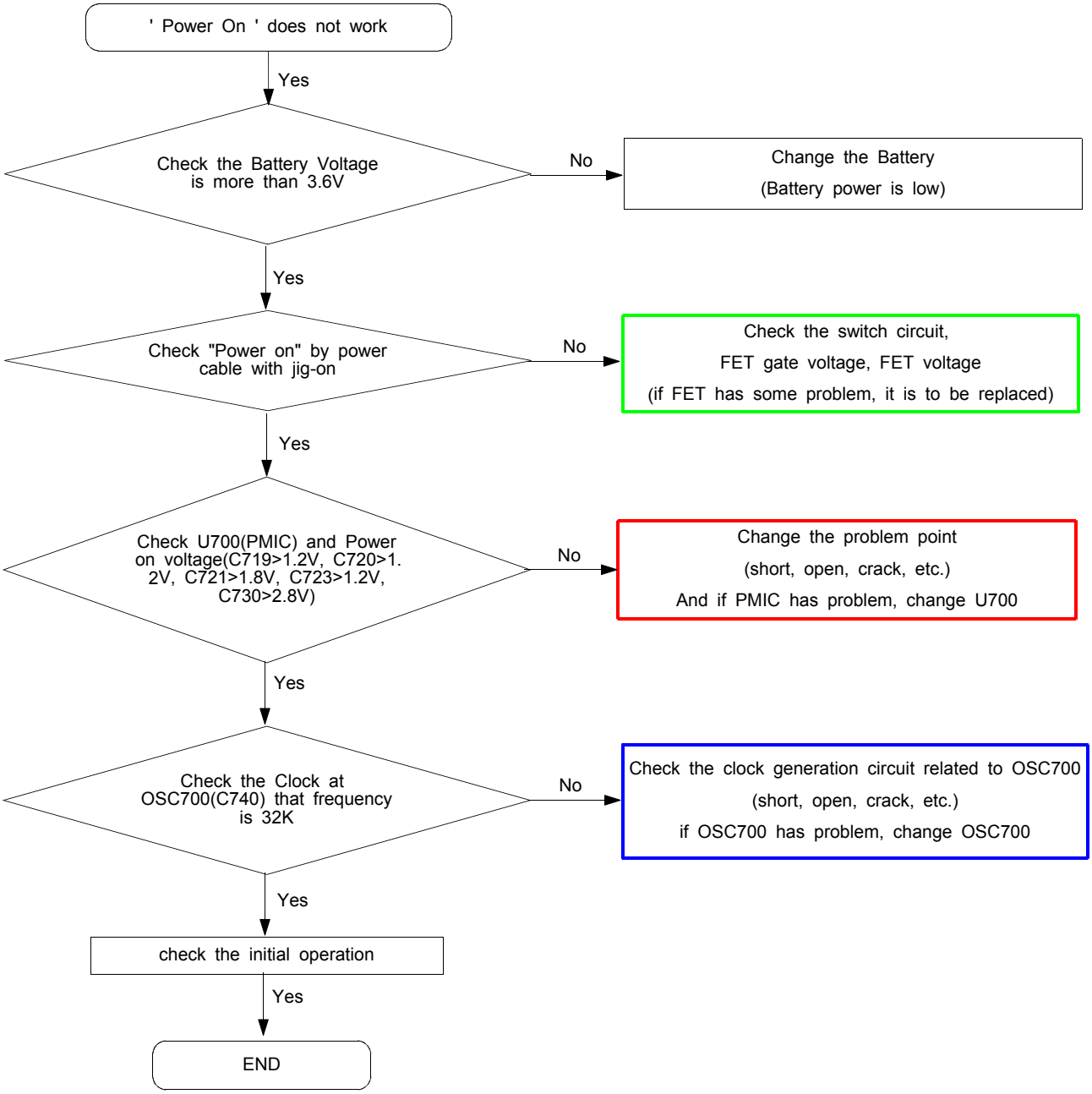
ㄱ Power Supply



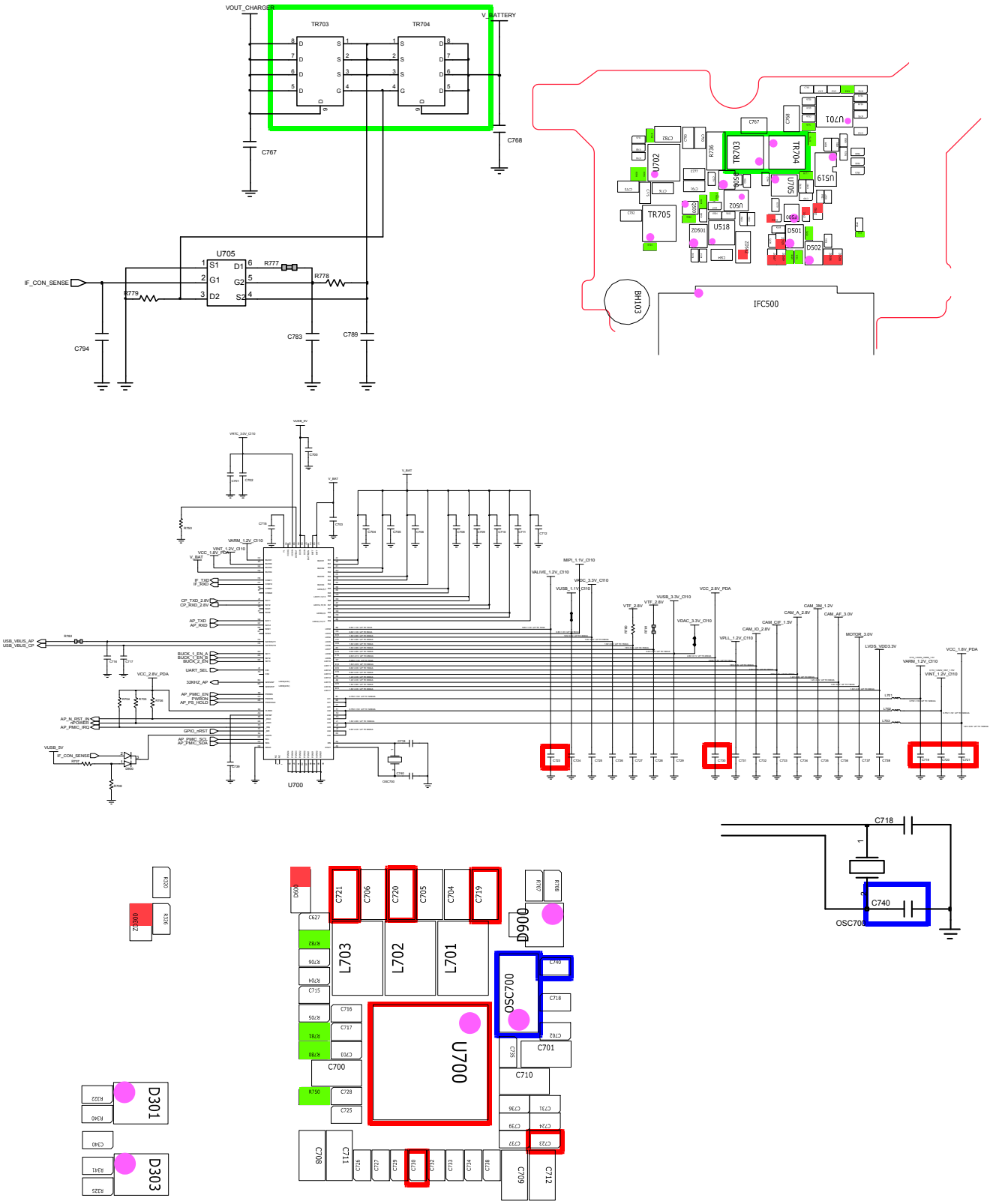
ㄱ + driver, Tweezer

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8-3-1. Power On

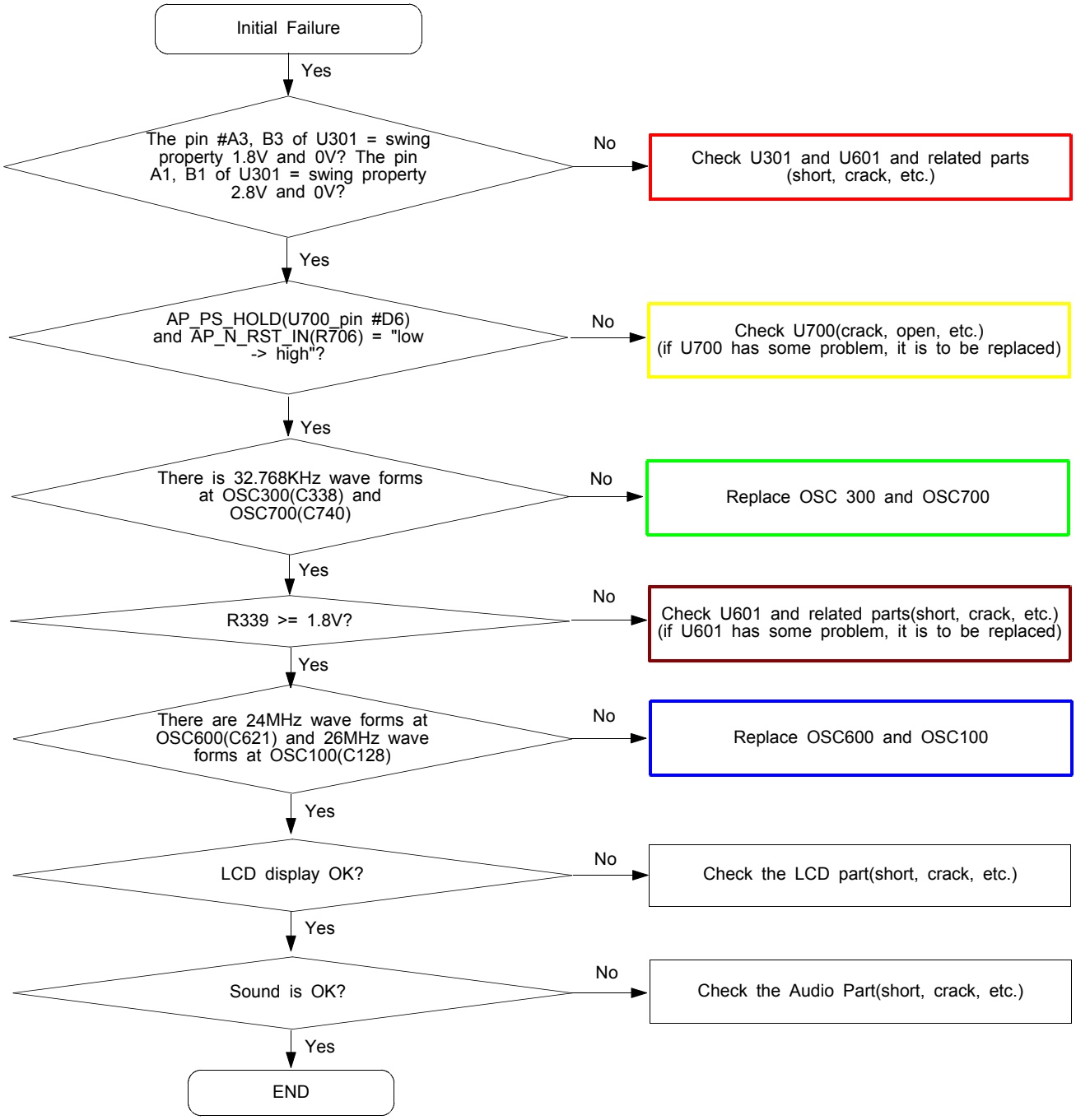


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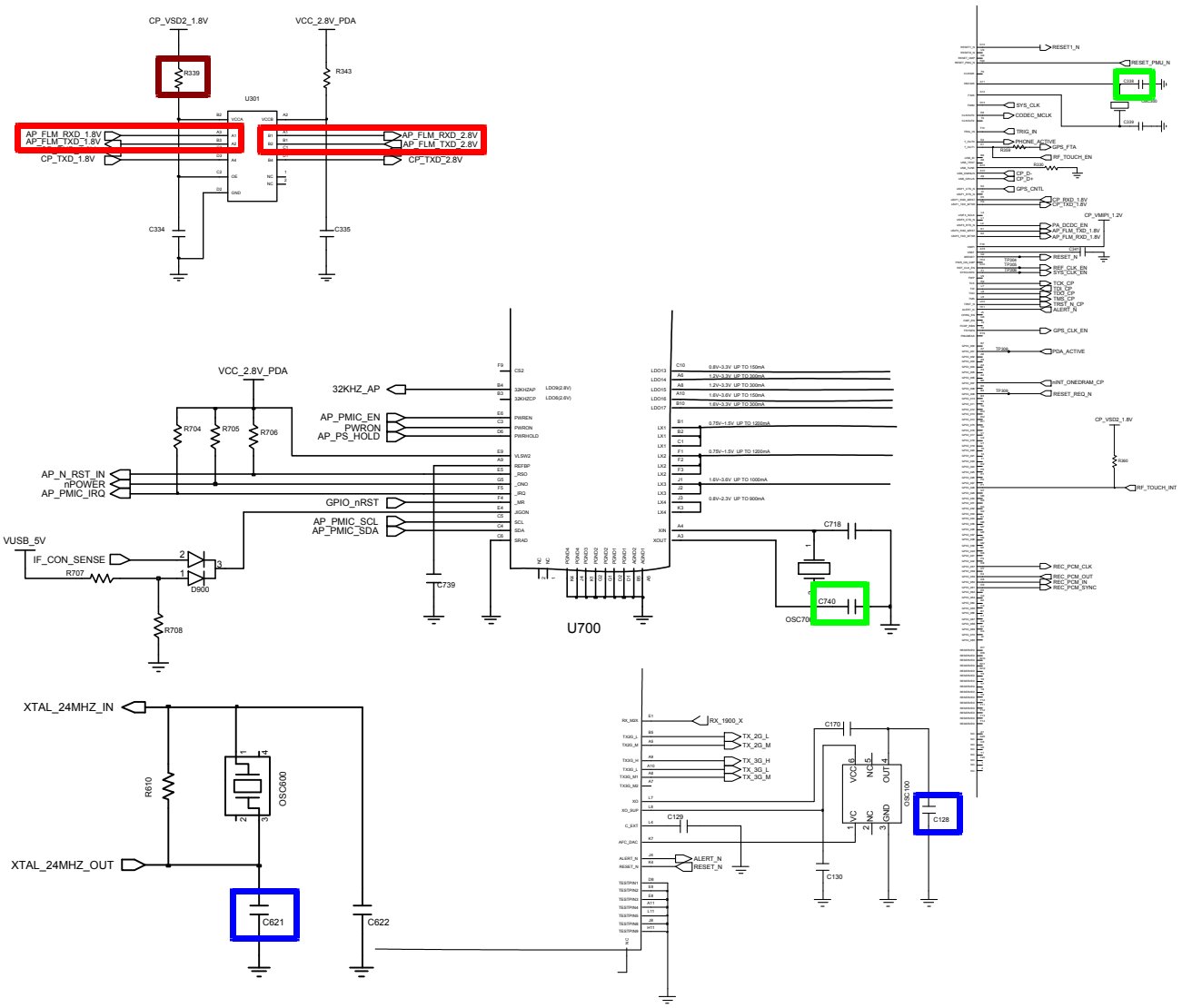


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8-3-2. Initial

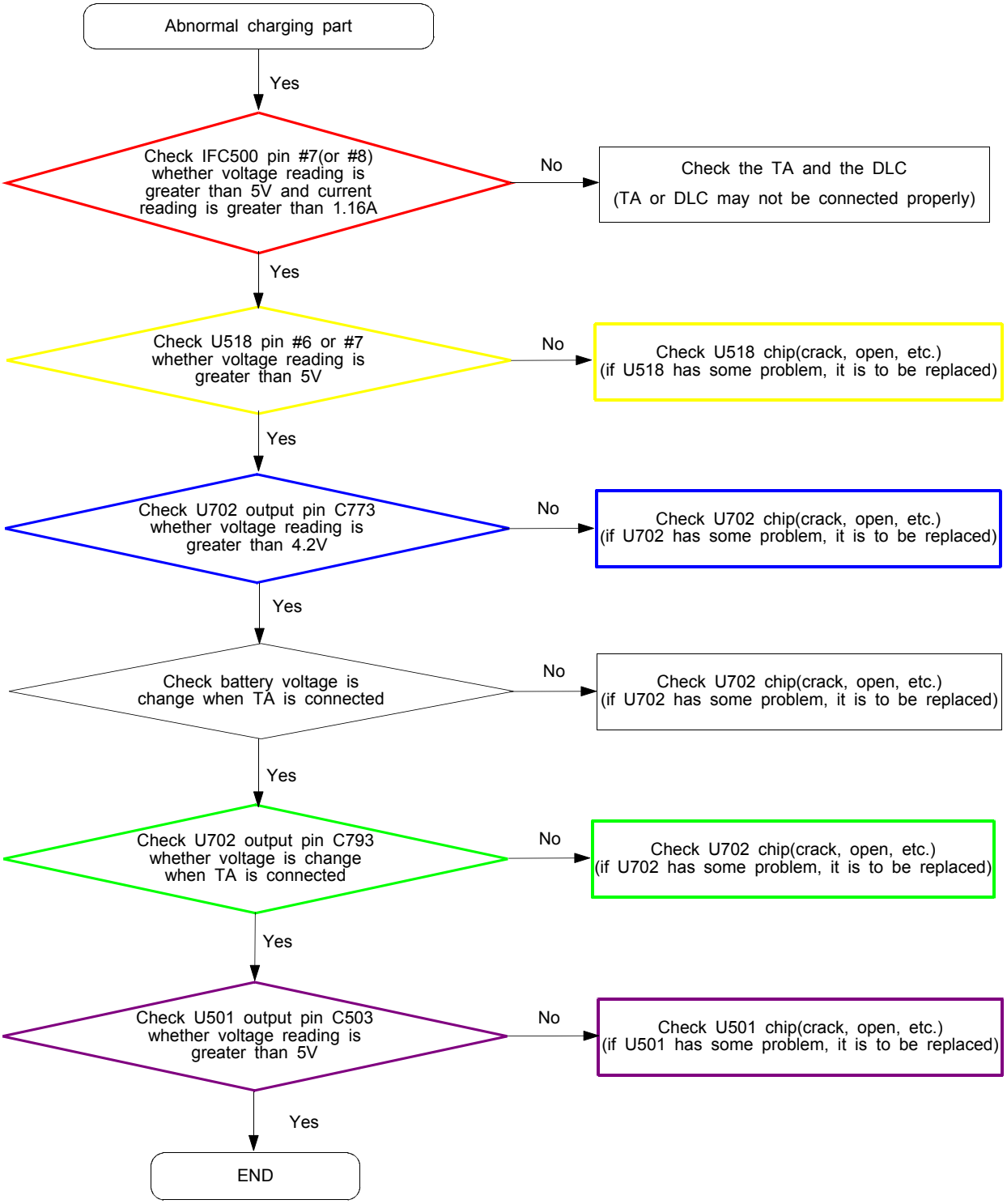


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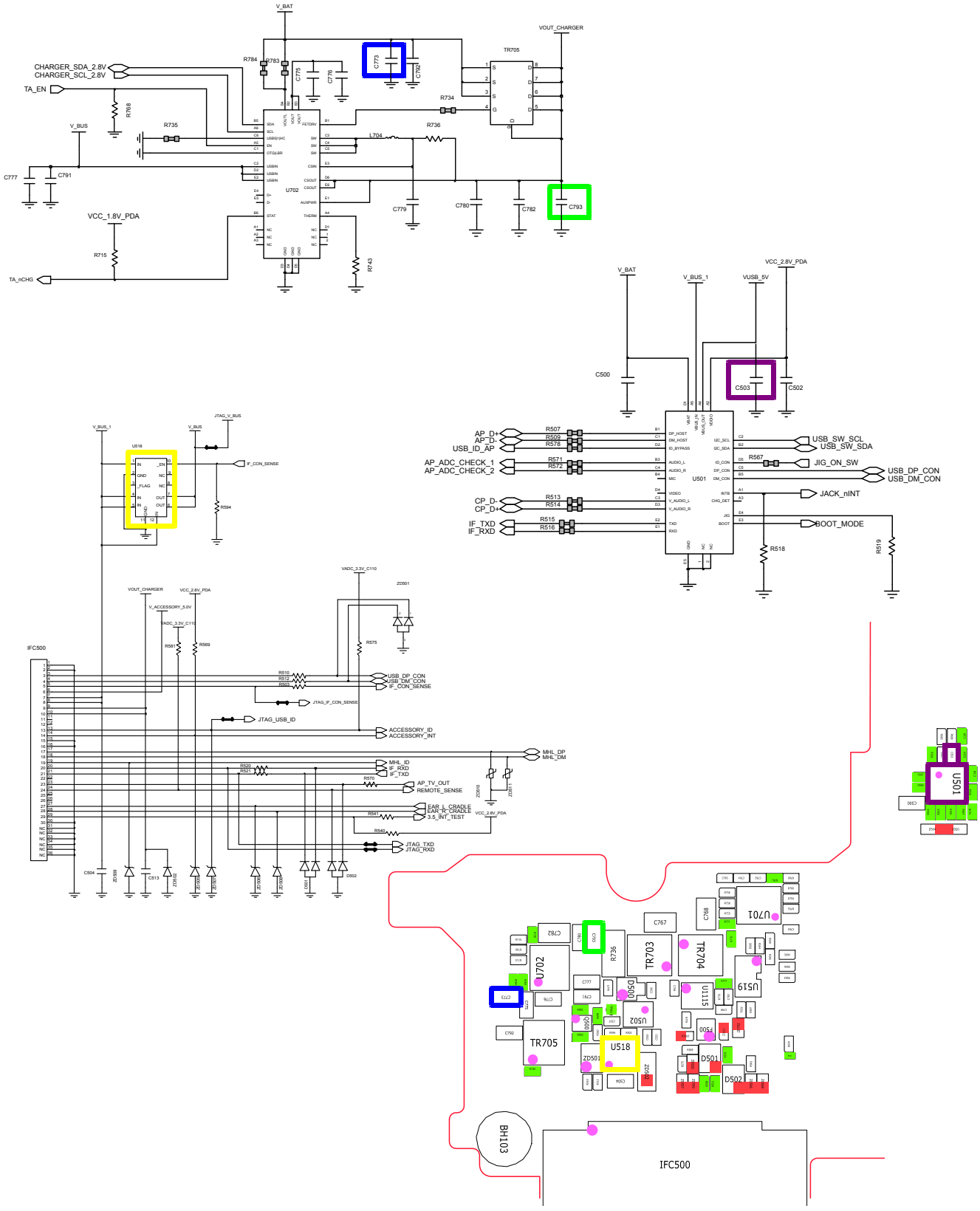


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8-3-3. Charging Part

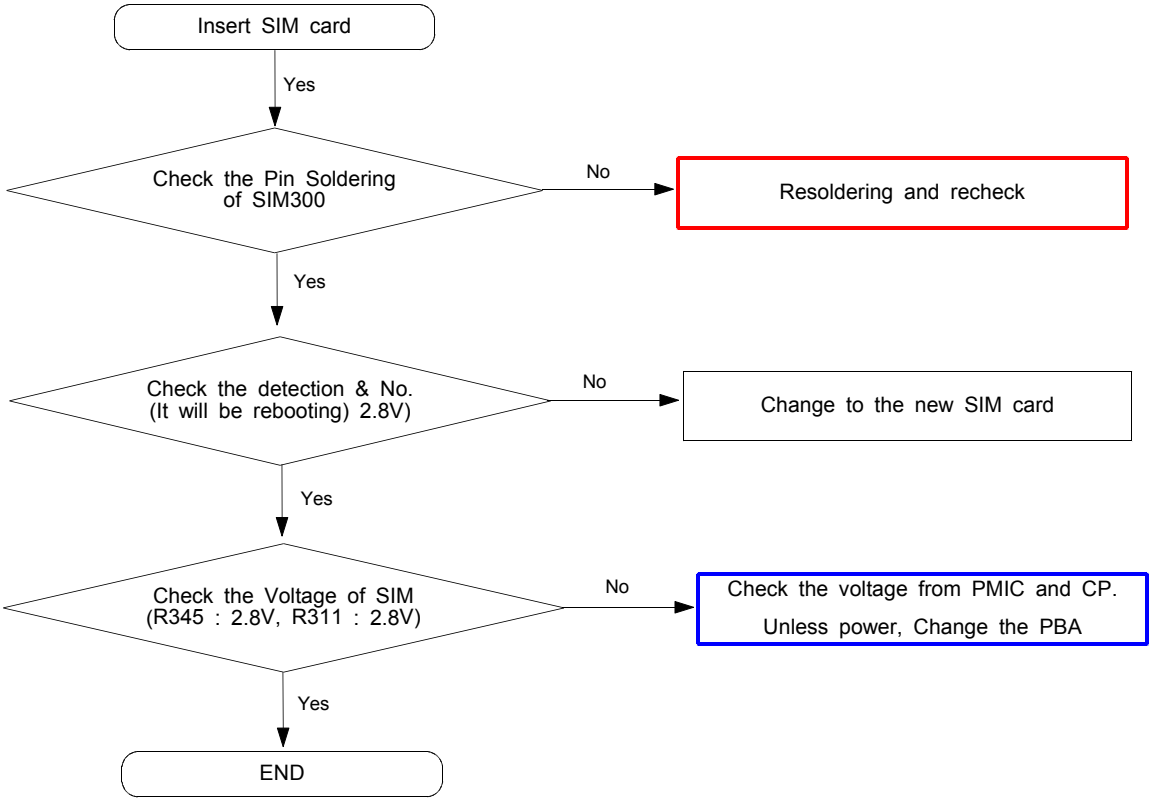


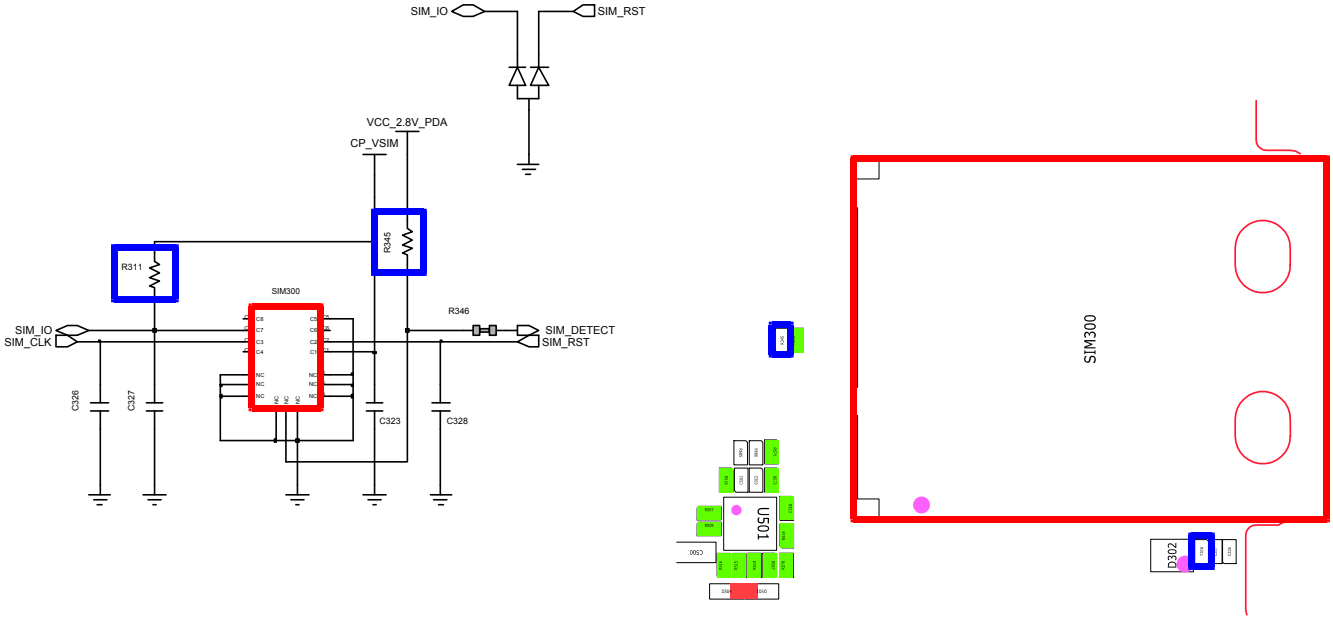
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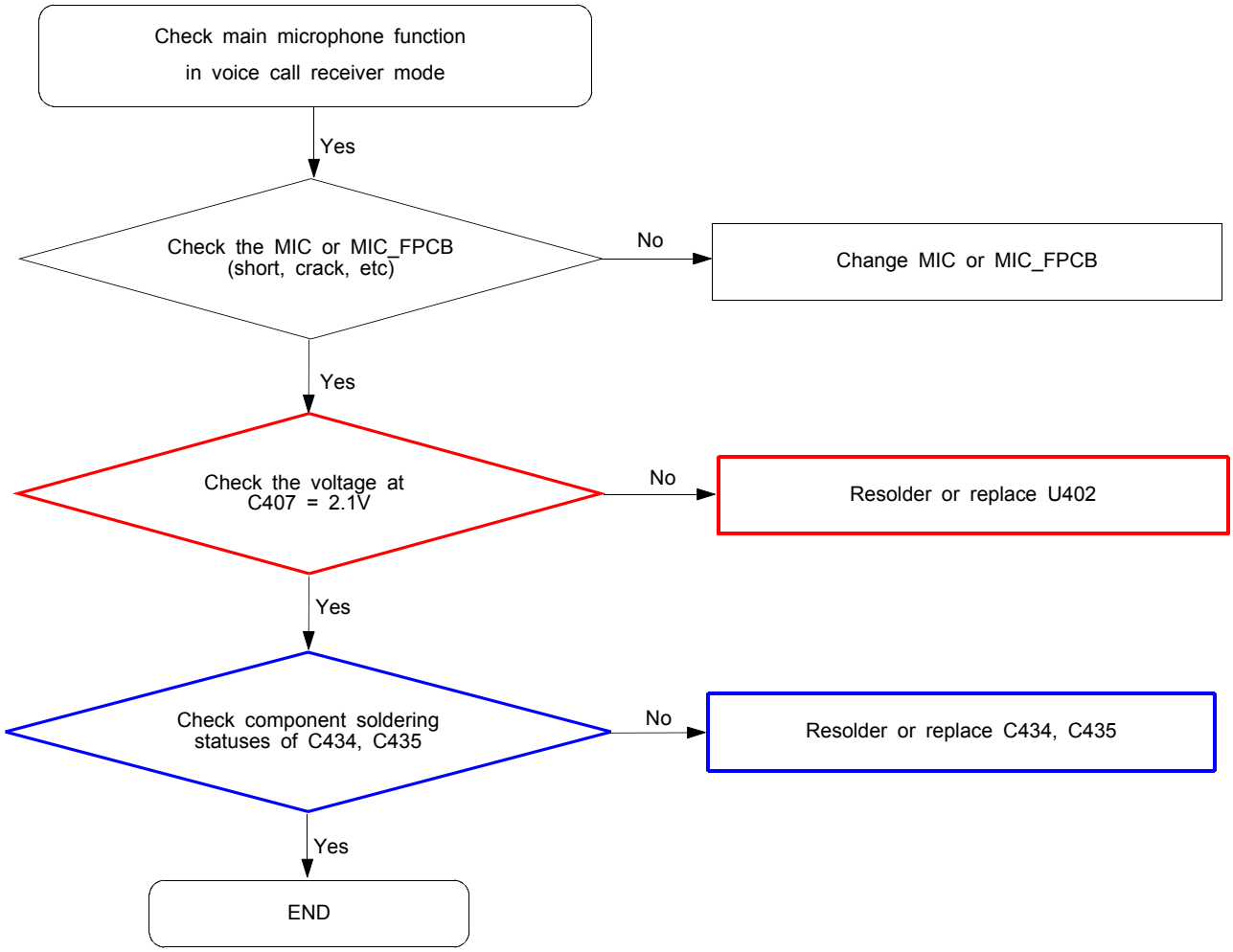
8-3-4. Sim Part



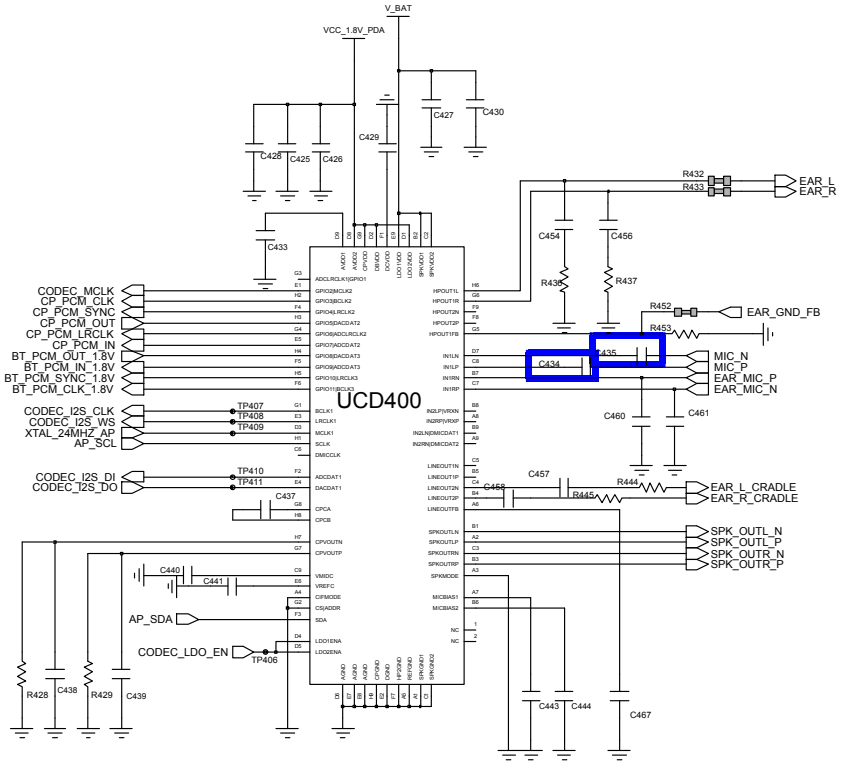


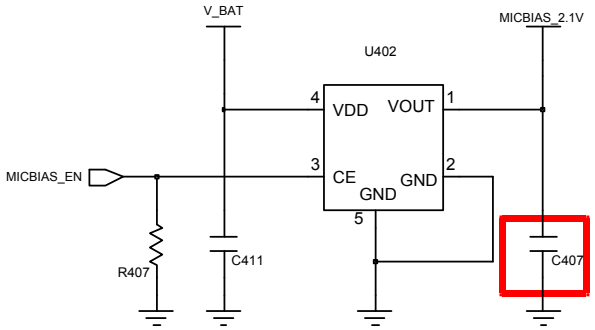
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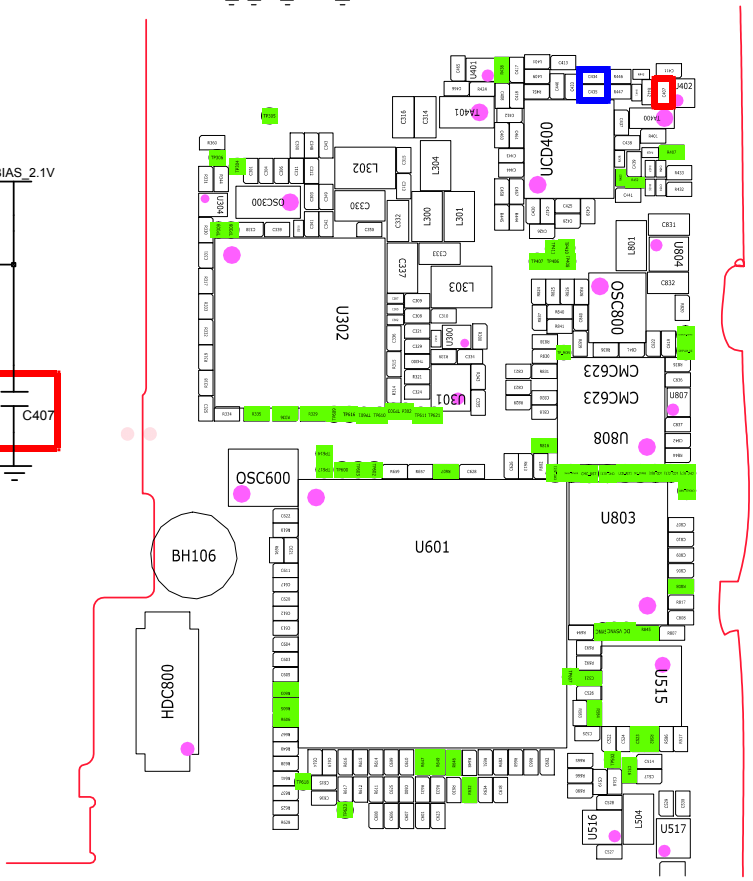
8-3-5. Microphone Part



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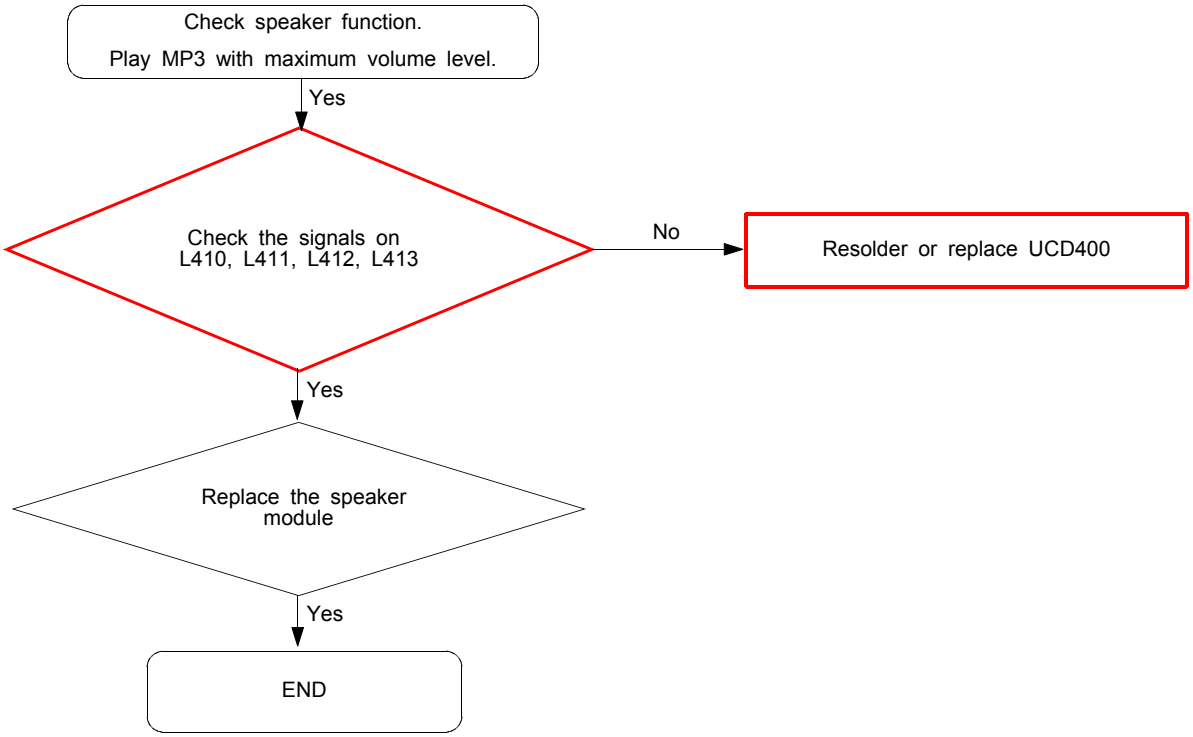




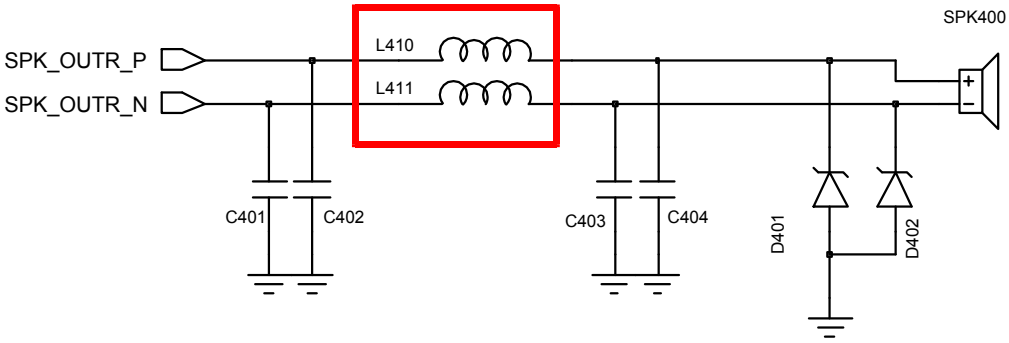


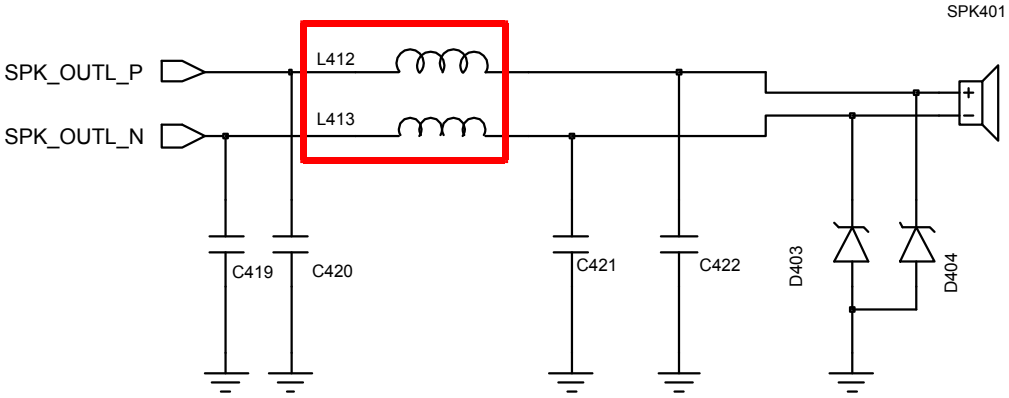
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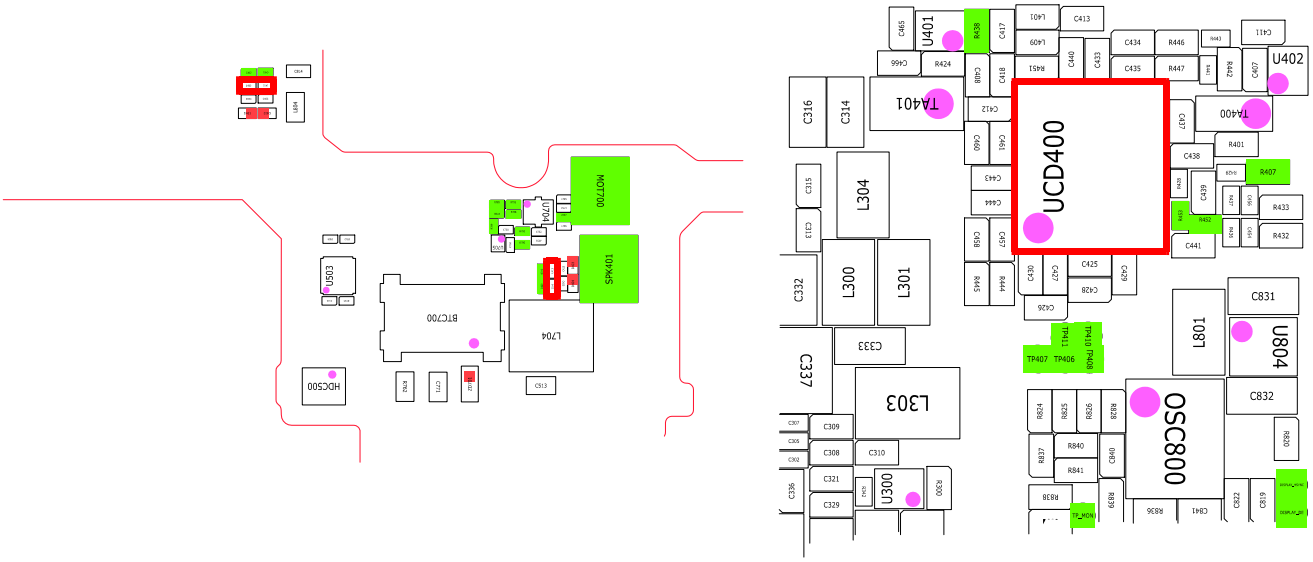
8-3-6. Speaker Part



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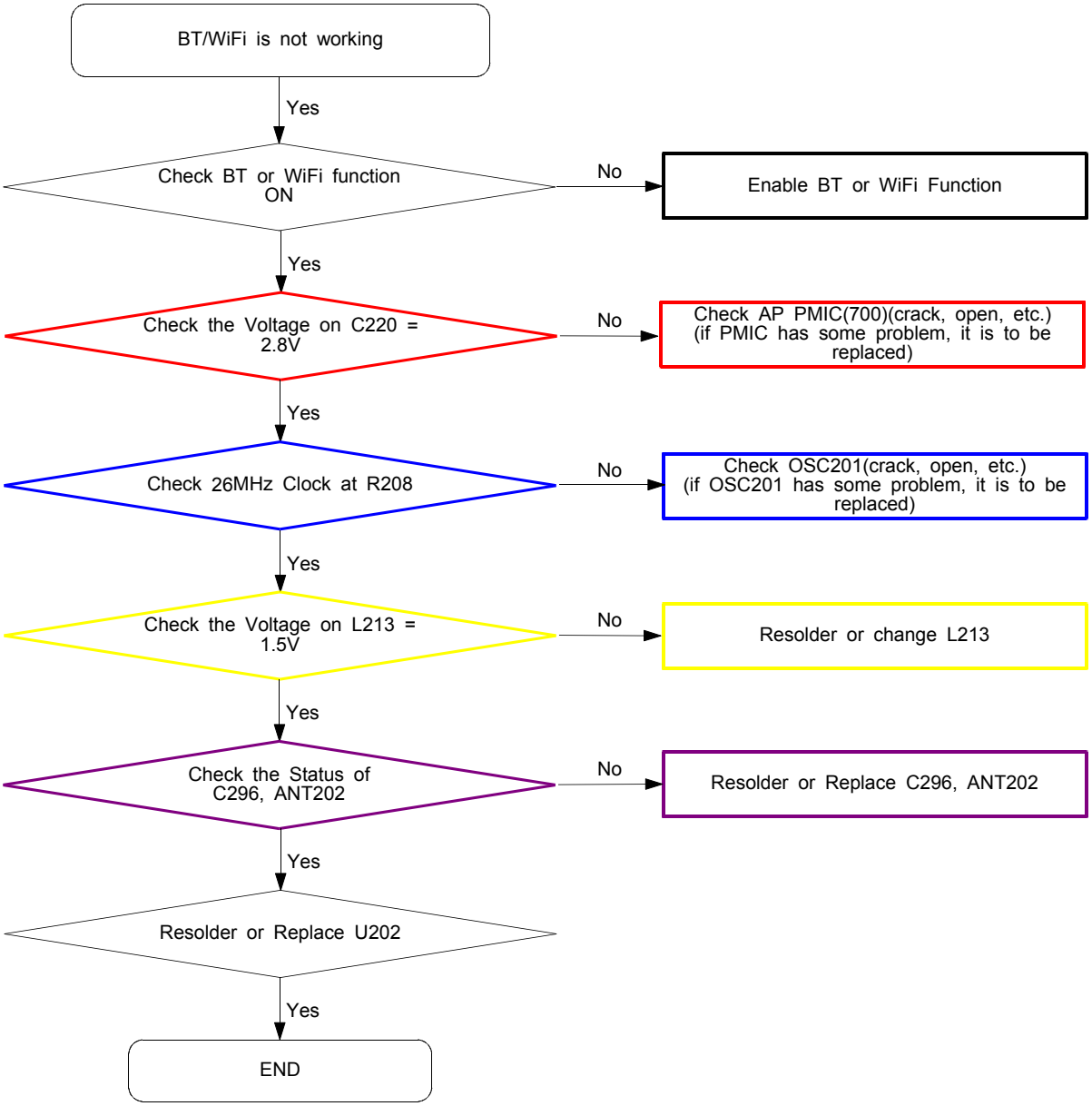




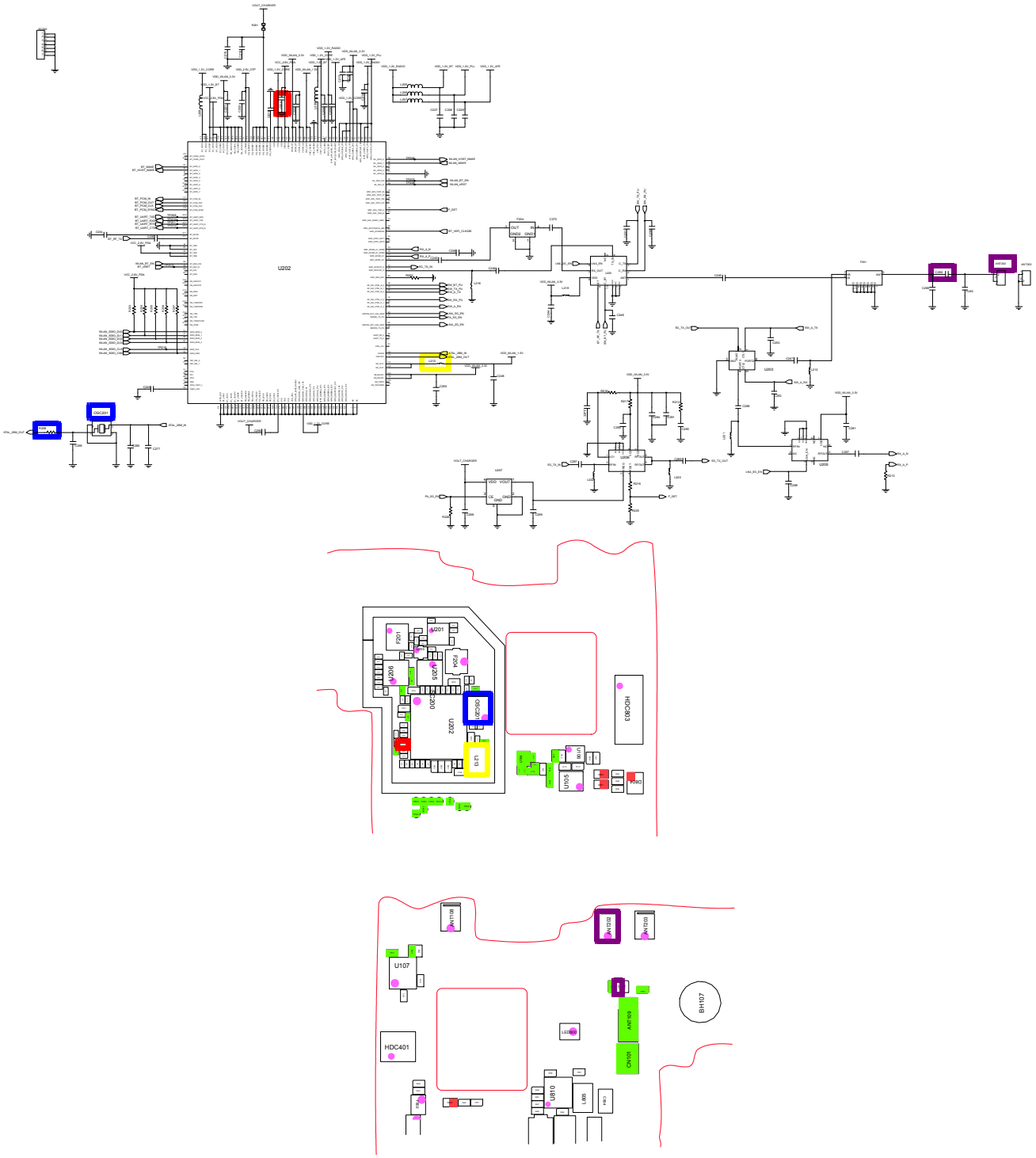


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8-3-7. BT/WIFI

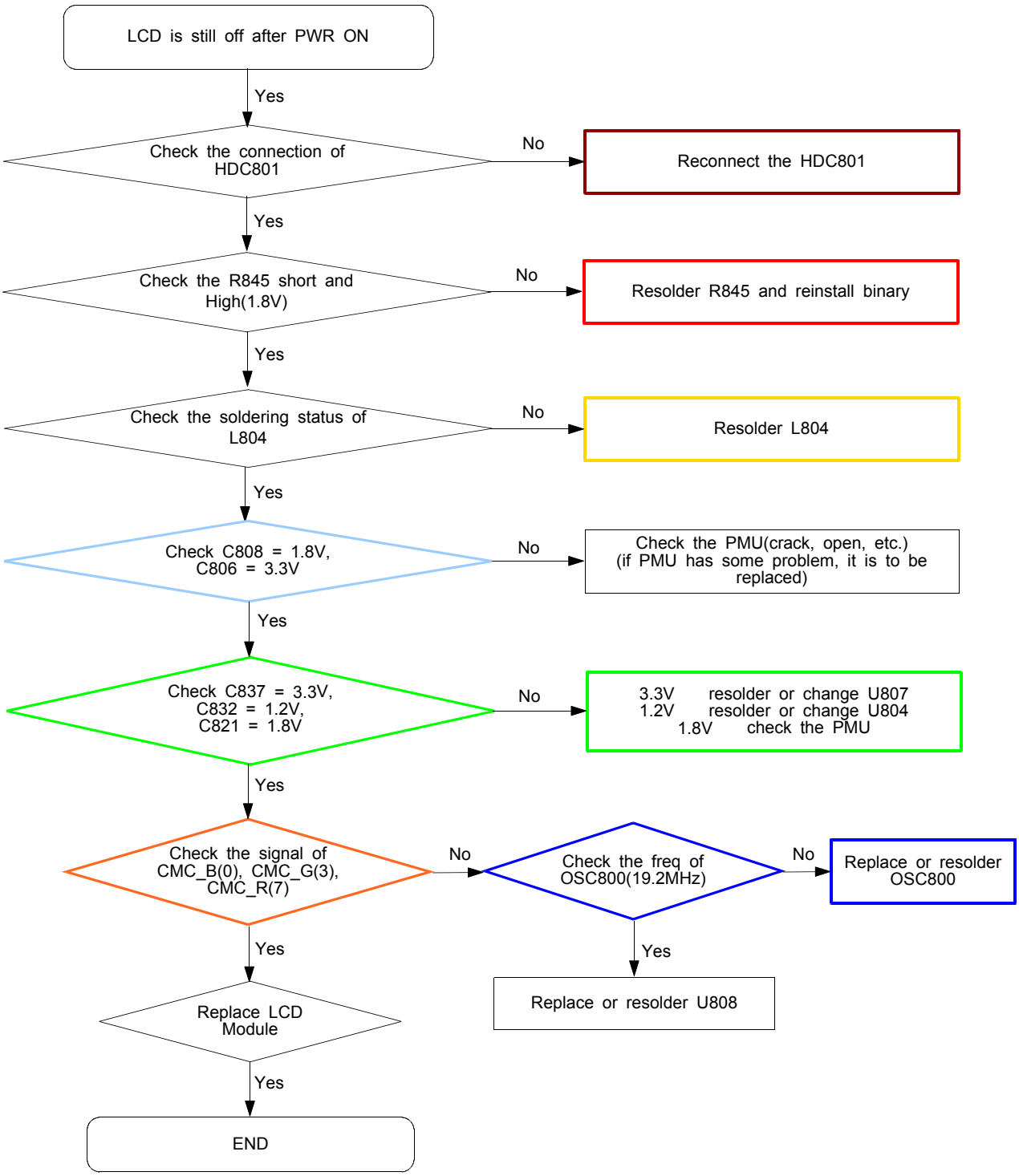


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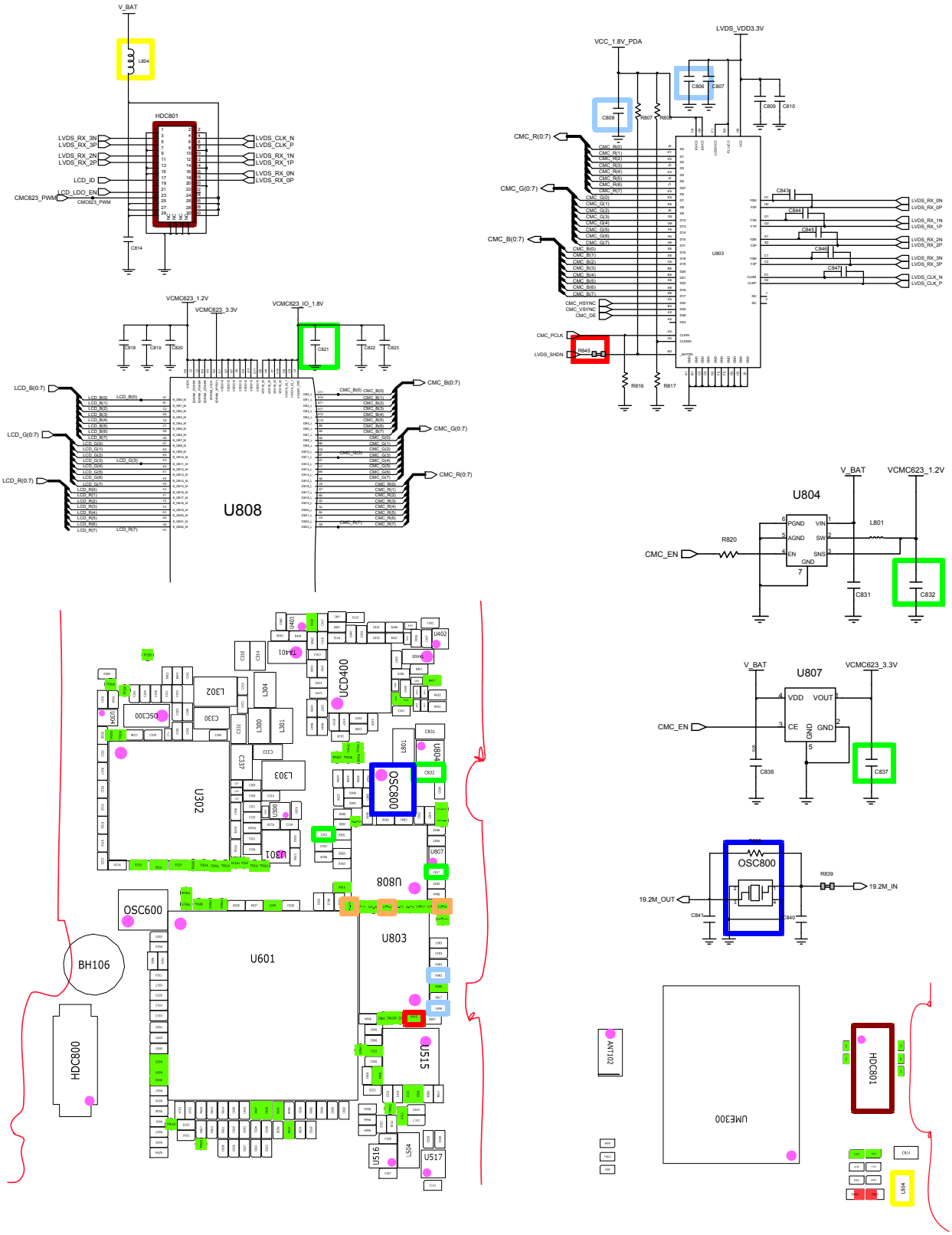


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8-3-8. LCD

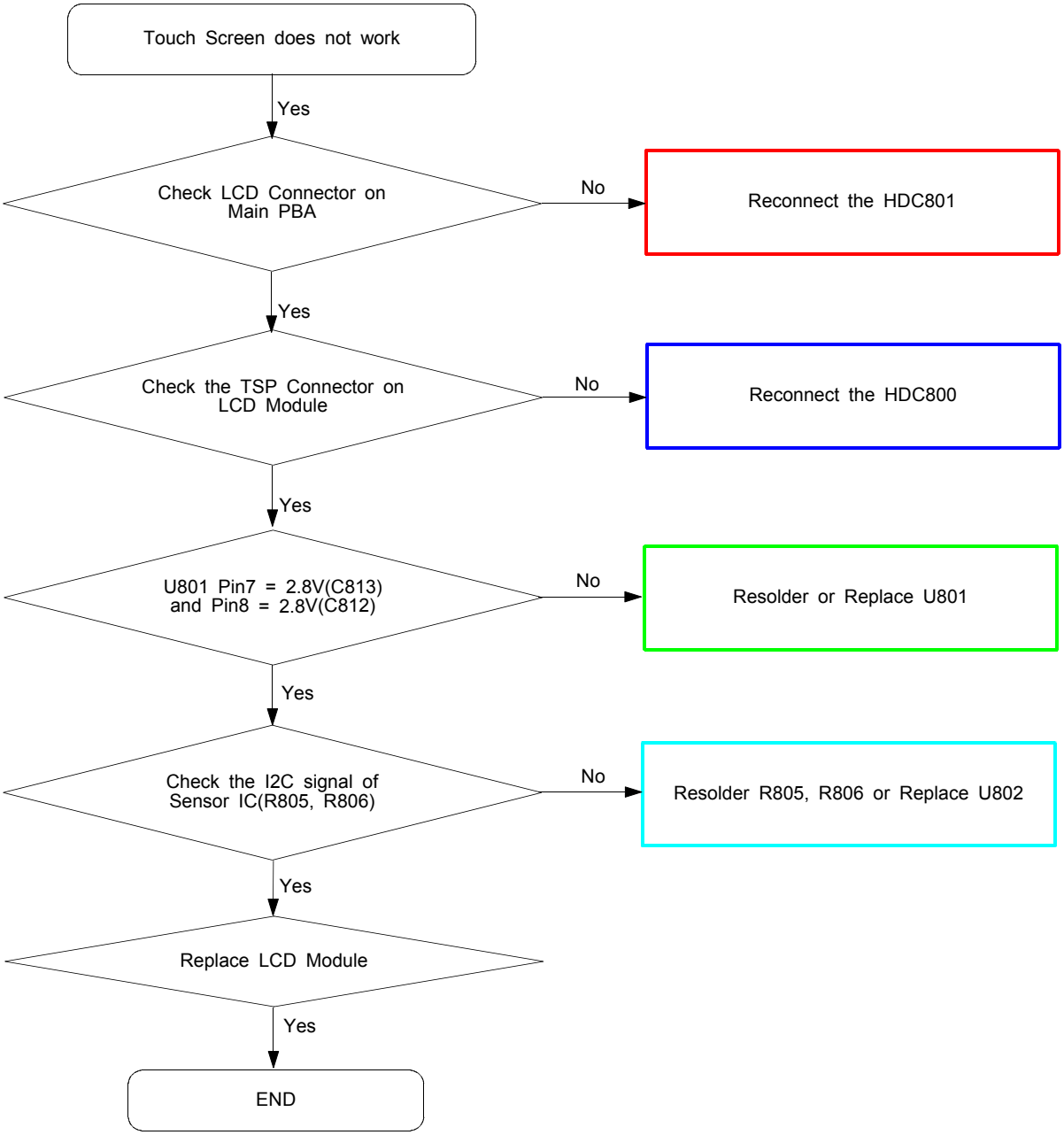


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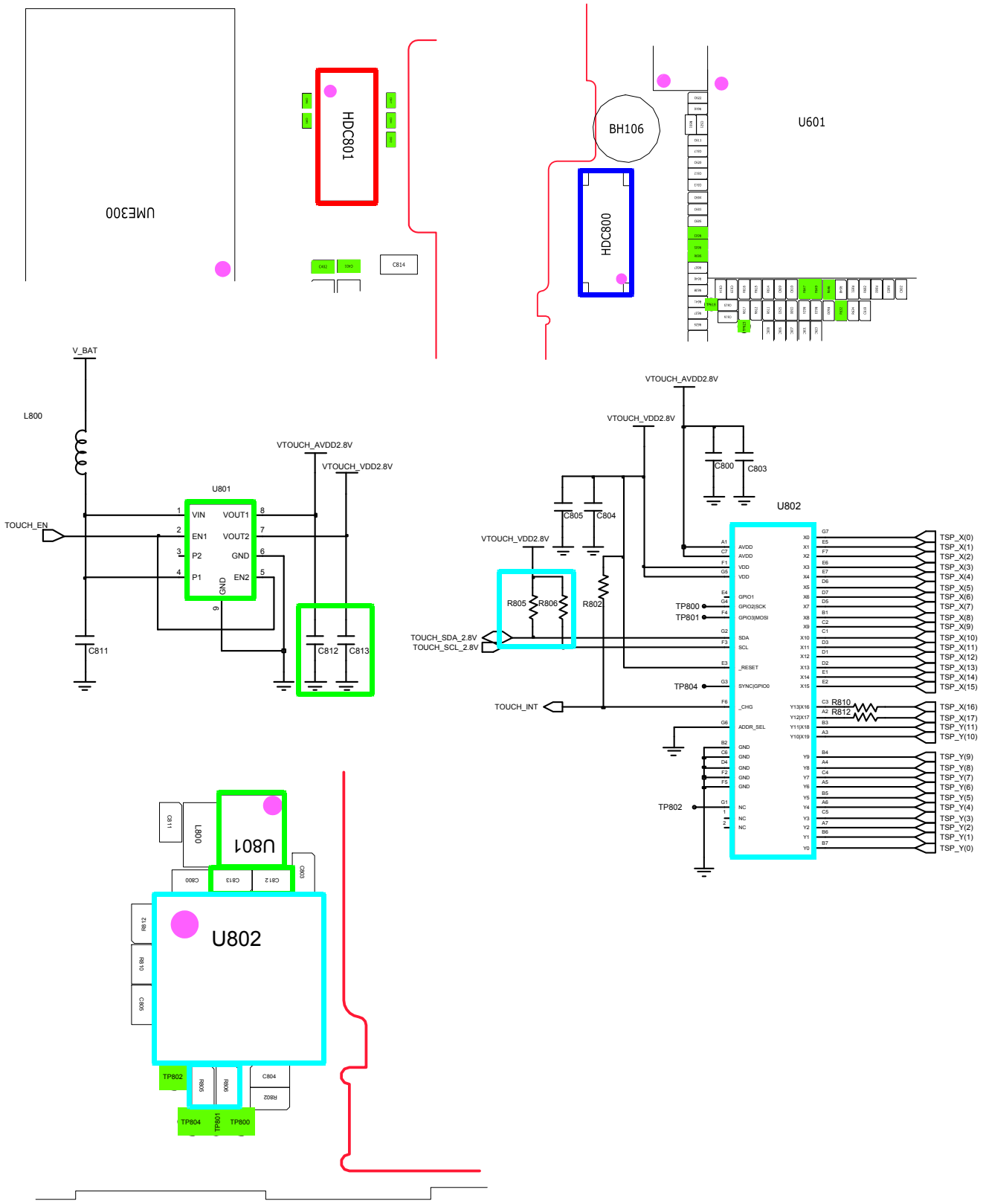


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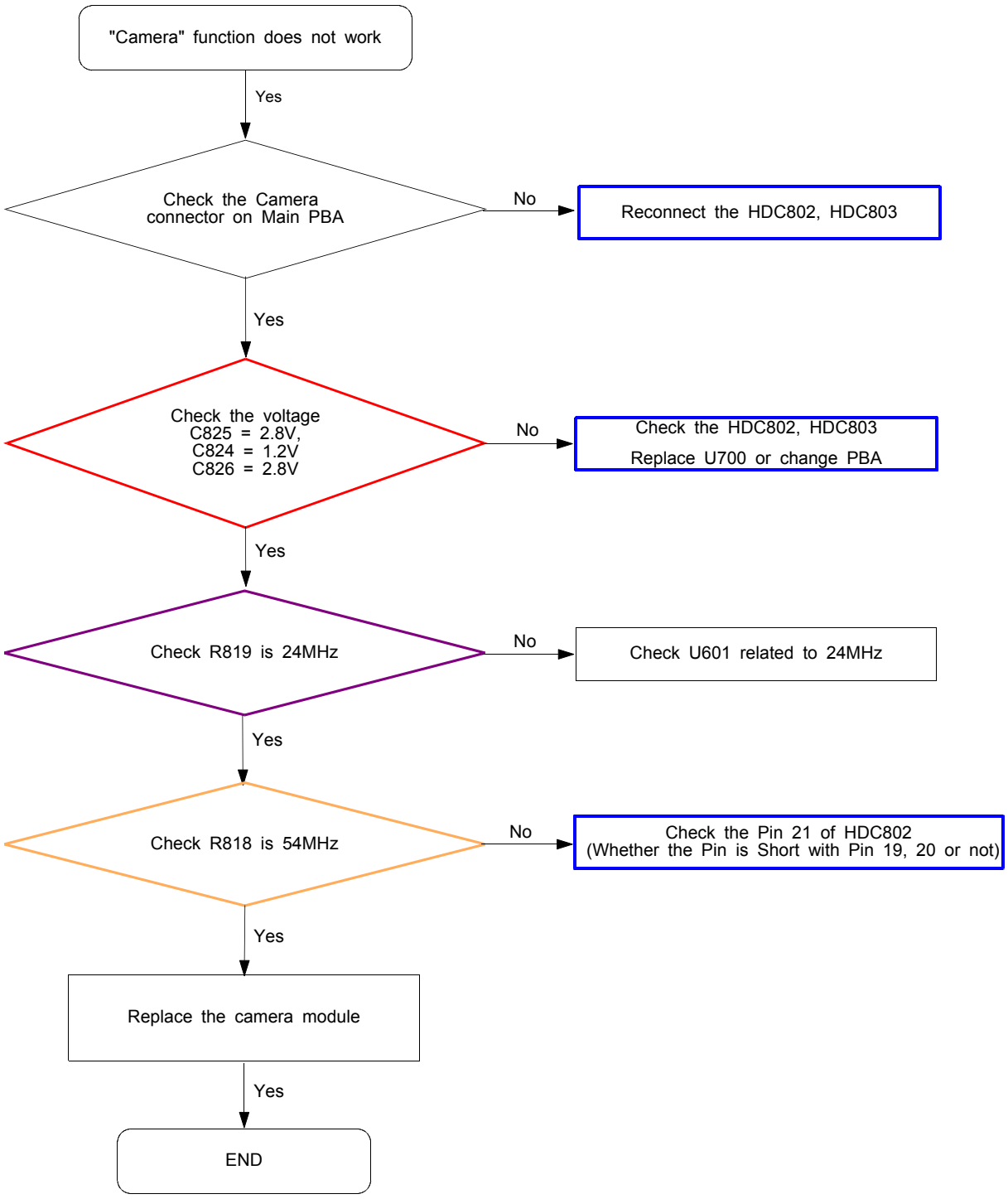


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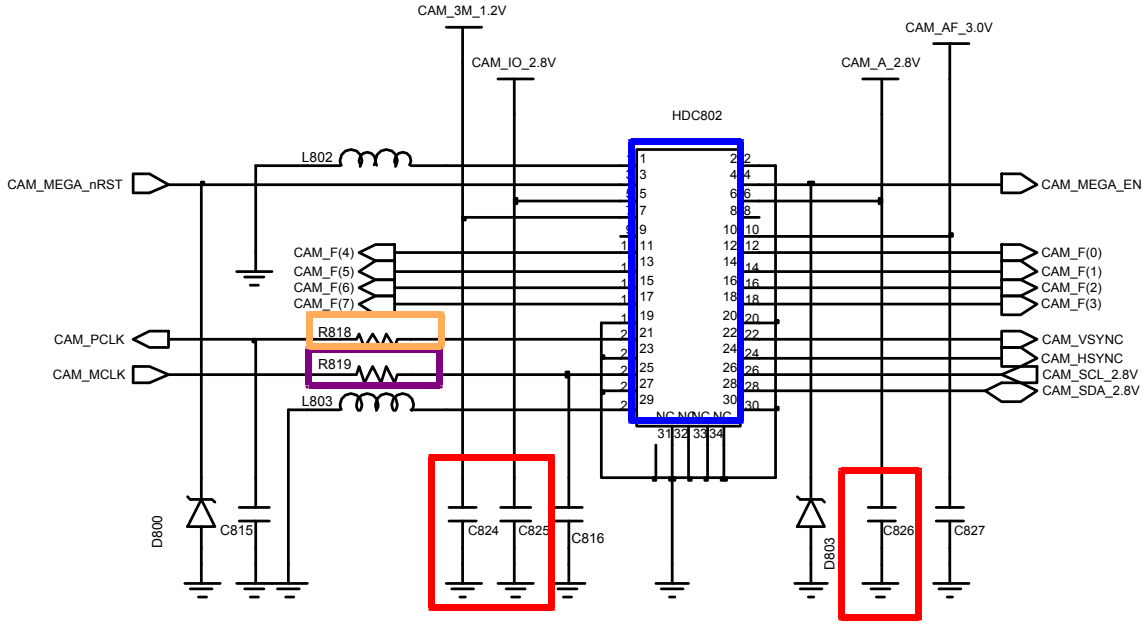


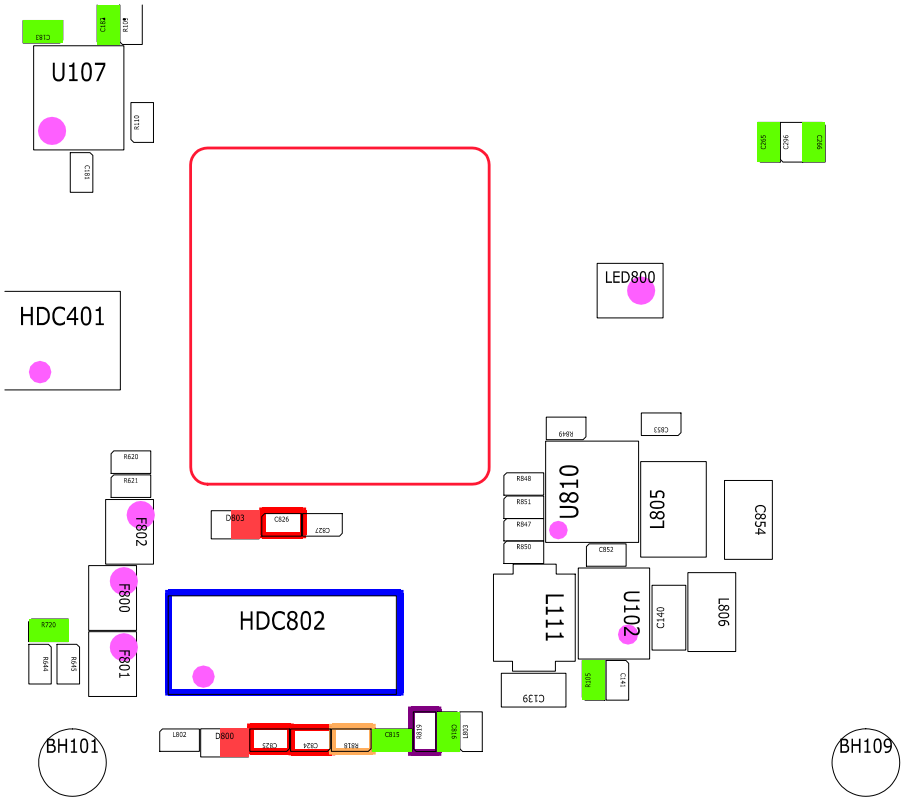
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8-3-10. 3M CAM



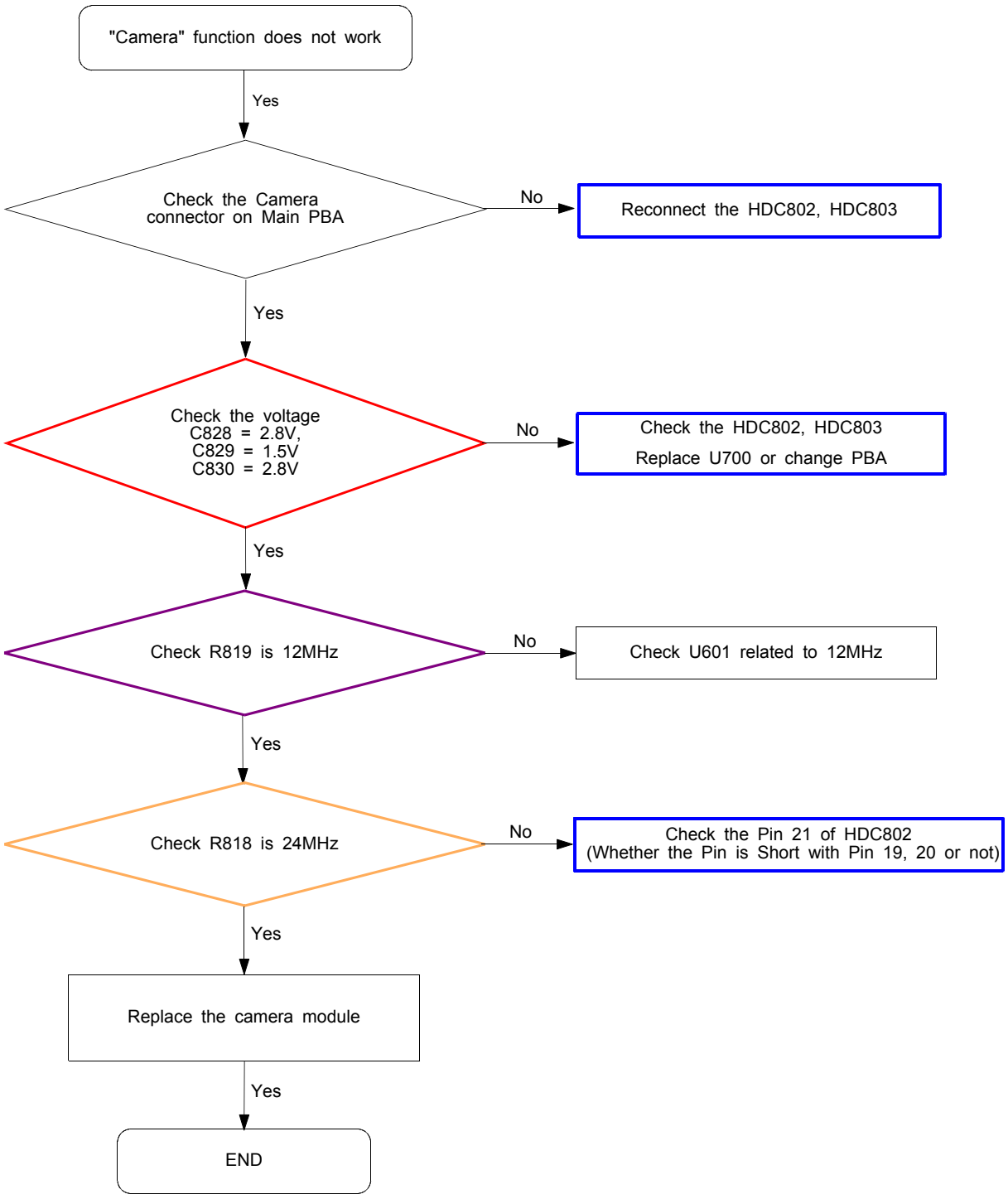
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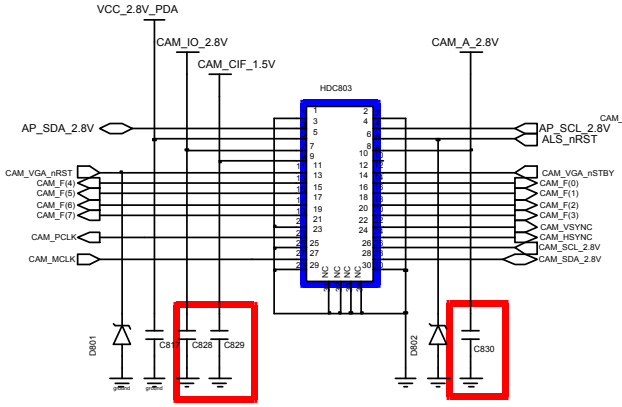


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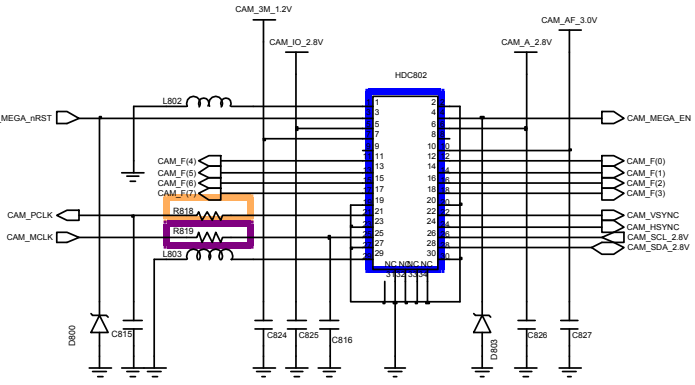
8-3-11. 1.3M CAM



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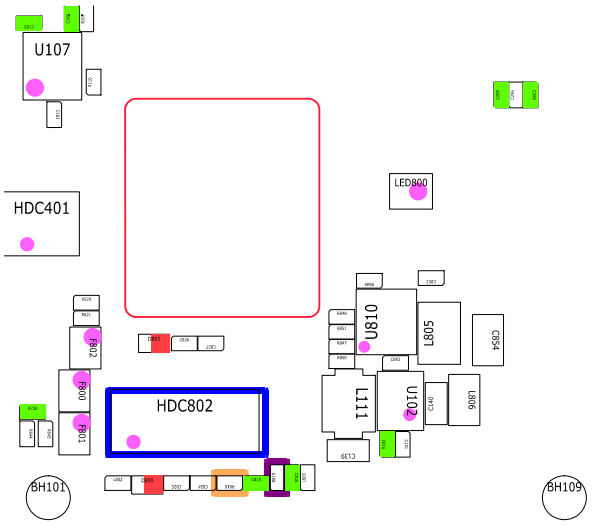


1.3M_CAM & LUME CONN.



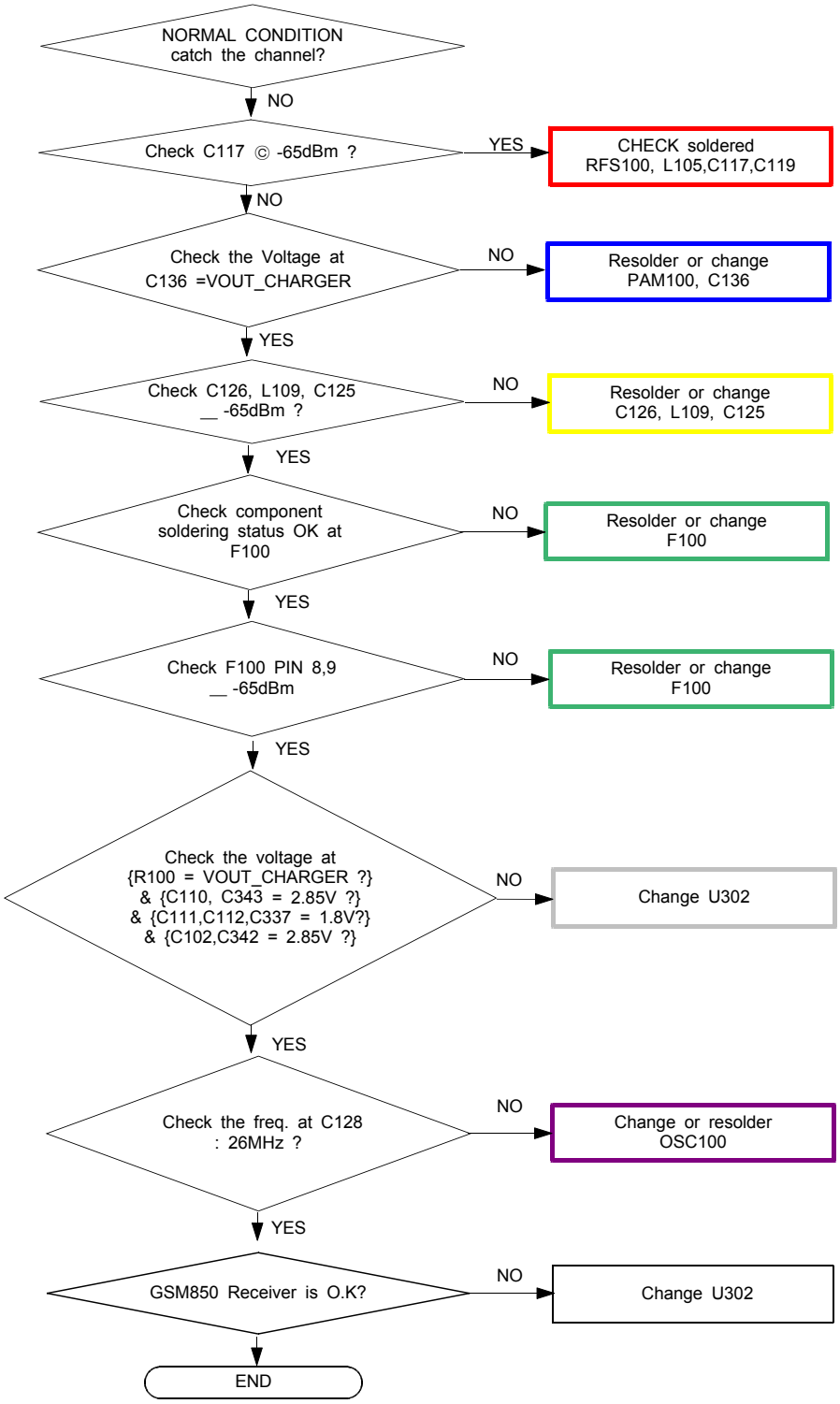
3M_CAMERA CONN.



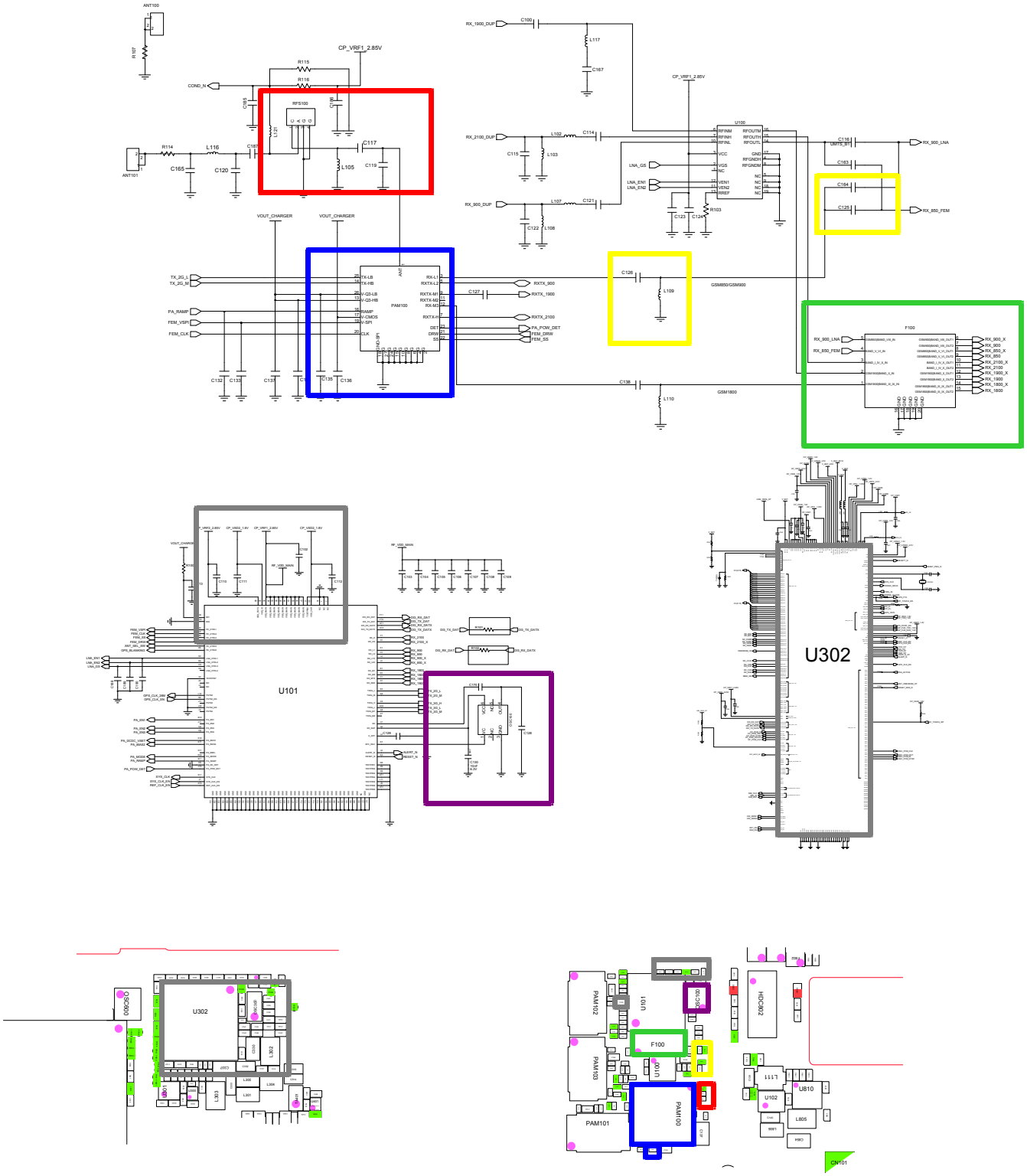


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8-3-12. GSM850 RX

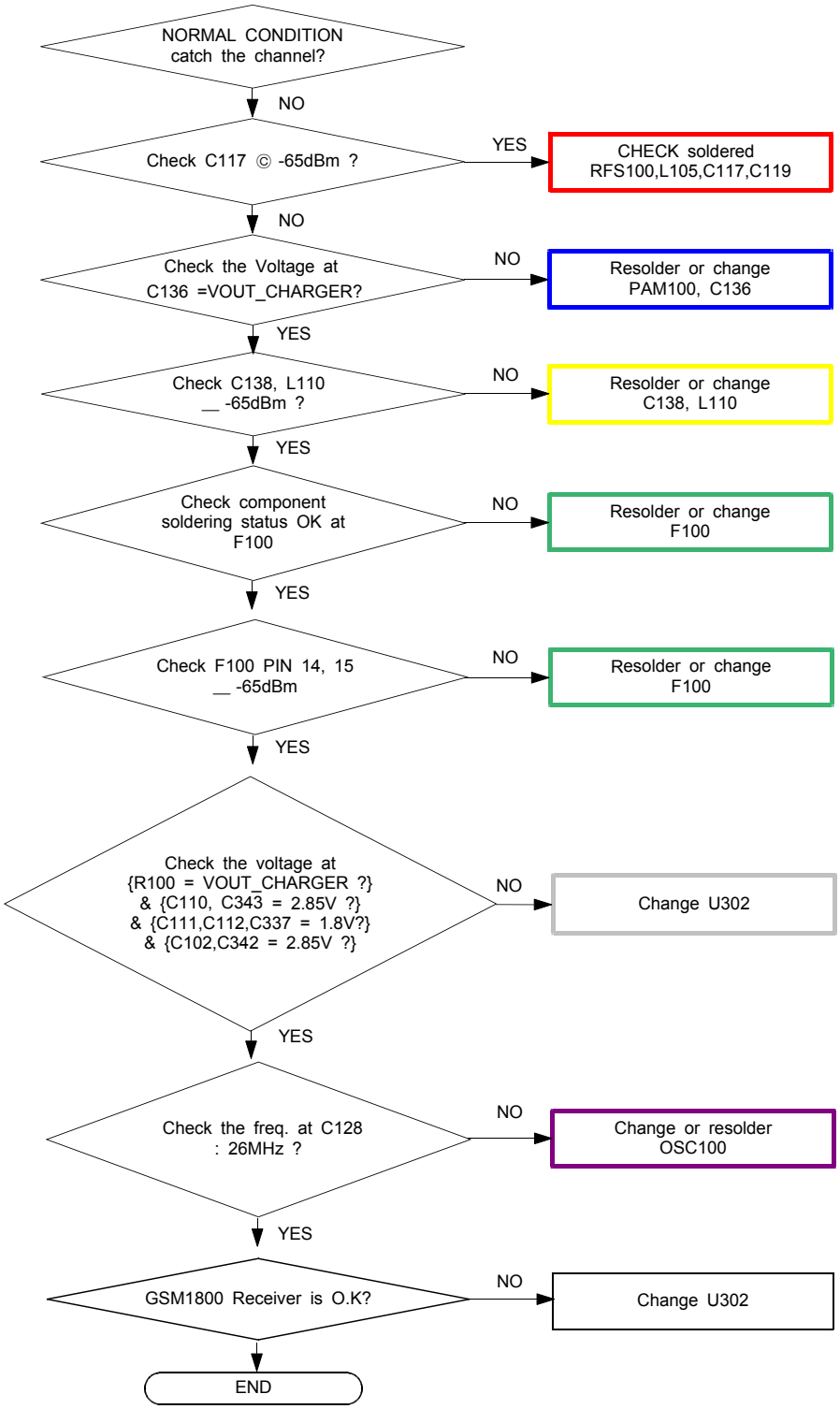


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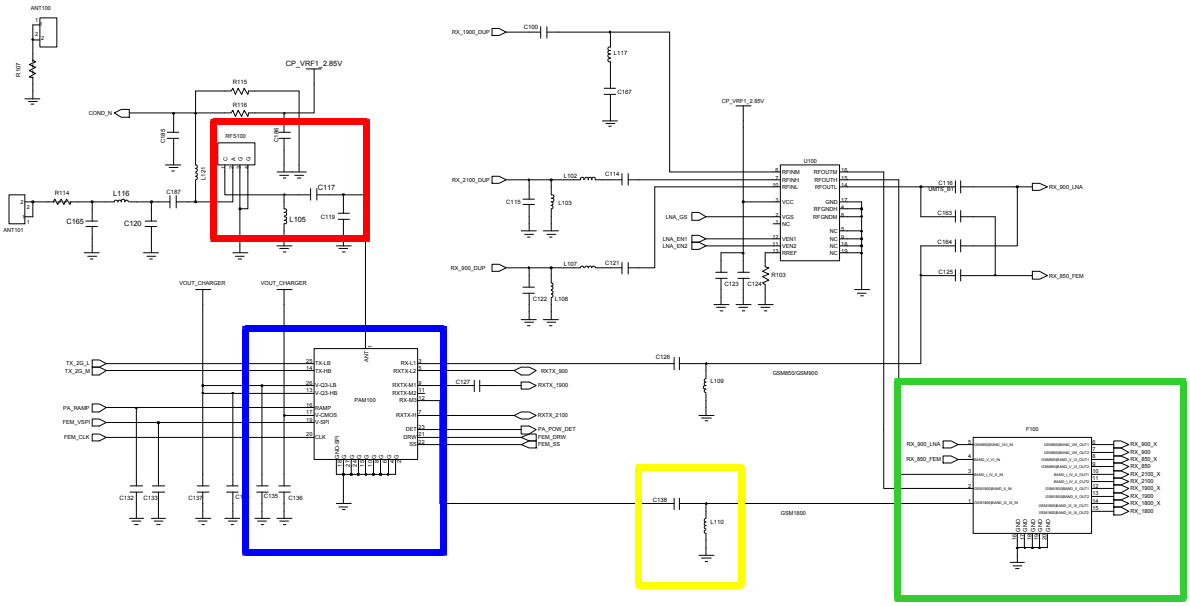


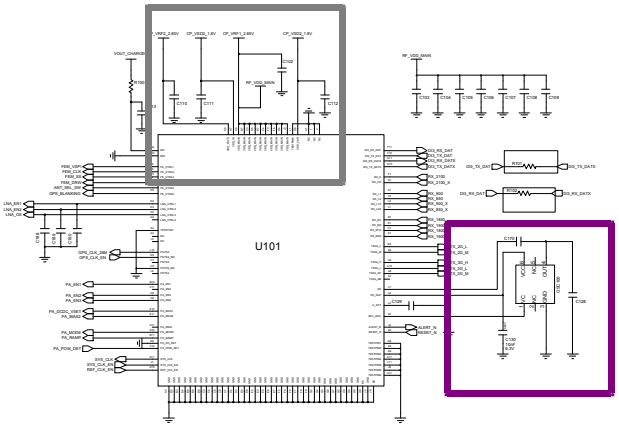
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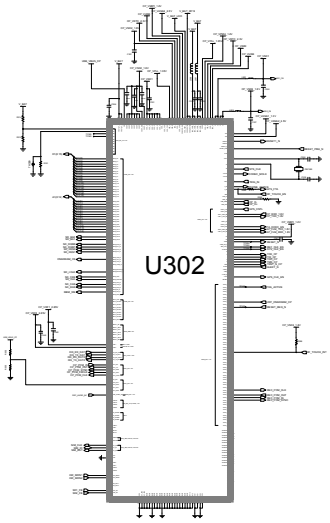
8-3-13. GSM1800 RX



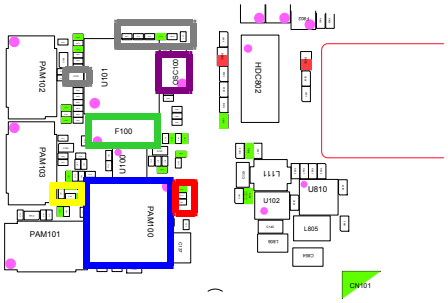
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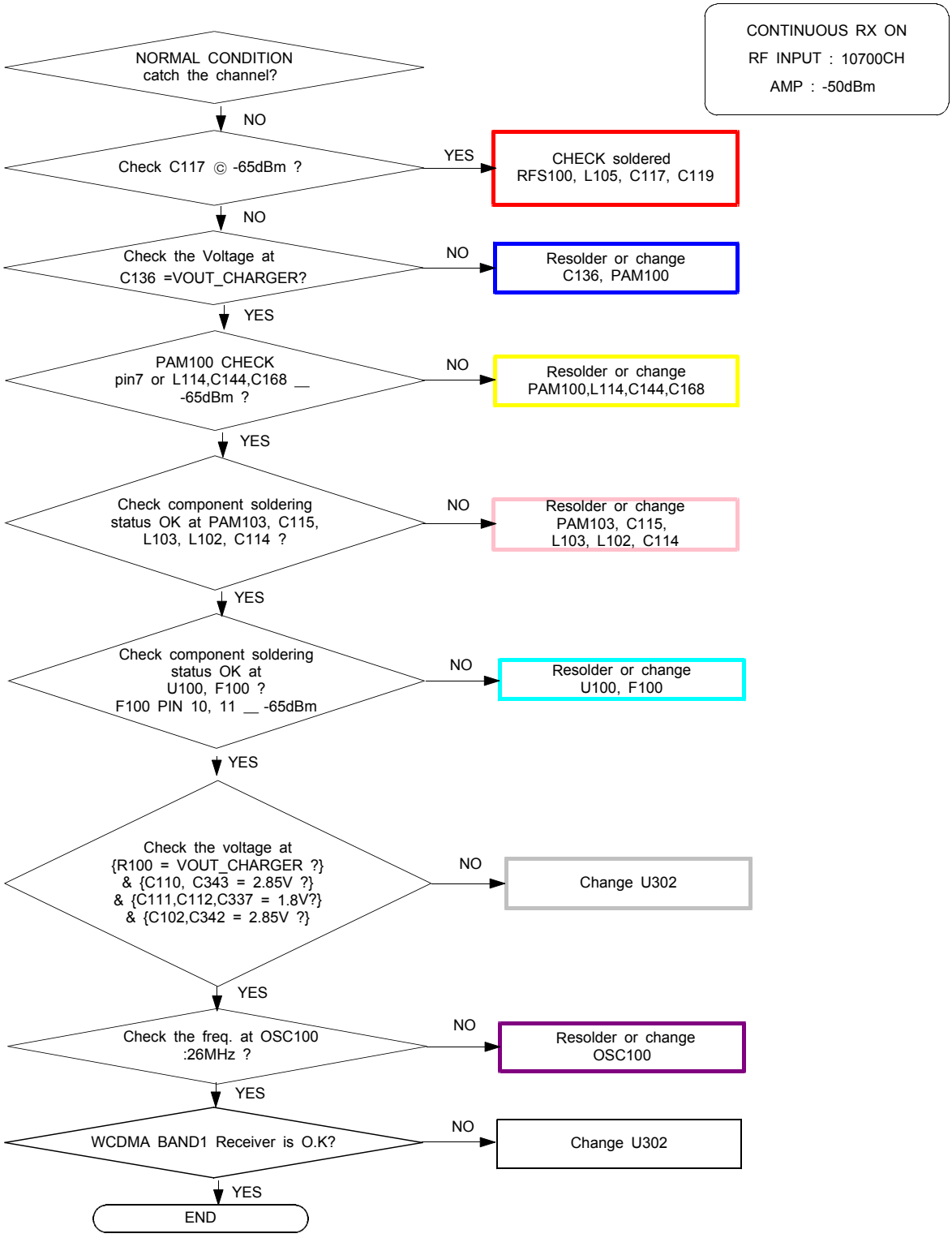




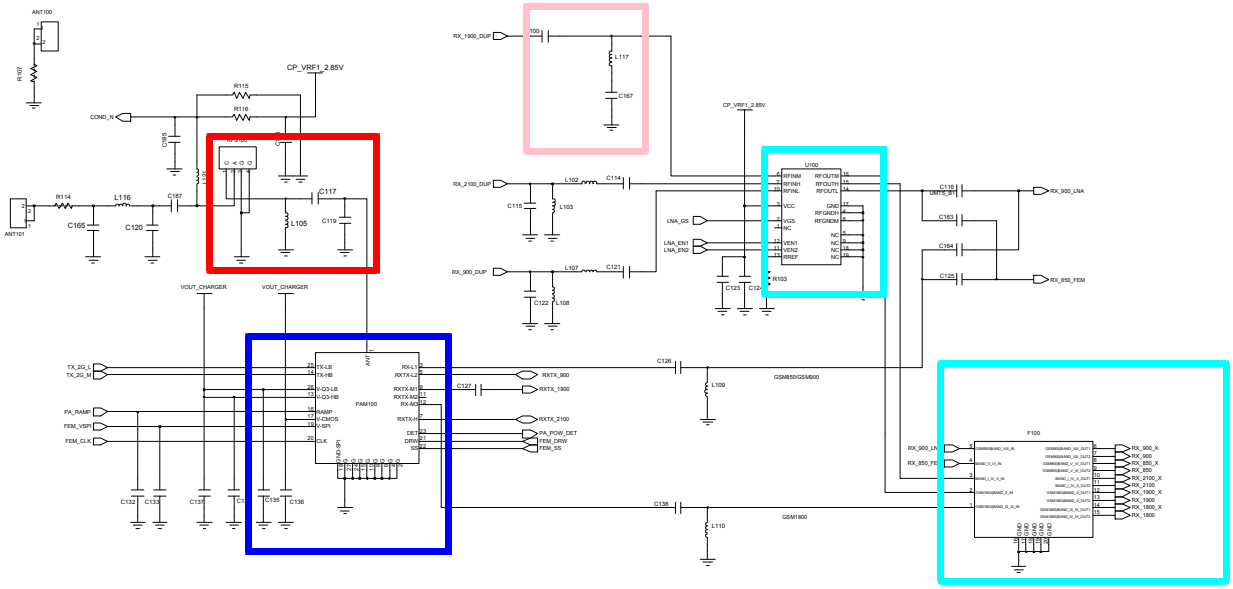


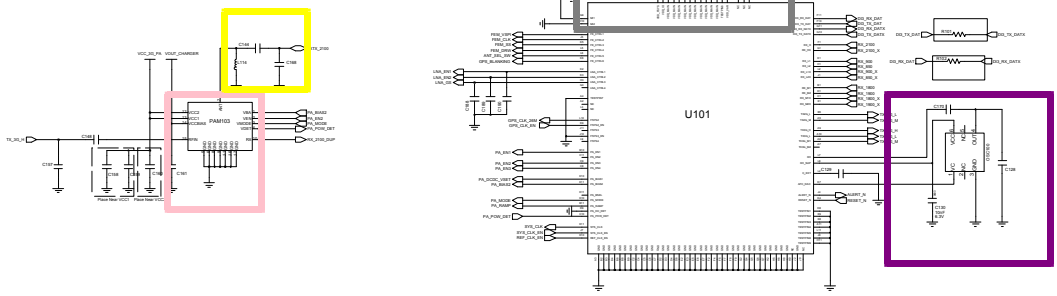
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8-3-14. WCDMA Band1 RX

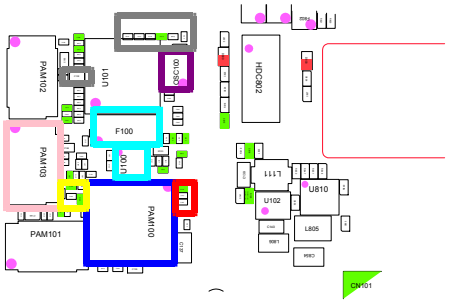


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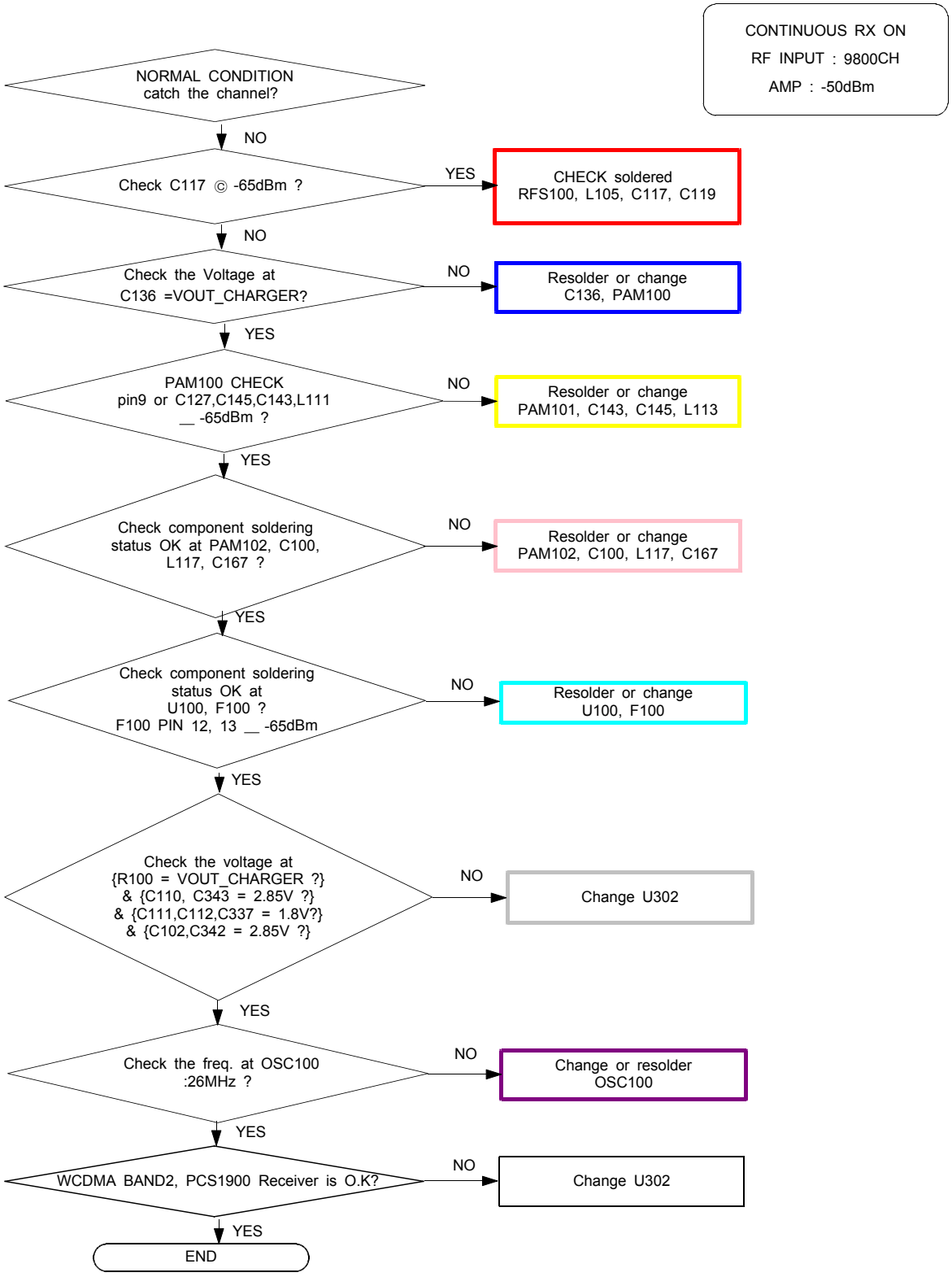




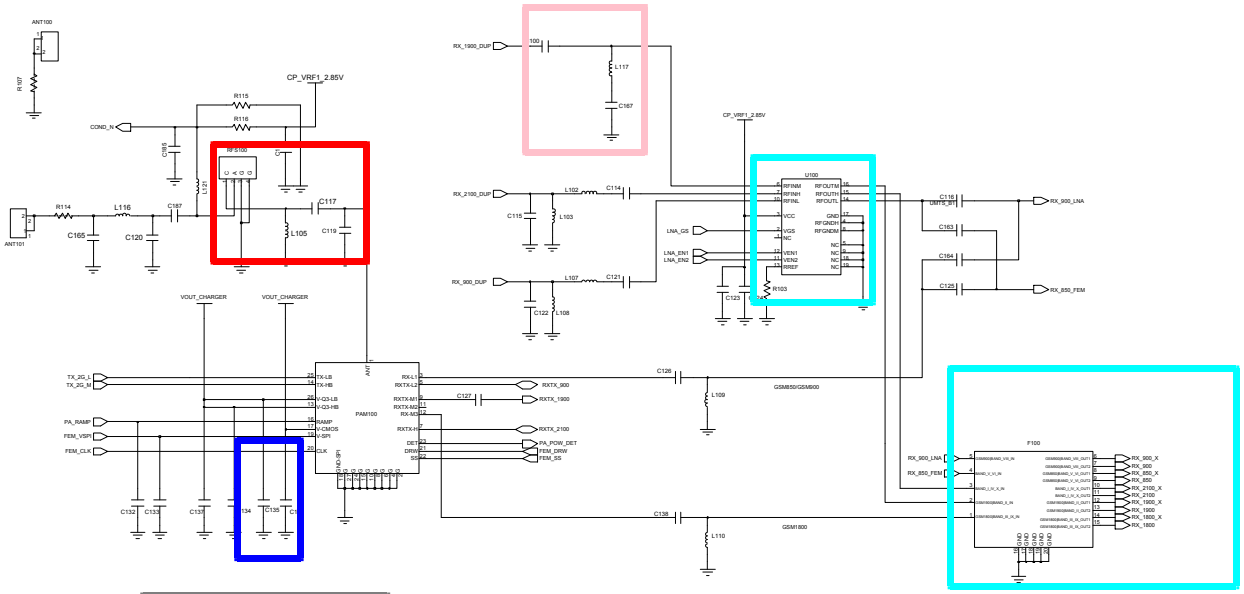


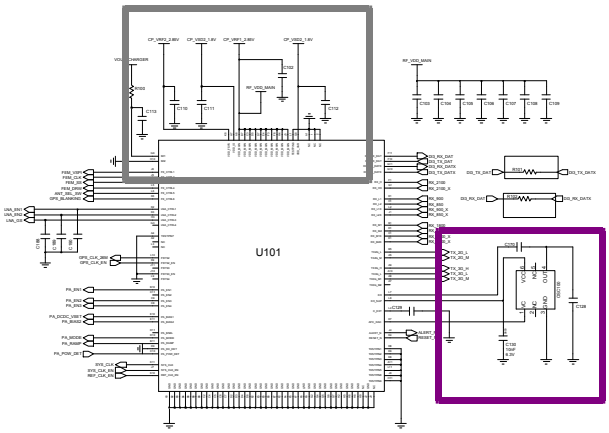
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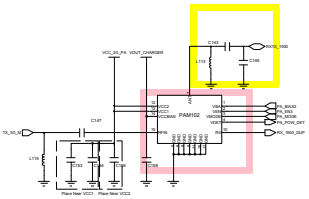
8-3-15. WCDMA Band2 / GSM1900 RX



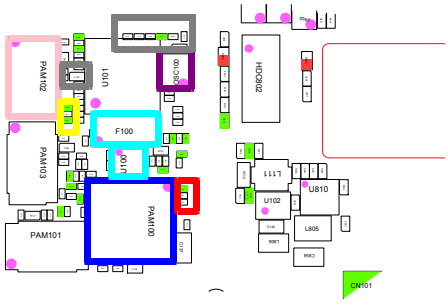
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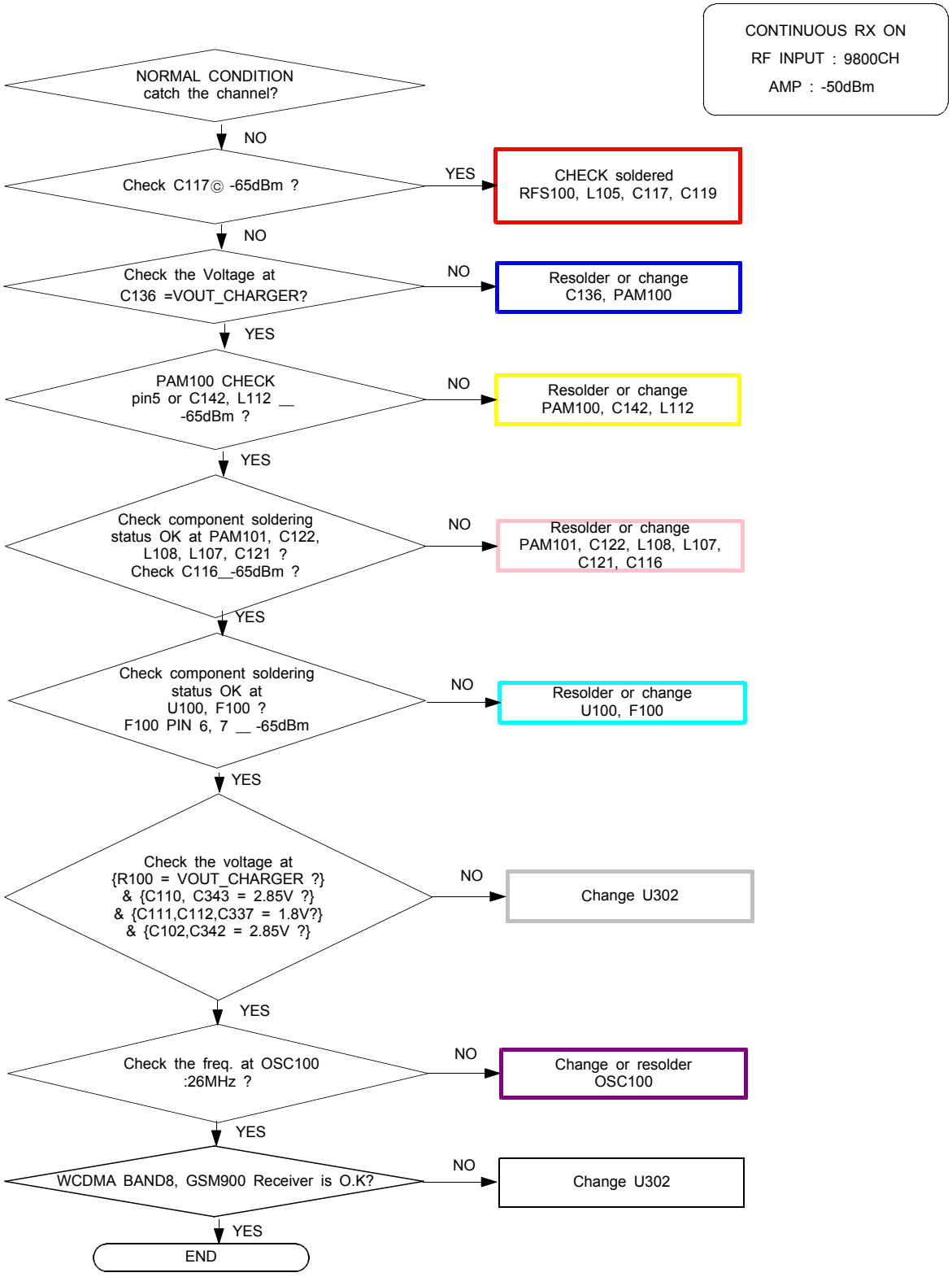




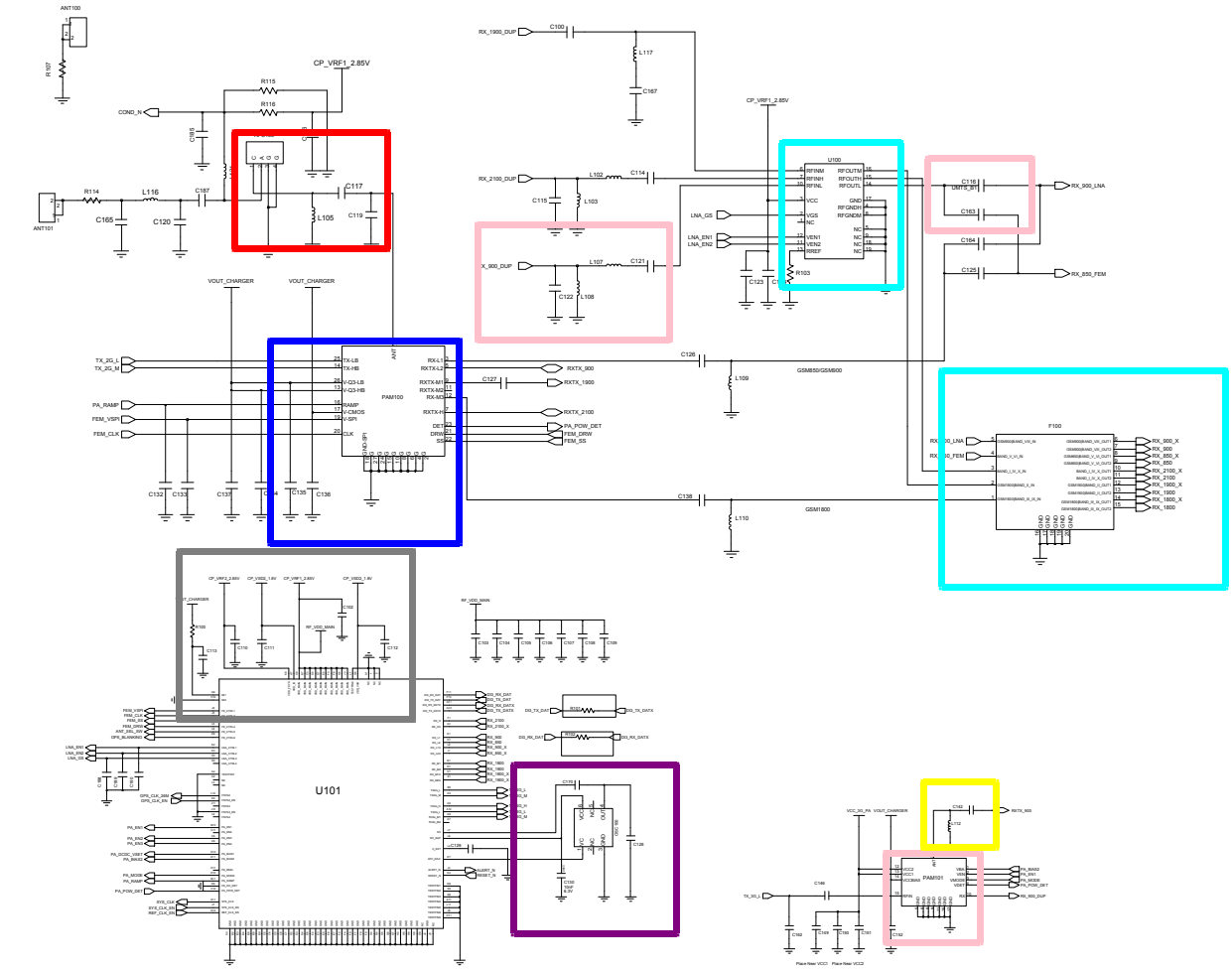


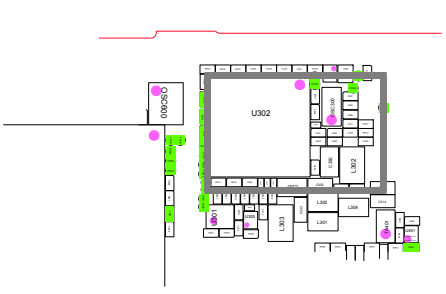
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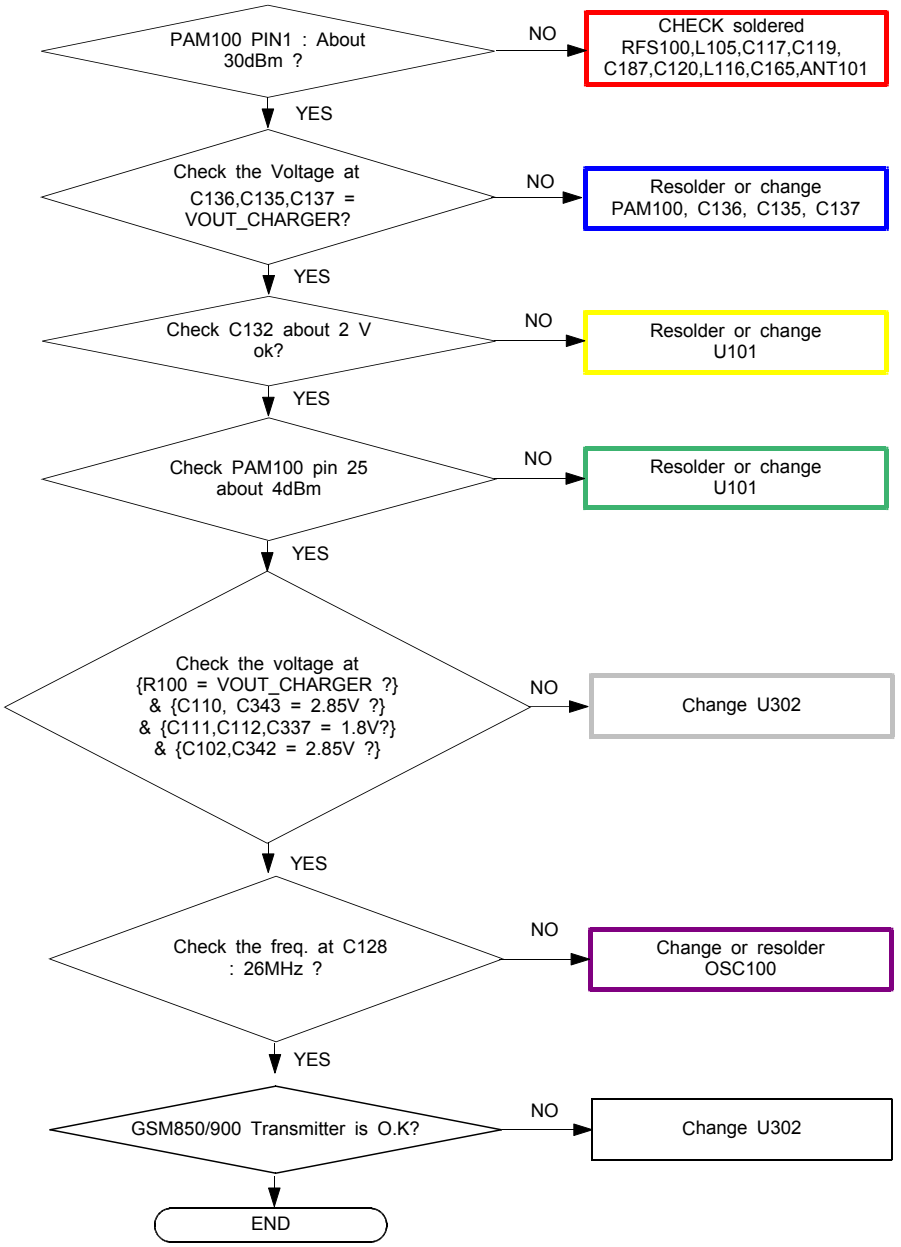
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8-3-17. GSM850/GSM900 TX

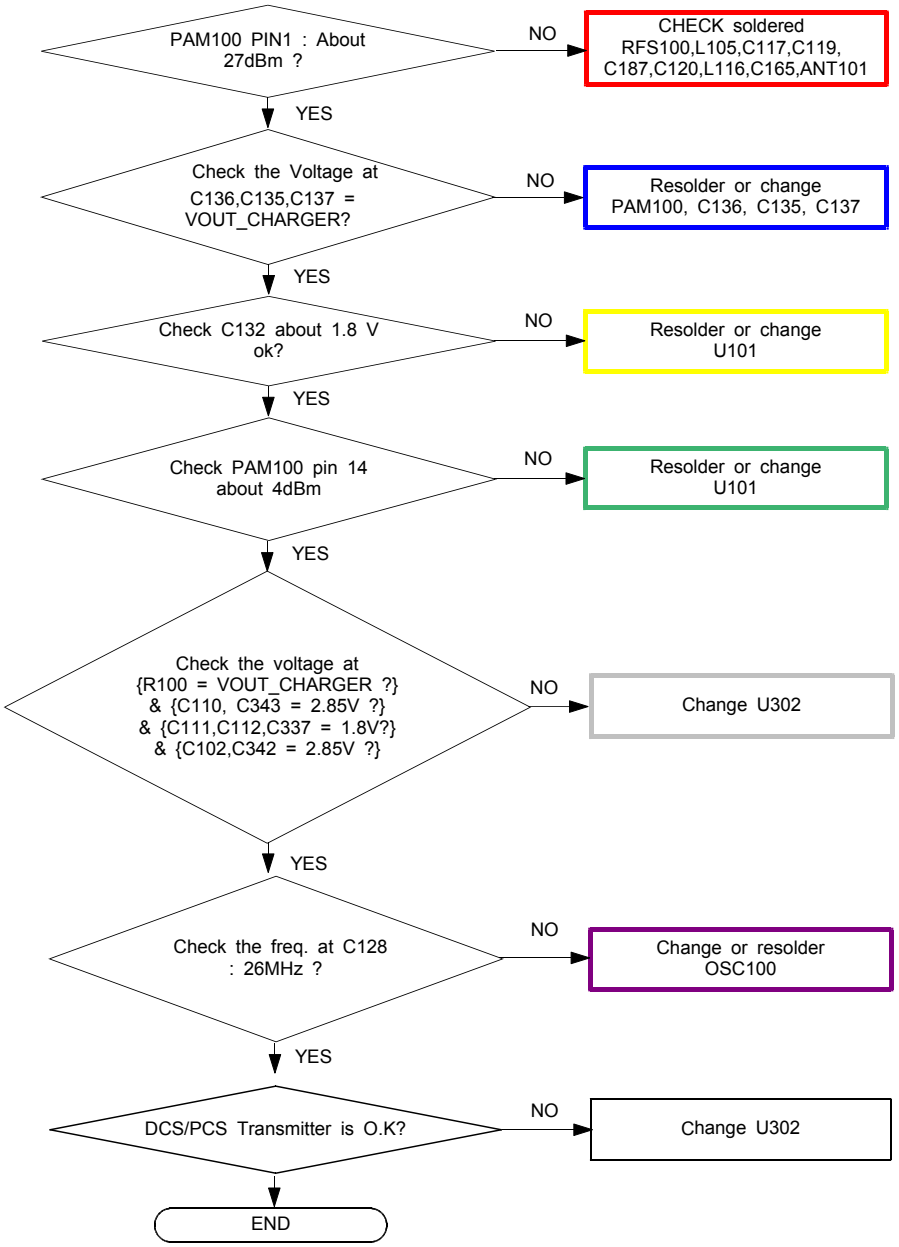
CONTINOUS TX ON CONDITION
TX POWER DAC:14500 CODE APPLIED
GSM850 CH : 190
GSM900 CH : 62
RBW : 100KHz
VBW : 100KHz
SPAN : 10MHz
REF LEV. : 10dBm
ATT. : 20dB



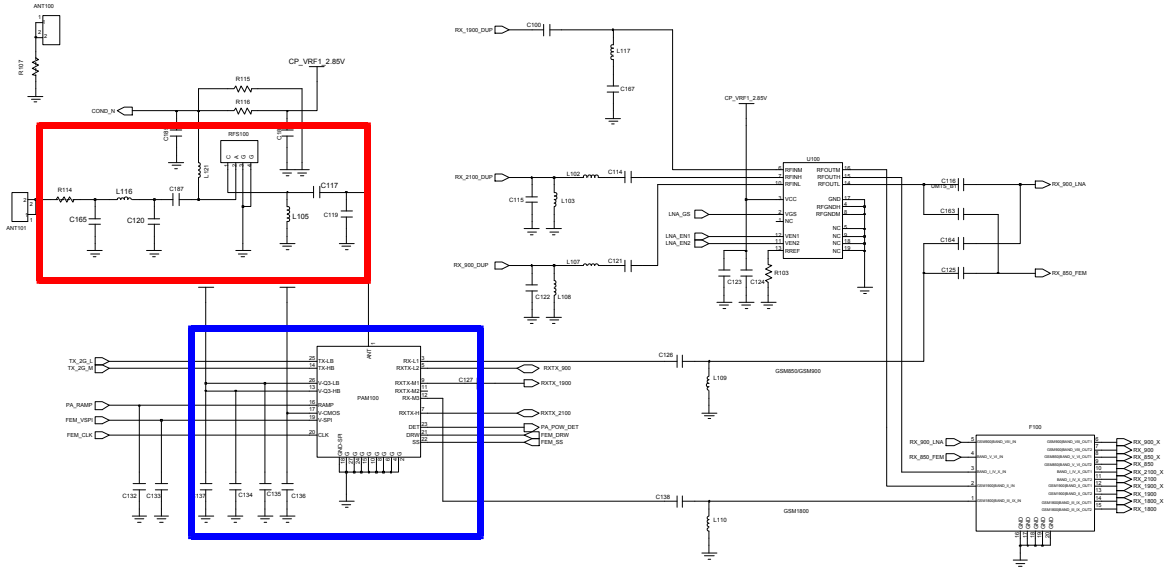
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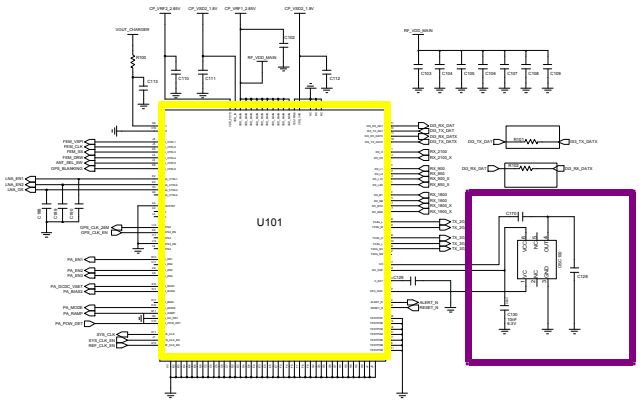
8-3-18. DCS/PCS TX

CONTINOUS TX ON CONDITION
TX POWER DAC:14500 CODE APPLIED
GSM850 CH : 190
GSM900 CH : 62
RBW : 100KHz
VBW : 100KHz
SPAN : 10MHz
REF LEV. : 10dBm
ATT. : 20dB

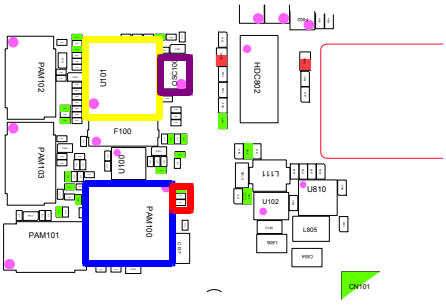


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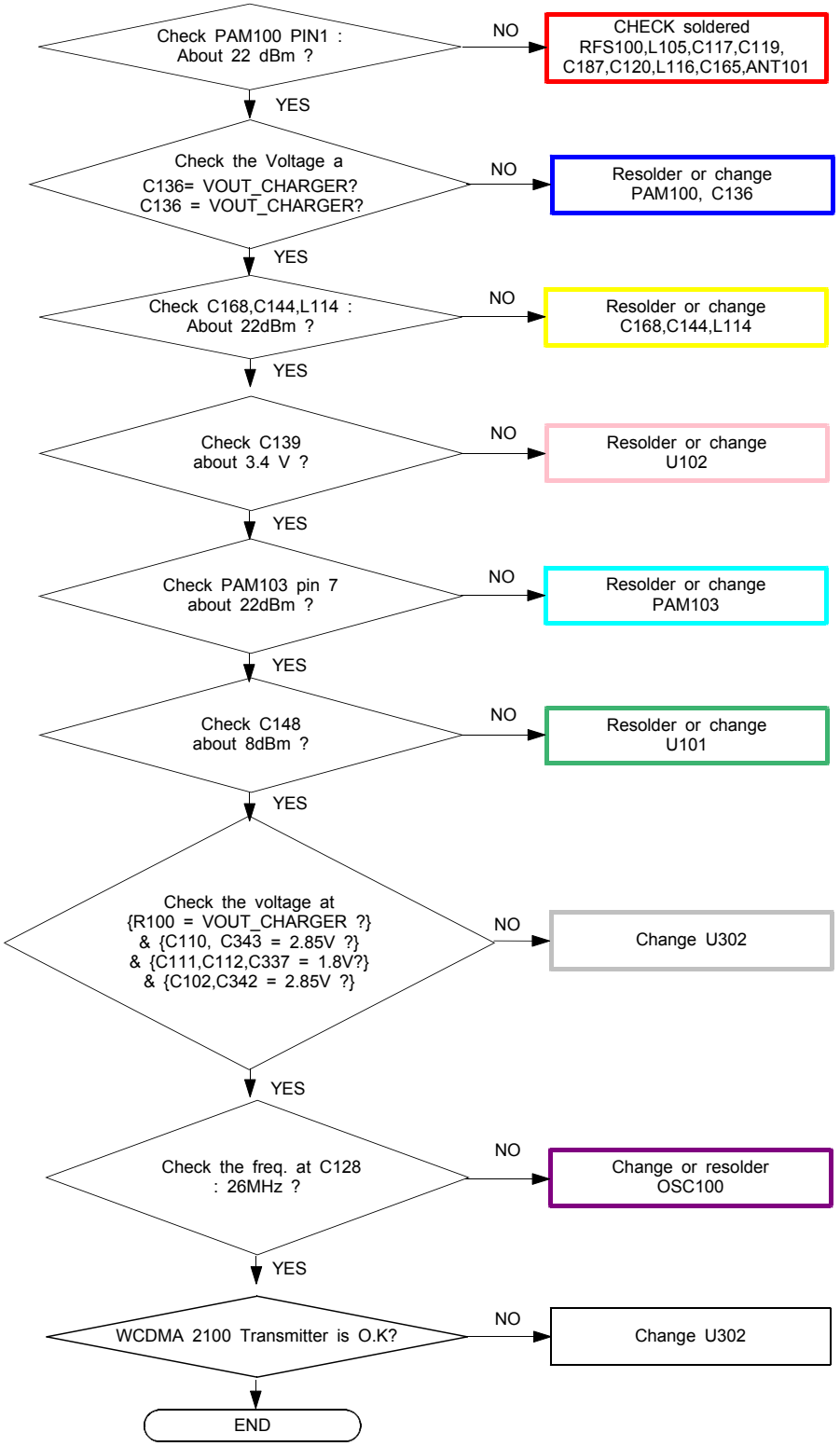




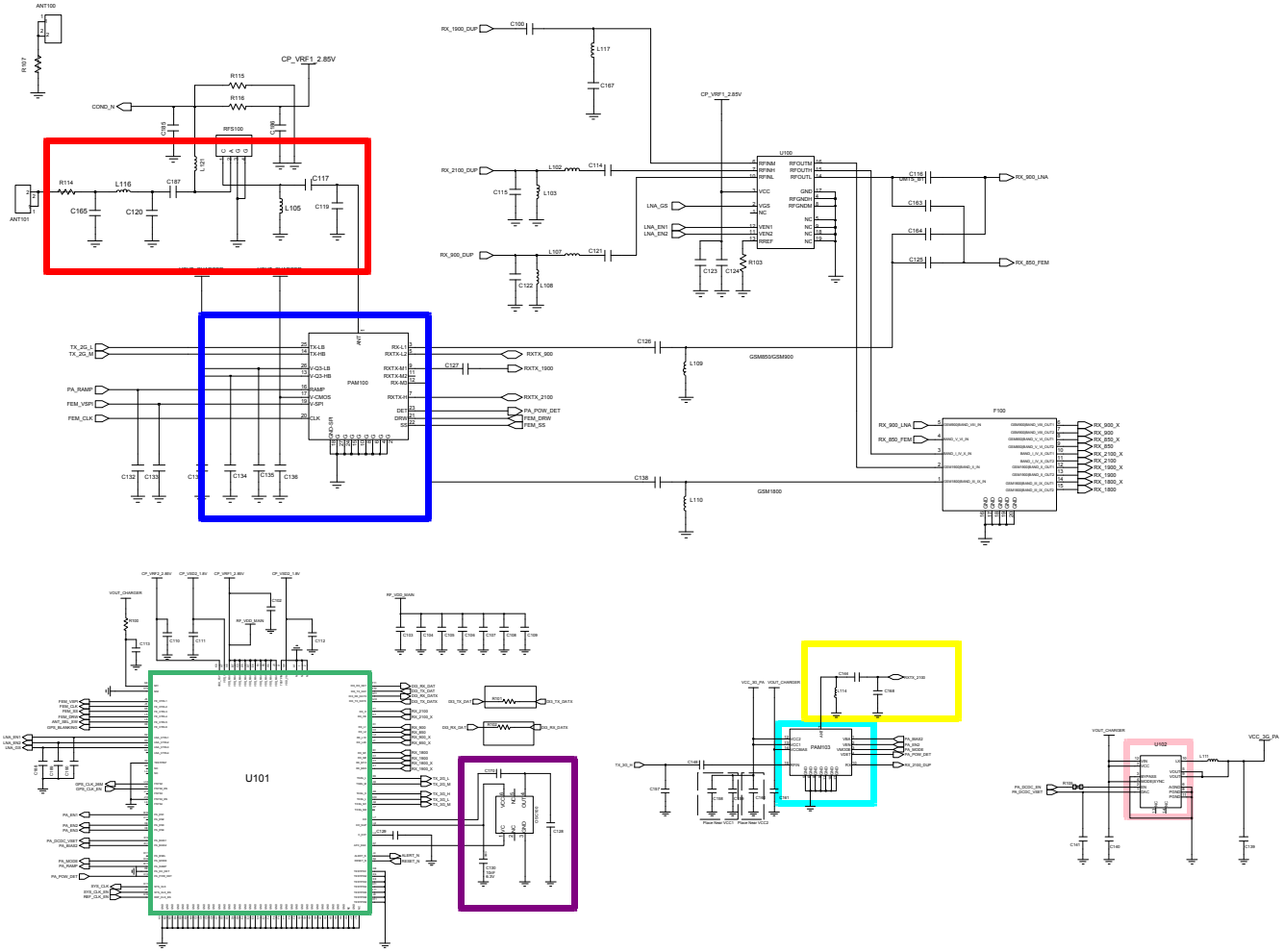
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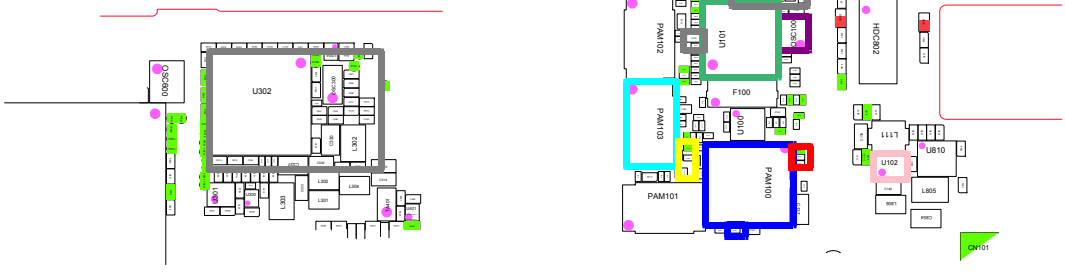
8-3-19. WCDMA BAND1 TX

CONTINOUS TX ON CONDITION
TX POWER DAC:14500 CODE APPLIED
GSM850 CH : 190
GSM900 CH : 62
RBW : 100KHz
VBW : 100KHz
SPAN : 10MHz
REF LEV. : 10dBm
ATT. : 20dB



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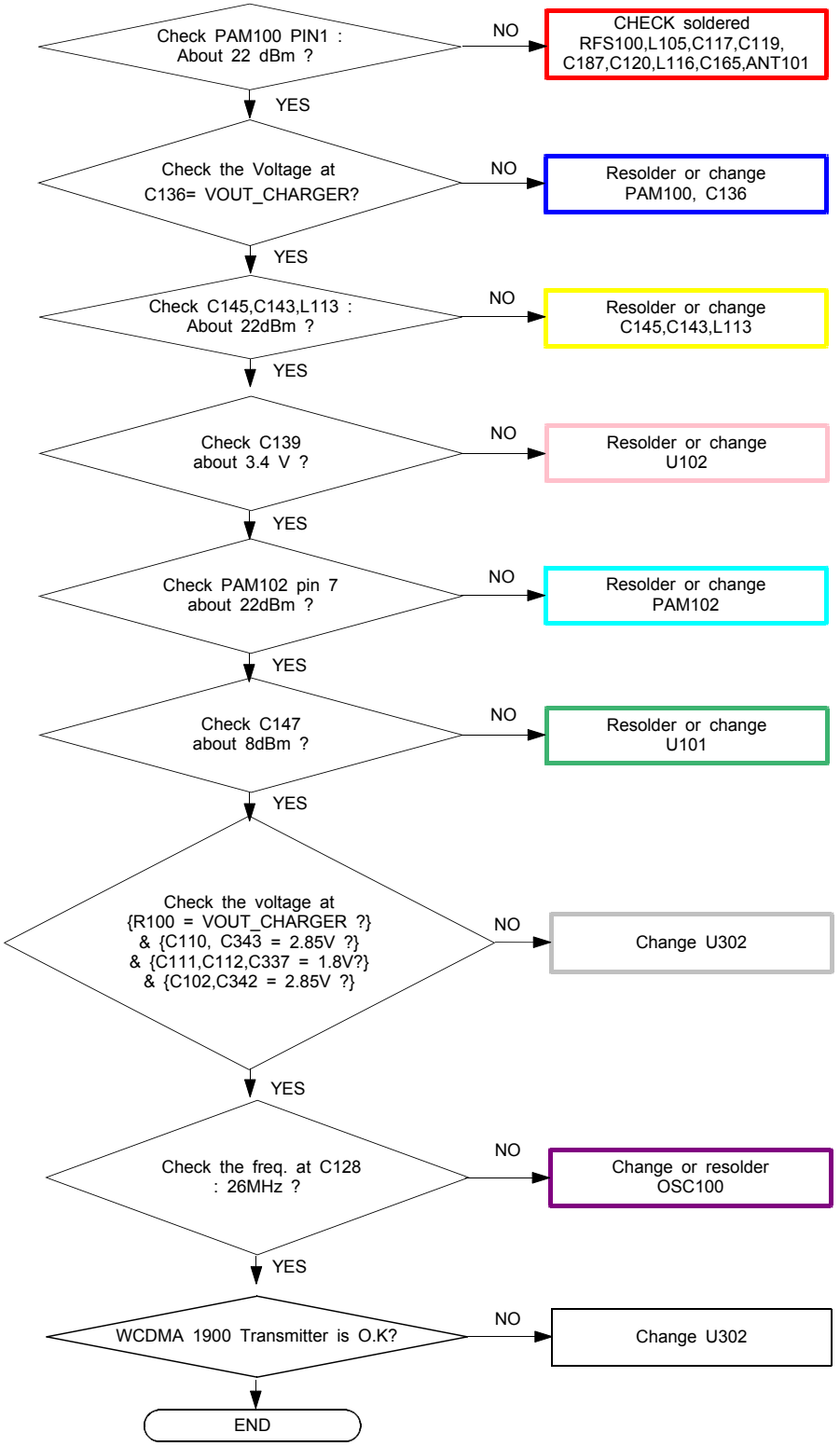




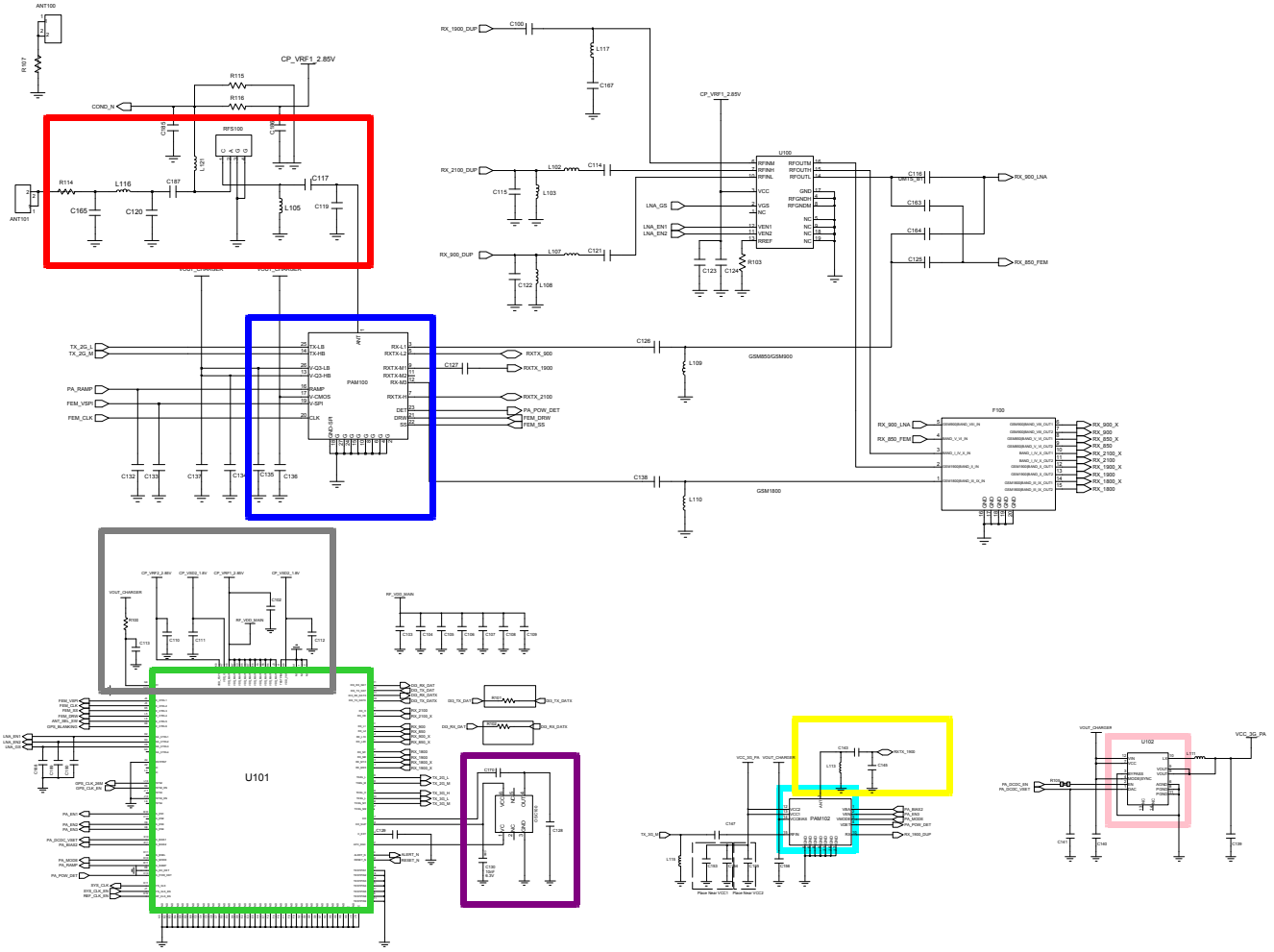
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8-3-20. WCDMA BAND2 TX

CONTINOUS TX ON CONDITION
TX POWER DAC:14500 CODE APPLIED
GSM850 CH : 190
GSM900 CH : 62
RBW : 100KHz
VBW : 100KHz
SPAN : 10MHz
REF LEV. : 10dBm
ATT. : 20dB



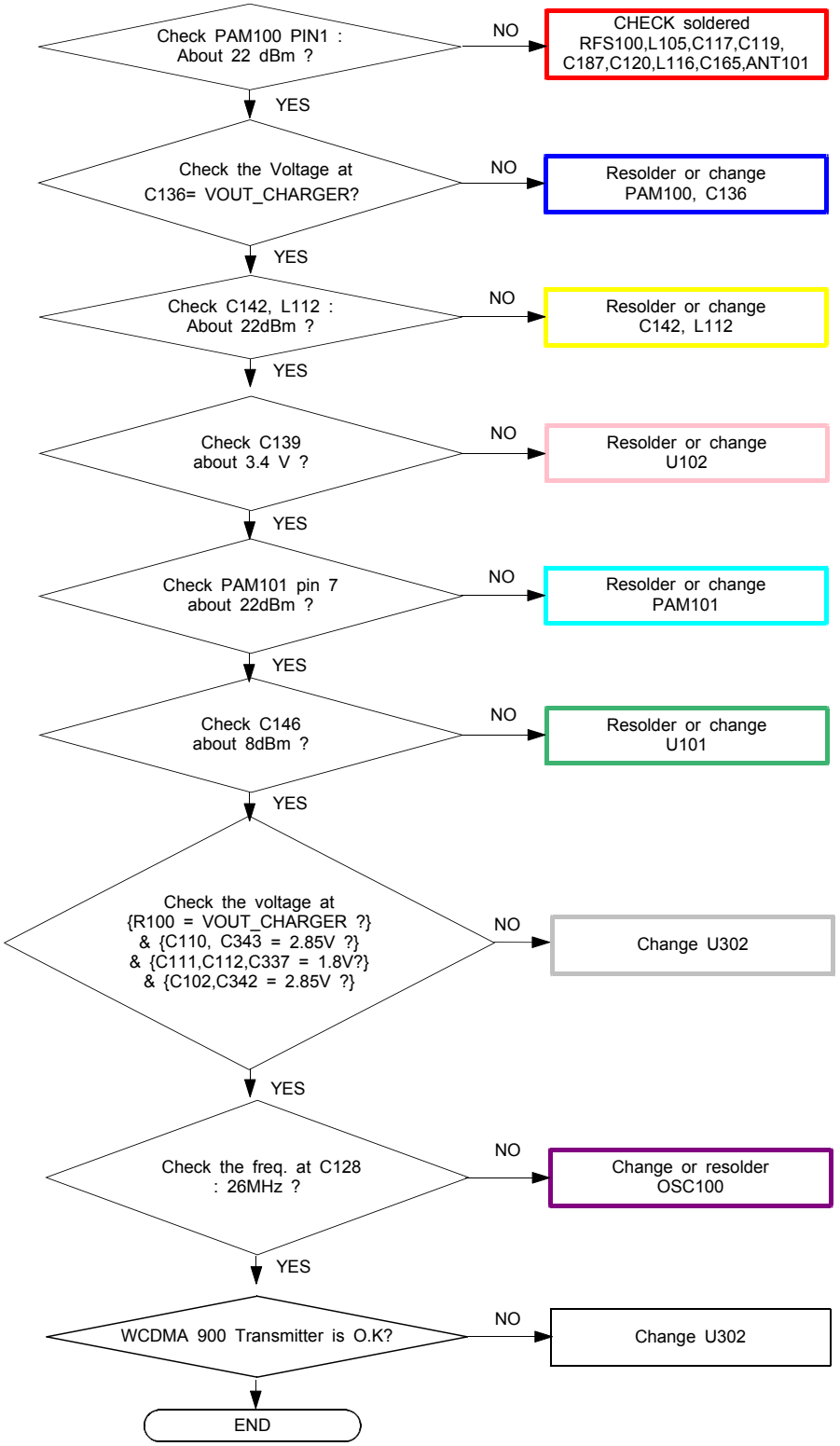
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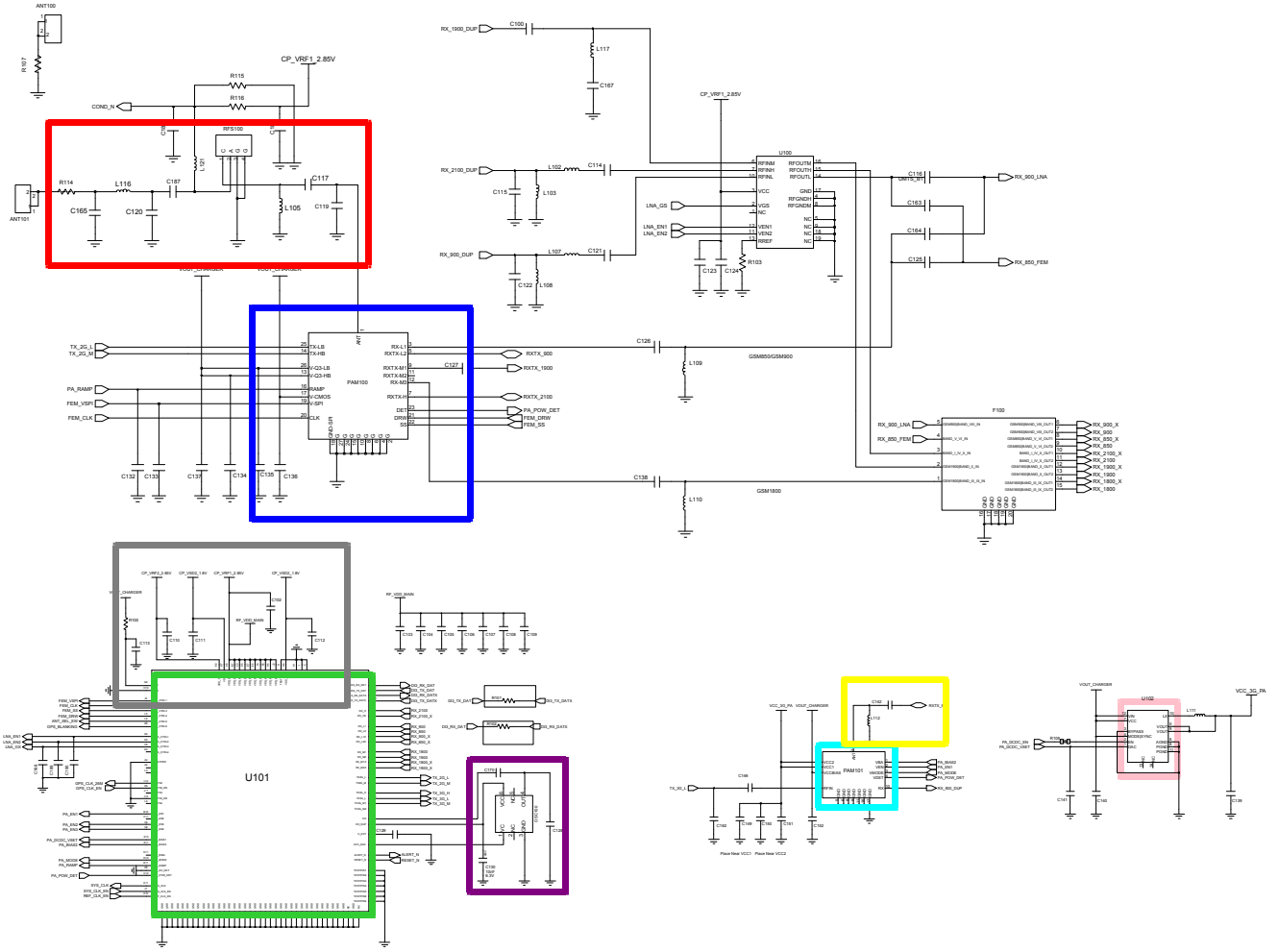
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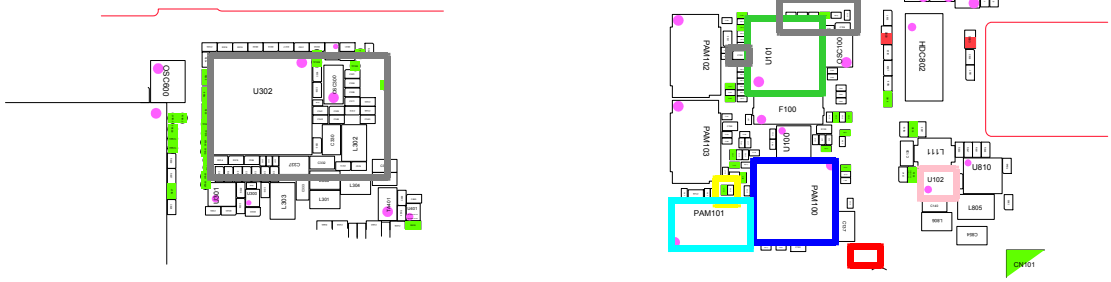
8-3-21. WCDMA BAND8 TX

CONTINOUS TX ON CONDITION
TX POWER DAC:14500 CODE APPLIED
GSM850 CH : 190
GSM900 CH : 62
RBW : 100KHz
VBW : 100KHz
SPAN : 10MHz
REF LEV. : 10dBm
ATT. : 20dB



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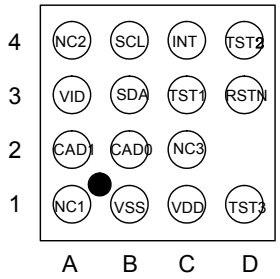


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8-4. Service Schematics

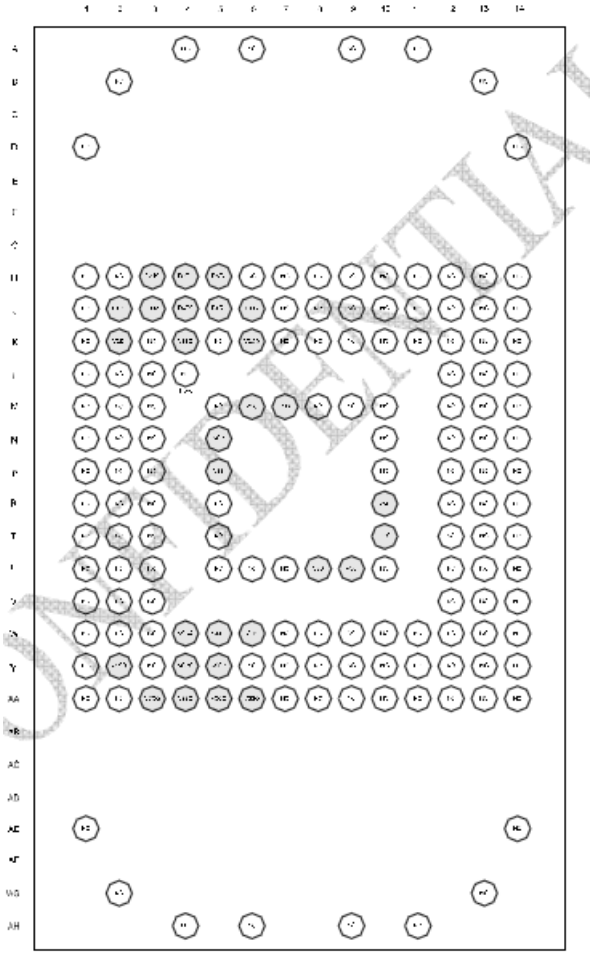
- NC Point(Top View)

U806



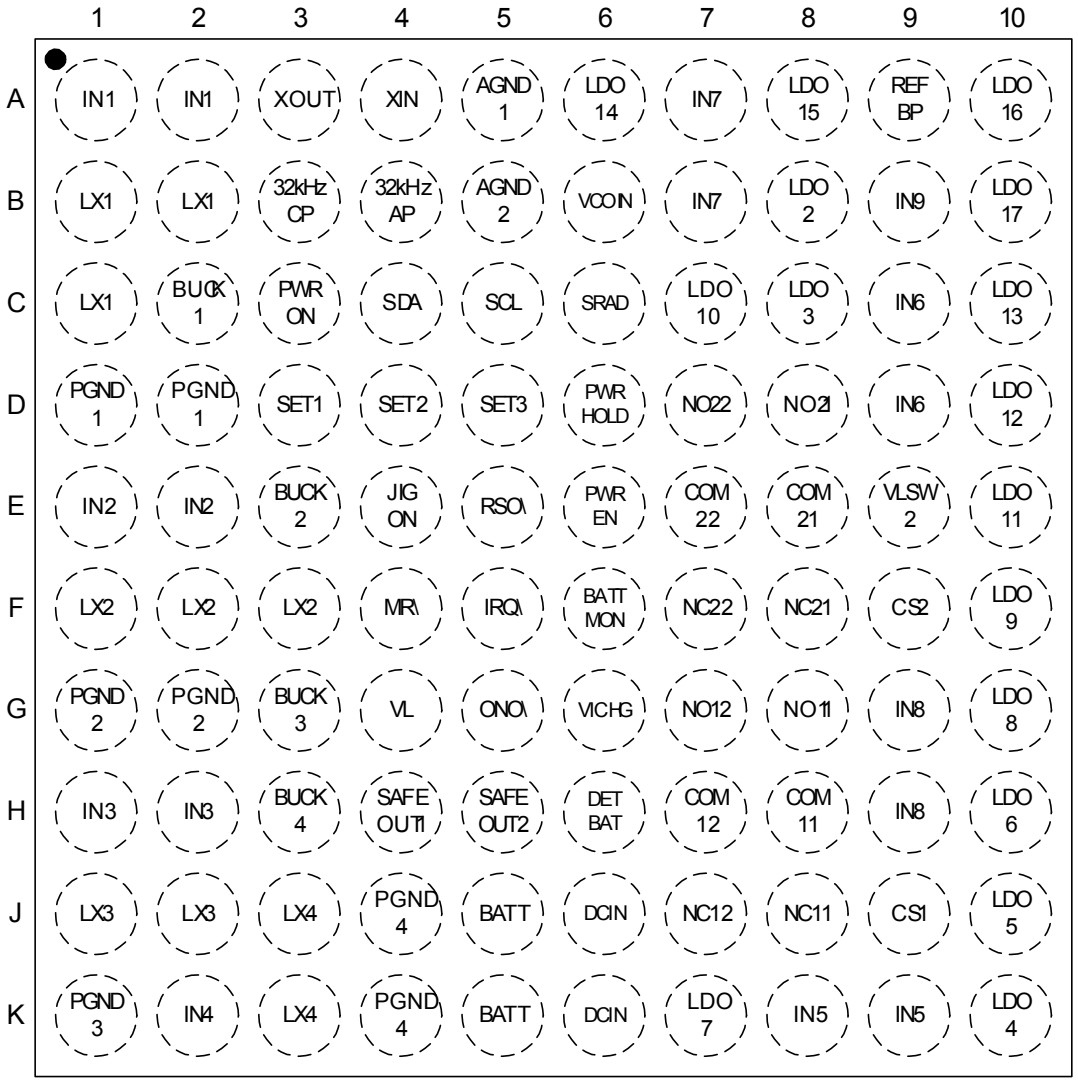
UME300

◯ : **NC**



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U700



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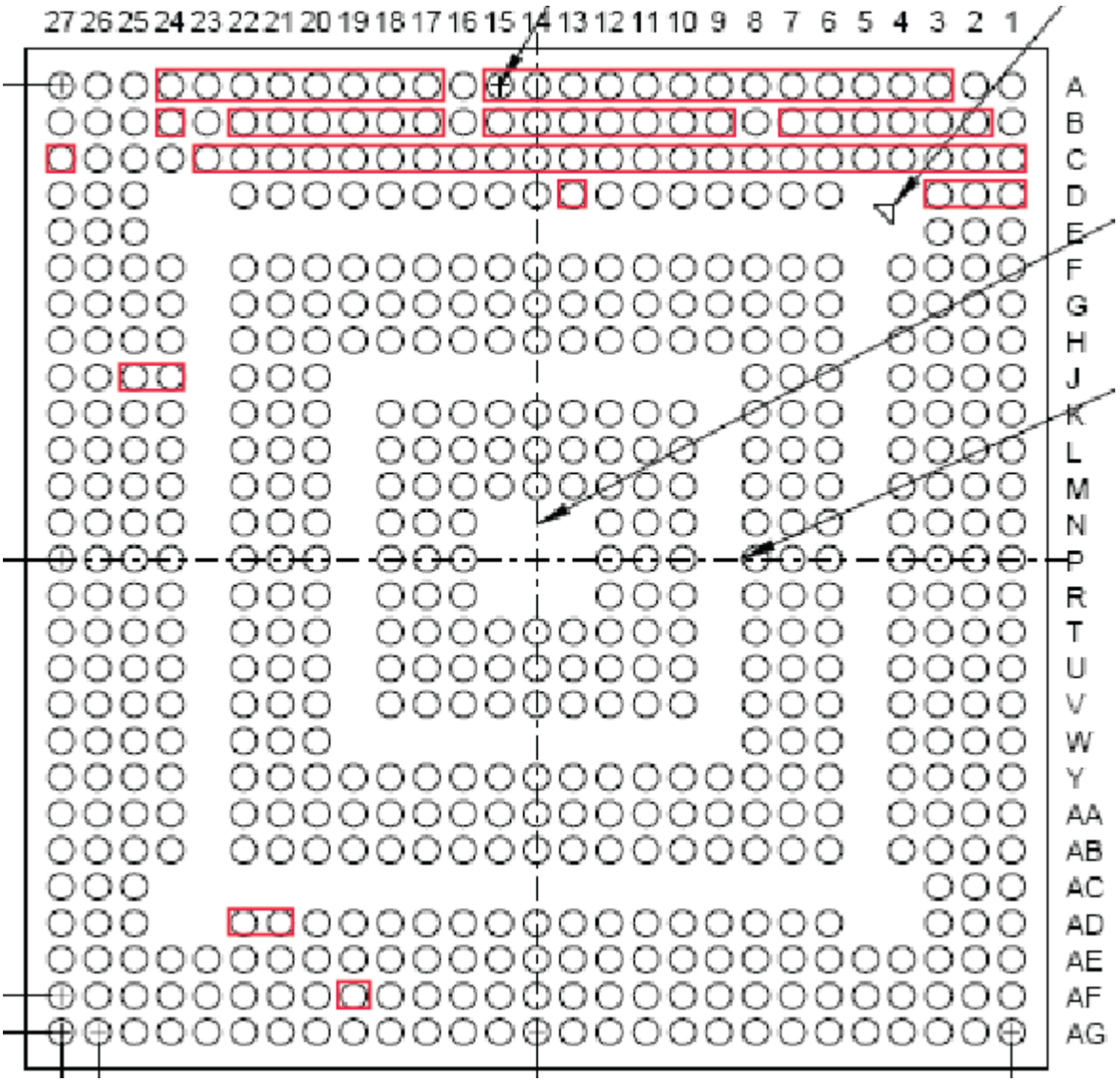
U302

	A	B	C	D	E	F	G	H	J	K	L	M	N	P	R	T	U	V	W	Y	
20	NC	VSD2	VSD2	VSS_SD2	SD2_FB	RESET_PMU_N	EPP1	VSS_MAIN	HF_SPK1P	VBAT_HF_SPK	HF_SPK2P	CP1	MEM_AD[0]	MEM_A[0]	MEM_A[1]	MEM_A[3]	MEM_BC2_n	MEM_A[5]	MEM_A[7]	NC	20
19	VSD1	VSS_SD1	VSS_SD1	VSS_SD2	PMUMEAS	VDD_VSIM	EPN1	VSS_HF_SPK	VBAT_HF_SPK	HF_SPK1N	HF_SPK2N	CP2	MEM_AD[1]	MEM_AD[2]	MEM_A[2]	MEM_A[4]	MEM_BC0_n	MEM_A[6]	MEM_A[9]	MEM_A[8]	19
18	VDD_VBAT_SD1	VSD1				VANA2			VSS_HF_SPK			VDD_CPN			MEM_AD[6]				MEM_A[11]	MEM_A[10]	18
17	SD1_FB	VDD_VBAT_SD1		VDD_VBAT_SD2	VDD_VBAT_SD2	VDD_VPMU	VDD_VRTC	MICP1	AUX1P	AUX2P	EPPA2	EPPA1	MEM_AD[3]	MEM_AD[5]	MEM_AD[8]	MEM_AD[11]	VDDP_DIG		MEM_A[12]	MEM_BC3_n	17
16	VRF1	VSS_ANA		VSS_MAIN	VDD_VBAT_LDO	VMIPI	VANA1	MICN1	AUX1N	AUX2N	VMIC	VUMIC	MEM_AD[4]	MEM_AD[7]	MEM_AD[10]	VSS_MAIN	MEM_AD[15]		MEM_A[13]	MEM_A[15]	16
15	VREF	VDD_VBAT_RF12	VSS_RF12	VRF2	VDD_VUSB				M2			M3				VDD_CORE	MEM_WAIT_n	MEM_ADV_n	MEM_BC1_n	MEM_A[14]	15
14	Di3_TX_DAT	Di3_RX_DAT		VDD_VBAT_RF12	VDD_OSD2		ON_OFF1	VDDP_DIG_MS	M1	M0	M7	M9	MEM_AD[9]	MEM_AD[13]		MEM_CAS_n	MEM_BFCLKO0		MEM_BFCLKO1	Reserved	14
13	Di3_TX_DATX	Di3_RX_DATX		F26M	VDD_VPLL		ON_OFF2	M6	M4	M5	M8	M10	MEM_AD[12]	MEM_AD[14]		MEM_RAS_n	MEM_WR_n		MEM_SDCLKO	Reserved	13
12	F32K	VDD_RTC	VSS_RTC	VDD_LVDS	DPLUS	CC_IO	RESET1_N	PWR_ON_CMP					MMCI1_CLK	MEM_CS0_n	MEM_CS1_n	MEM_CS2_n	MEM_RD_n	VSS_MAIN	Reserved	Reserved	12
11	OSC32K	VSS_MAIN		VSS_MAIN	DMINUS		CC_CLK	ALERT_N		VDD_CORE	VSS_MAIN		MMCI1_CMD	MMCI1_DAT[0]		MEM_CSA0_n	MEM_CKE		Reserved	Reserved	11
10	USB_DMINUS	USB_TUNE		VDD_USB_DIG	CC_RST		REF_CLK_EN	HW_MON1		MMCI1_DAT[2]	MMCI1_DAT[3]		VDDP_MMC	MMCI1_DAT[1]		TRIG_IN	TRST_n		Reserved	Reserved	10
9	USB_DPLUS	VBUS1	VDD_USB	VDD_USB_HSPHY	VDD_CORE	CLK32K	RESET_CMP	HW_MON2					MMCI1_CD	VDD_FUSE_LS	TCK	FCDP_RBn	RESET2_N	FWP	VSS_MAIN	Reserved	9
8	USB_TEST	USB_ID		MMCI2_CMD	CLKOUT0		CMP_EN	I2S1_RX	I2S1_CLK0	VDDP_DIG	TDO	GPIO_035	GPIO_041	VDD_FUSE_FS		VDD_CORE	MIPI1_DATA7		Reserved	Reserved	8
7	GPIO_001	GPIO_000		MMCI2_DAT[0]	VSS_MAIN		VSS_MAIN	I2S1_WA0	I2S1_WA1	VSS_MAIN	TDI	GPIO_036	GPIO_029	VSS_MAIN		VSS_MAIN	MIPI1_DATA6		Reserved	Reserved	7
6	GPIO_002	GPIO_007	NC	MMCI2_DAT[1]	VDDP_DIG				SWIF_TXRX			GPIO_030				VDDP_DIG	MIPI1_DATA5	NC	MIPI1_DATA2	Reserved	6
5	GPIO_003	GPIO_008		MMCI2_DAT[2]	MMCI2_DAT[3]	VDD_CORE	DSP_AUDIO_IN1	I2S1_TX	CHRG_EN	USIF1_RXD_MRST	TMS	GPIO_037	GPIO_031	GPIO_025	VDD_CORE	GPIO_020	MIPI1_DATA4		MIPI1_DATA1	Reserved	5
4	GPIO_004	GPIO_009		MMCI3_CD	MMCI2_CLK	VSS_MAIN	MMCI2_CD	USIF1_TXD_MTSR	USIF1_RTS_N	USIF1_CTS_N	USIF3_SCLK	GPIO_038	GPIO_032	GPIO_042	VSS_MAIN	GPIO_021	MIPI1_DATA3		MIPI1_DATA0	MIPI1_CLK	4
3	GPIO_005	GPIO_010				VDDP_DIG			VSS_MAIN			VSS_MAIN			VDDP_DIG				GPIO_013	GPIO_011	3
2	GPIO_006	MMCI3_CMD	MMCI3_DAT[0]	MMCI3_DAT[2]	T_OUT0	FSYSEN	XRESET	I2C1_SCL	I2C2_SCL	USIF3_TXD_MTSR	USIF3_RTS_N	GPIO_039	GPIO_033	GPIO_027	GPIO_024	CLKOUT2	GPIO_018	GPIO_016	GPIO_014	GPIO_012	2
1	NC	MMCI3_CLK	MMCI3_DAT[1]	MMCI3_DAT[3]	T_OUT1	SYSCLKEN	I2C_INT	I2C1_SDA	I2C2_SDA	USIF3_RXD_MRST	USIF3_CTS_N	GPIO_040	GPIO_034	GPIO_028	GPIO_026	GPIO_023	GPIO_019	GPIO_017	GPIO_015	NC	1
	A	B	C	D	E	F	G	H	J	K	L	M	N	P	R	T	U	V	W	Y	

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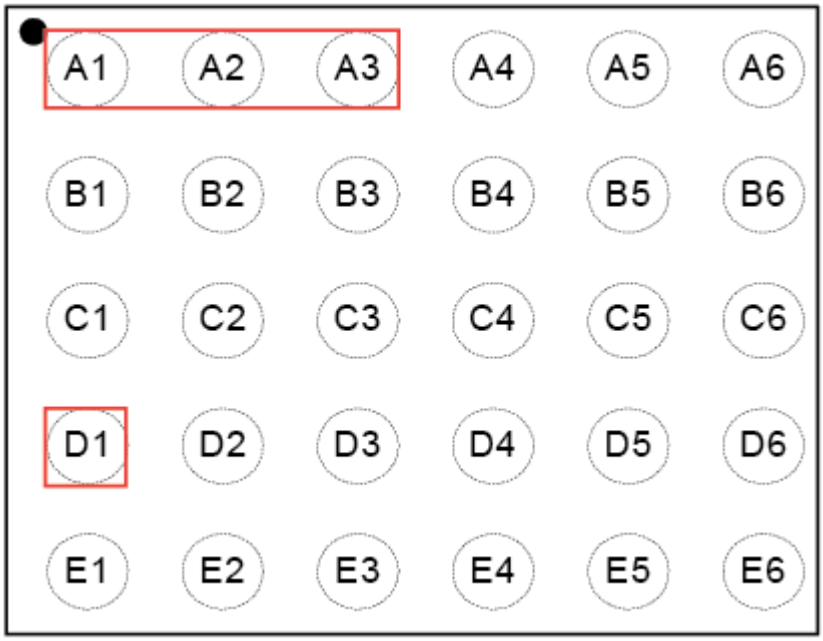
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