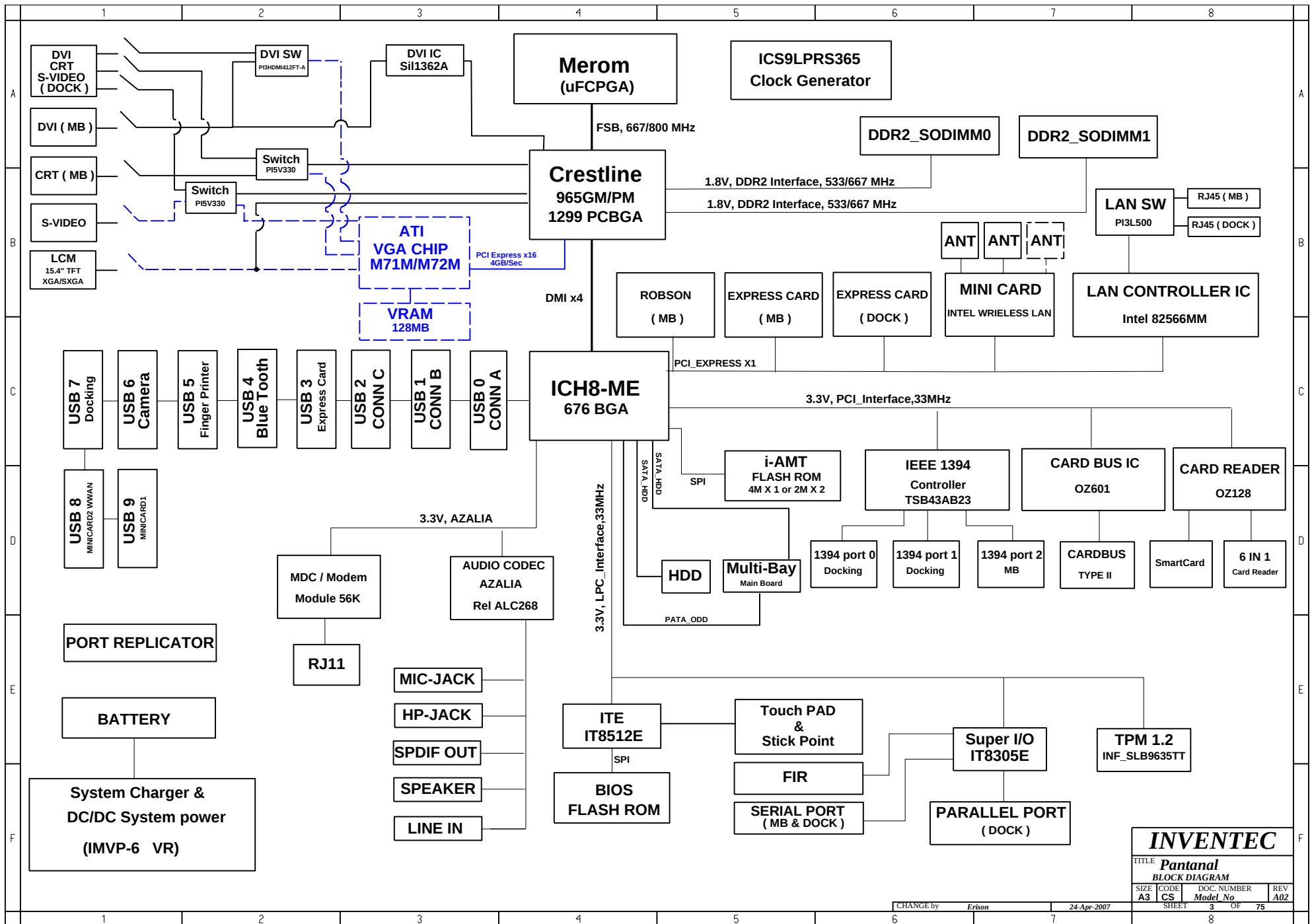
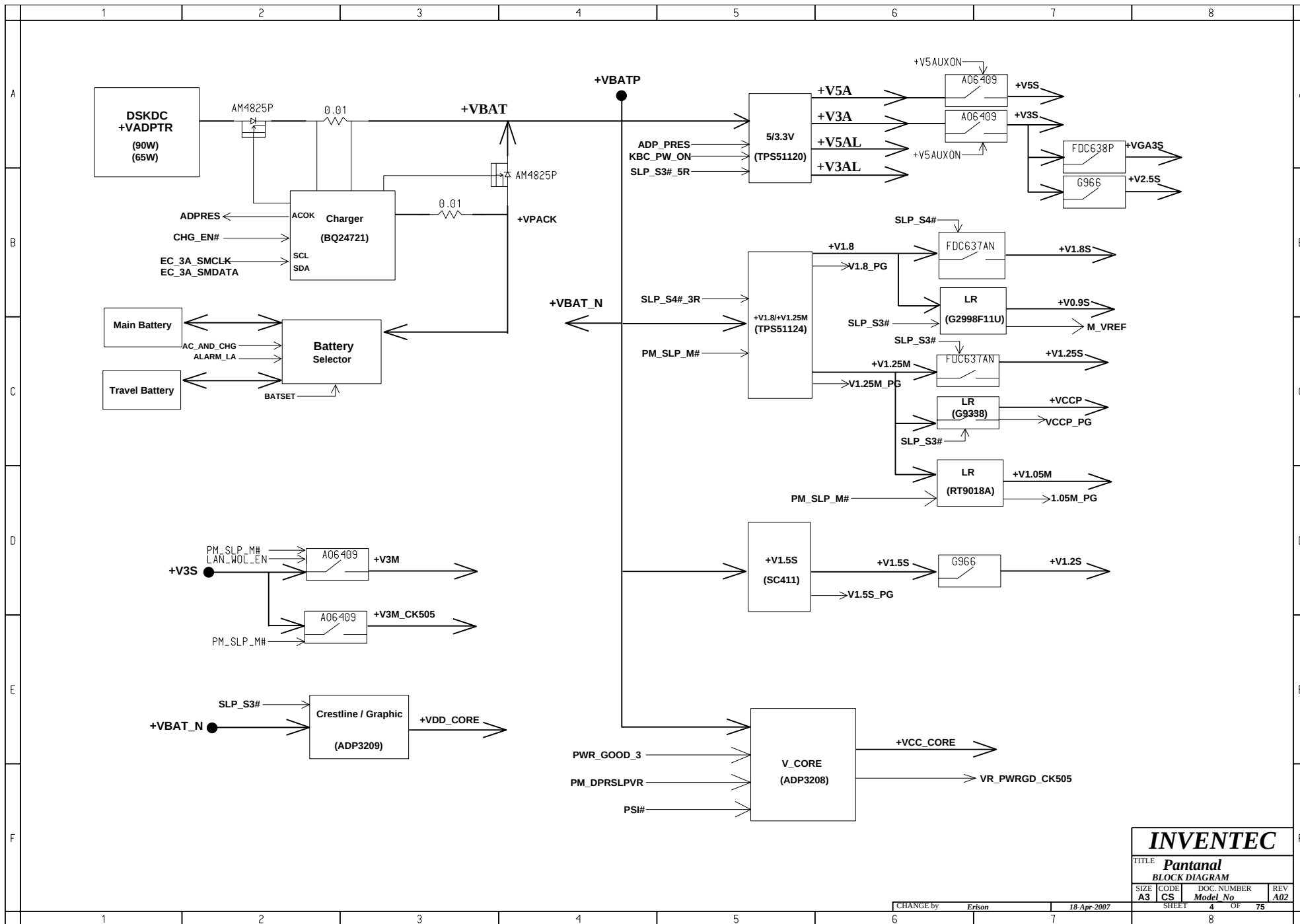


2007 0425



INVENTEC

TITLE Pantanal			
BLOCK DIAGRAM			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02
CHANGE by Erison		24-Apr-2007	
SHEET 3		OF 75	



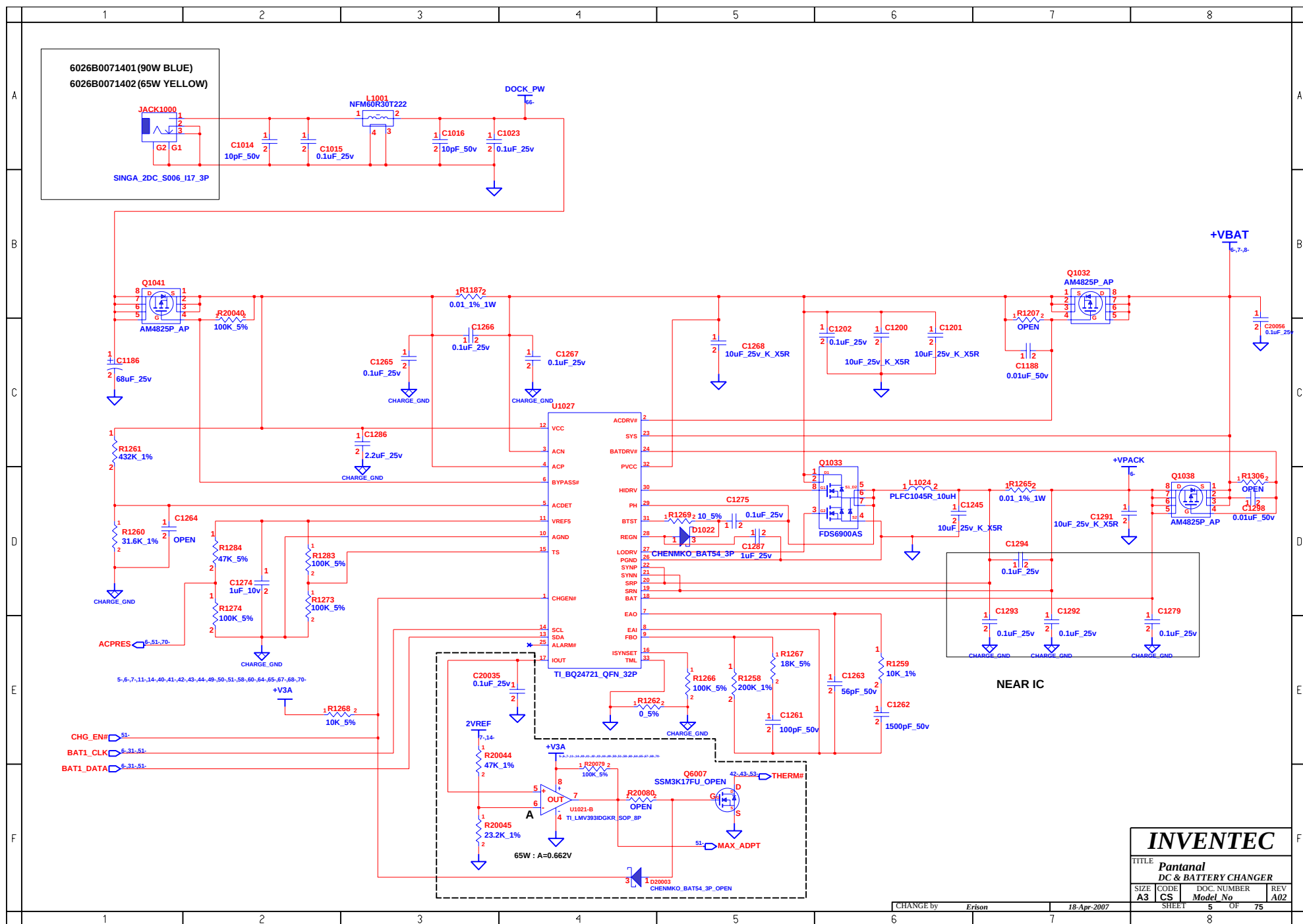
INVENTEC

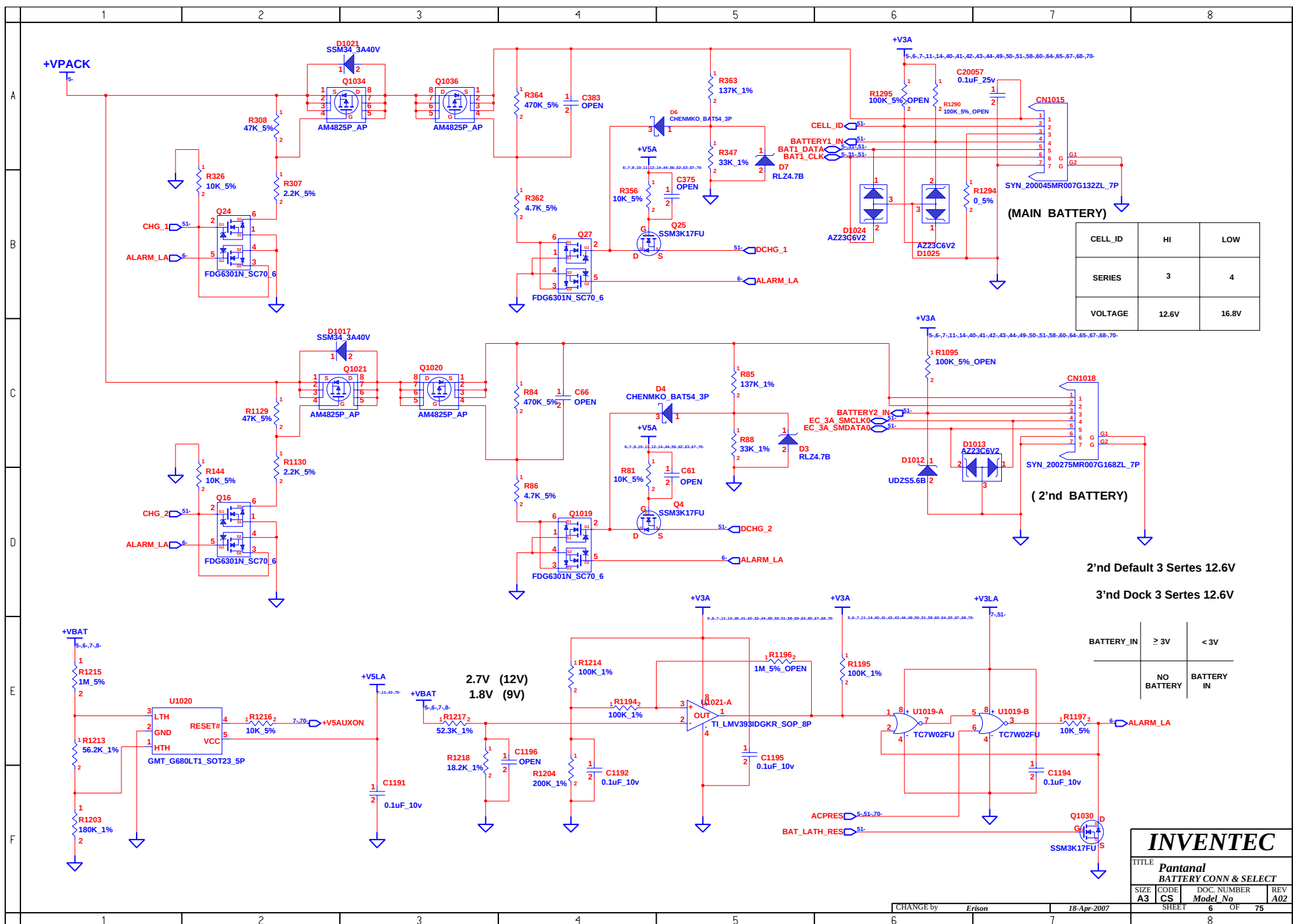
TITLE **Pantanal**
BLOCK DIAGRAM

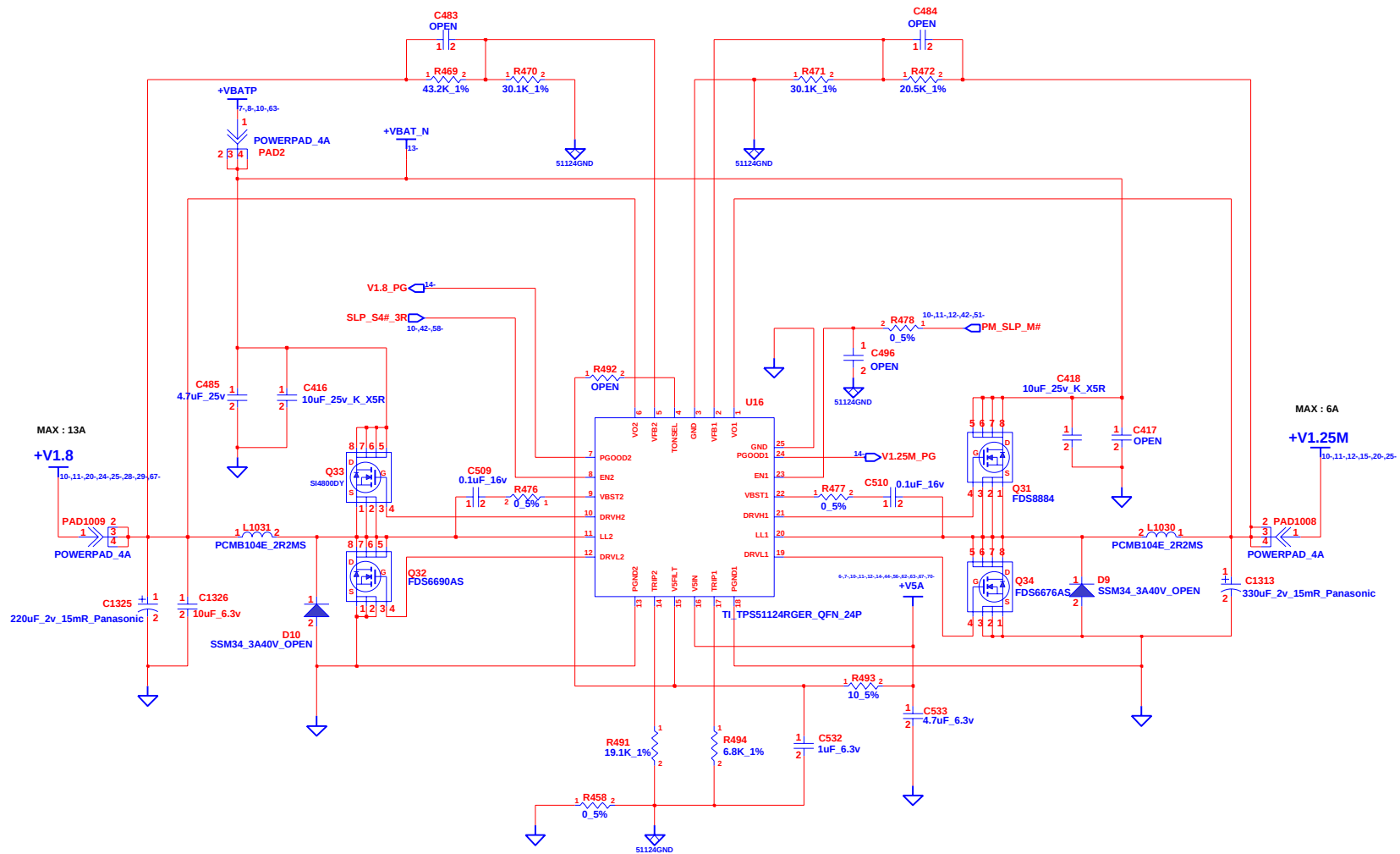
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02
-------------------	-------------------	--------------------------------	-------------------

CHANGE by *Erison* 18-Apr-2007

SHEET 4 OF 75





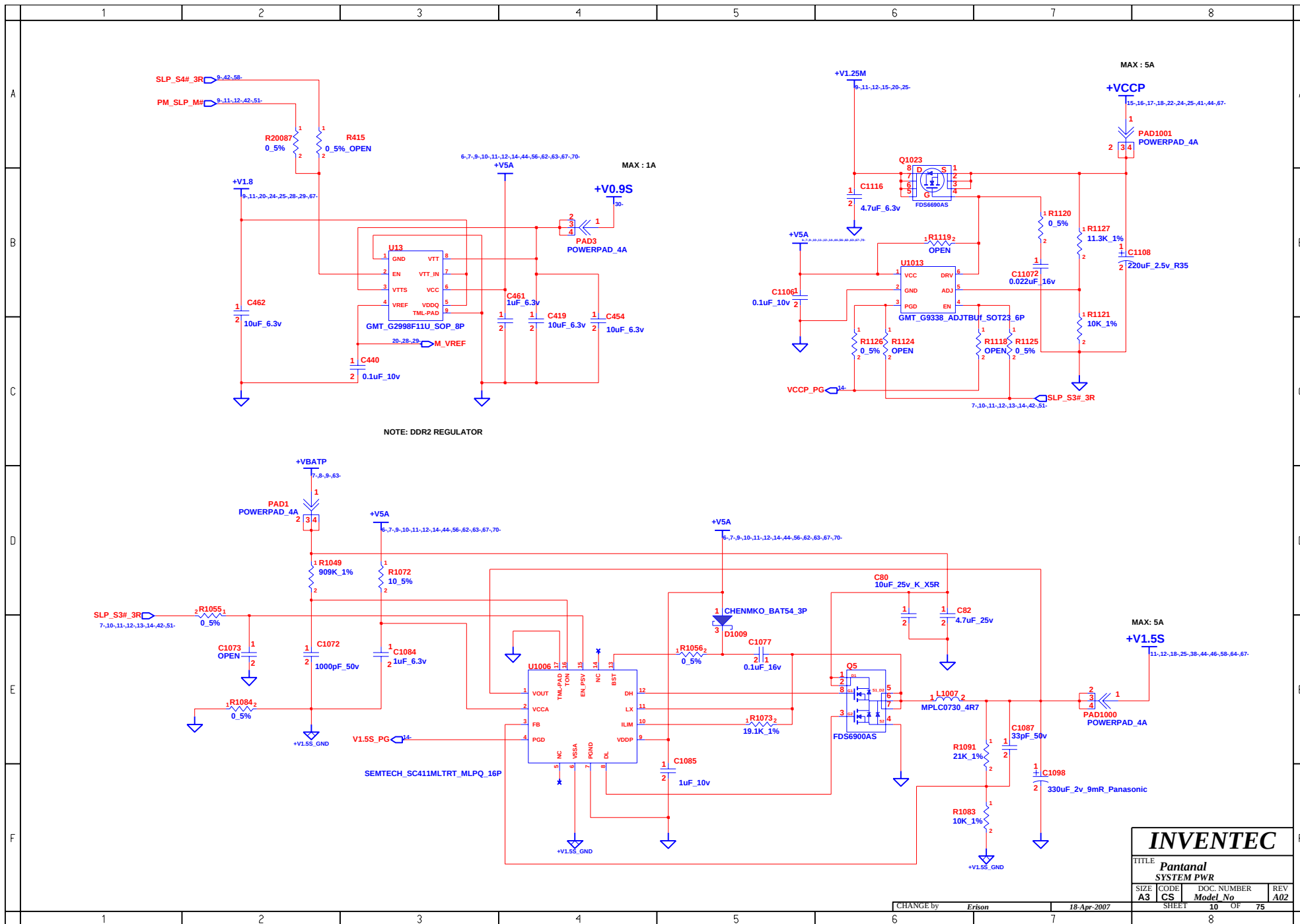


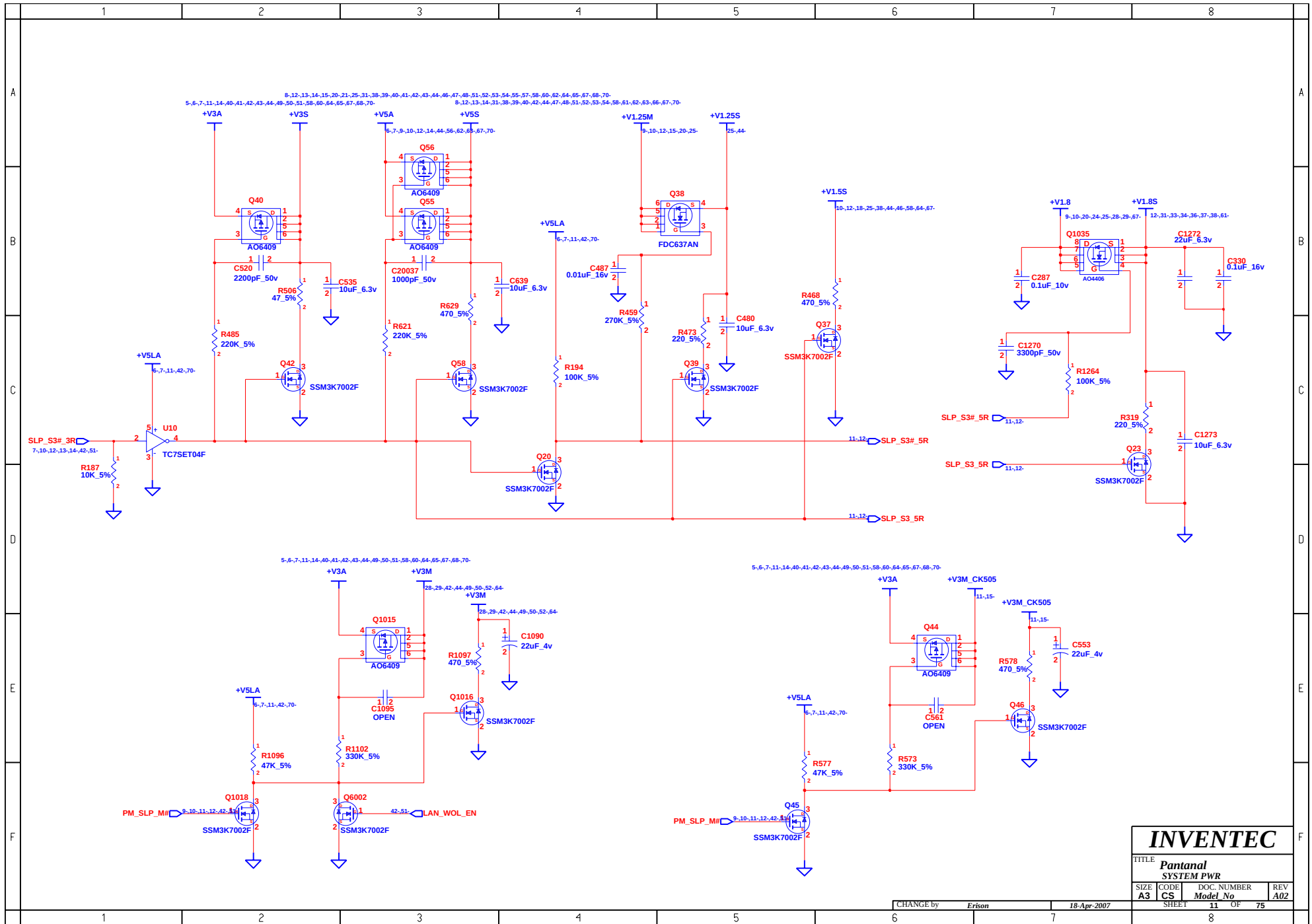
INVENTEC

TITLE			
Pantanal SYSTEM PWR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

SHEET 9 OF 75



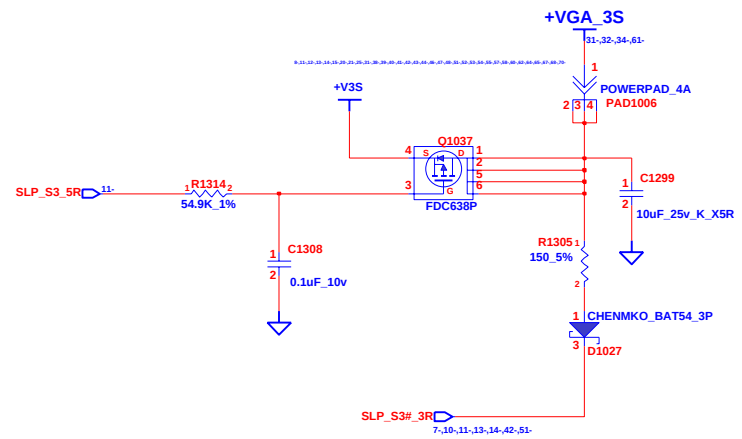
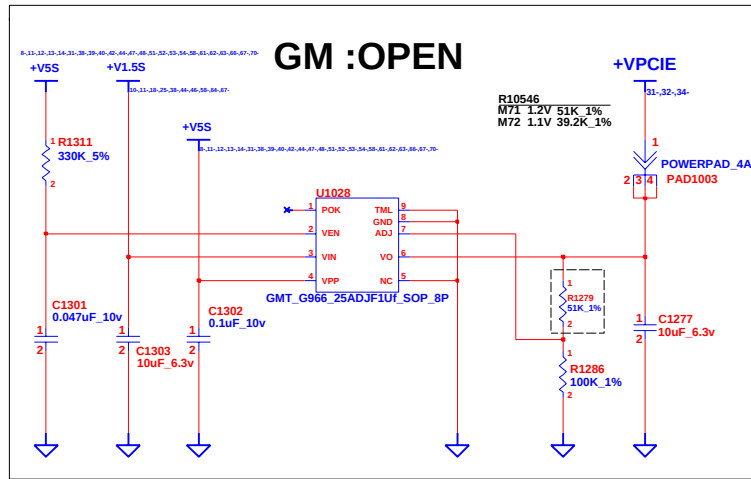
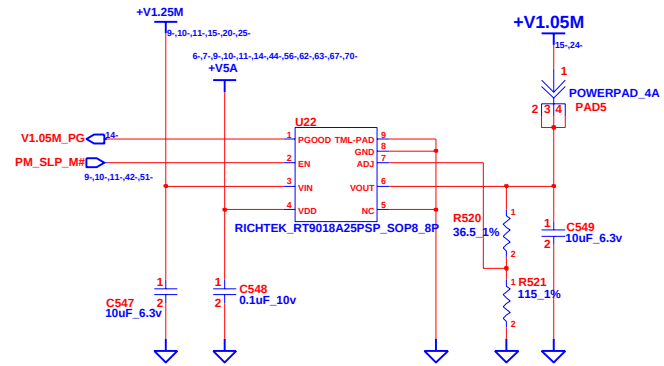
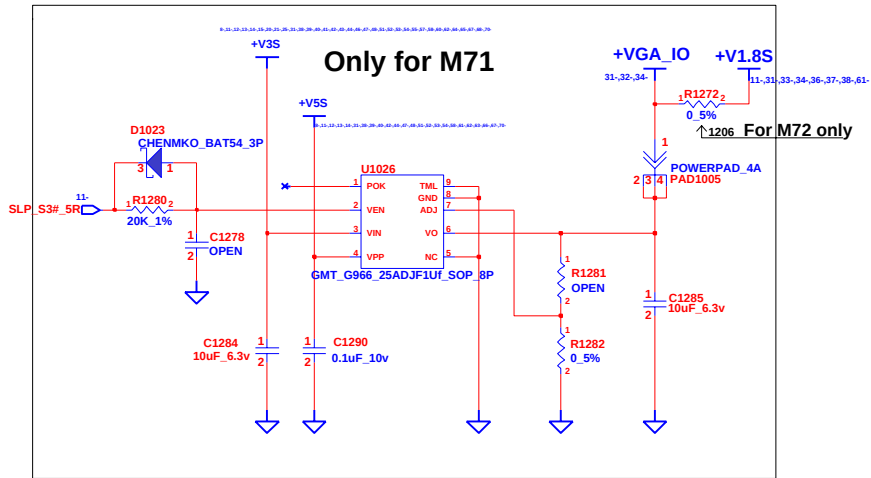


INVENTEC

TITLE			
Pantanal SYSTEM PWR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

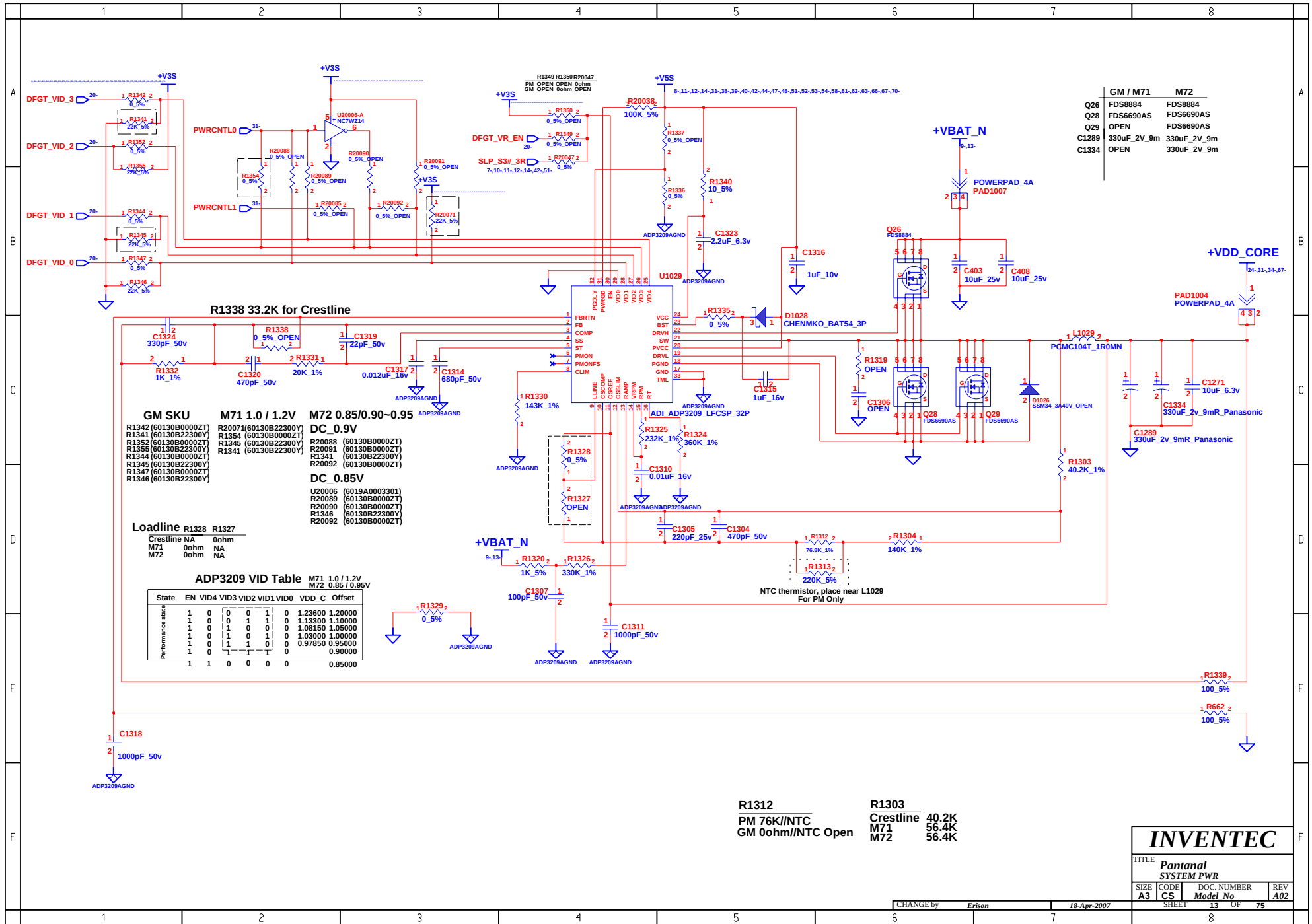
CHANGE by Erison 18-Apr-2007

SHEET 11 OF 75



INVENTEC				
TITLE Pantanal SYSTEM PWR				
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02	
SHEET 12 OF 75				

CHANGE by Erison 18-Apr-2007

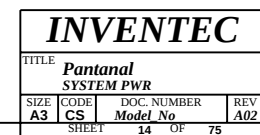


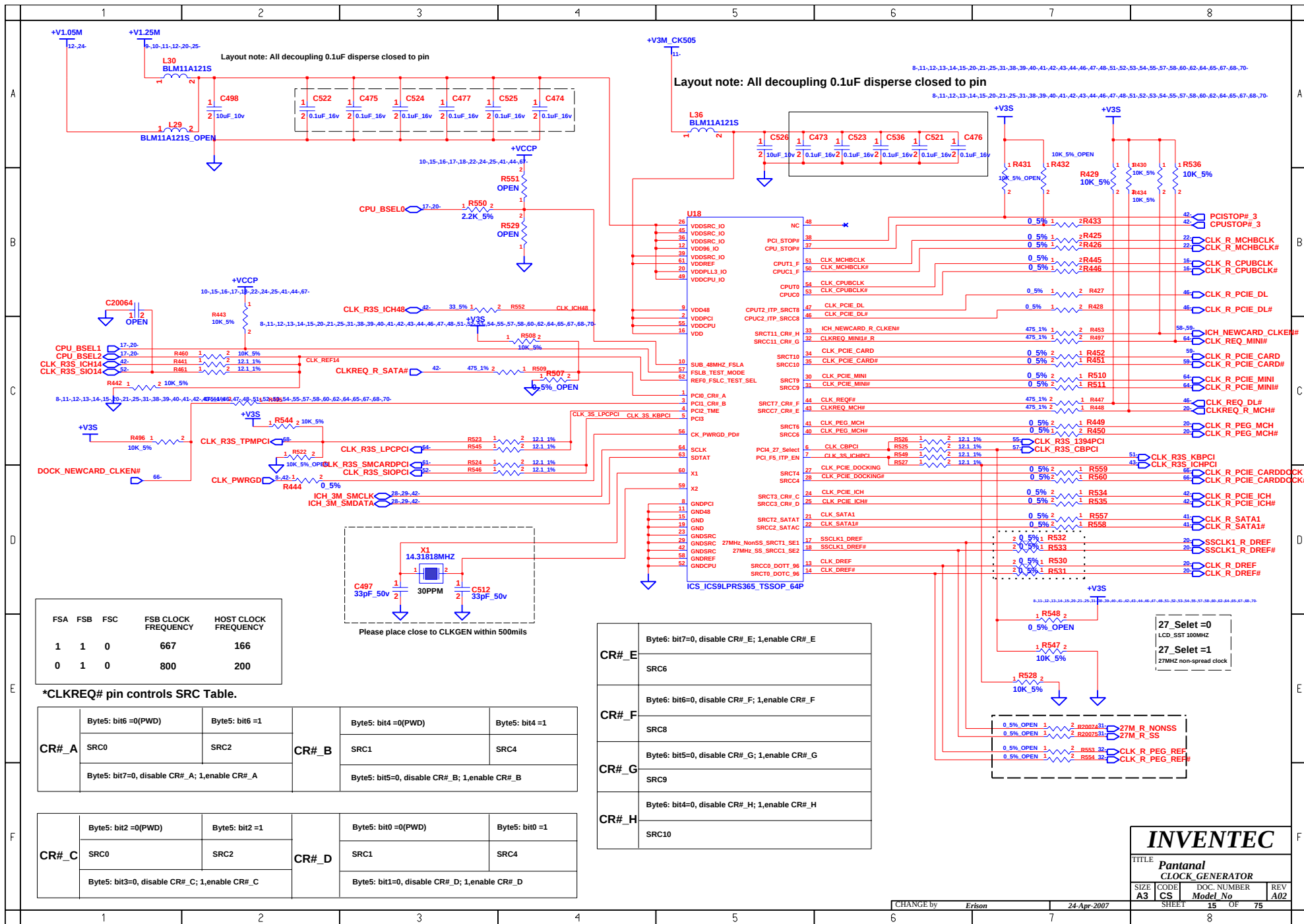
INVENTEC

TITLE			
Pantanal SYSTEM PWR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by *Erison* 18-Apr-2007

SHEET 13 OF 75





FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200

*CLKREQ# pin controls SRC Table.

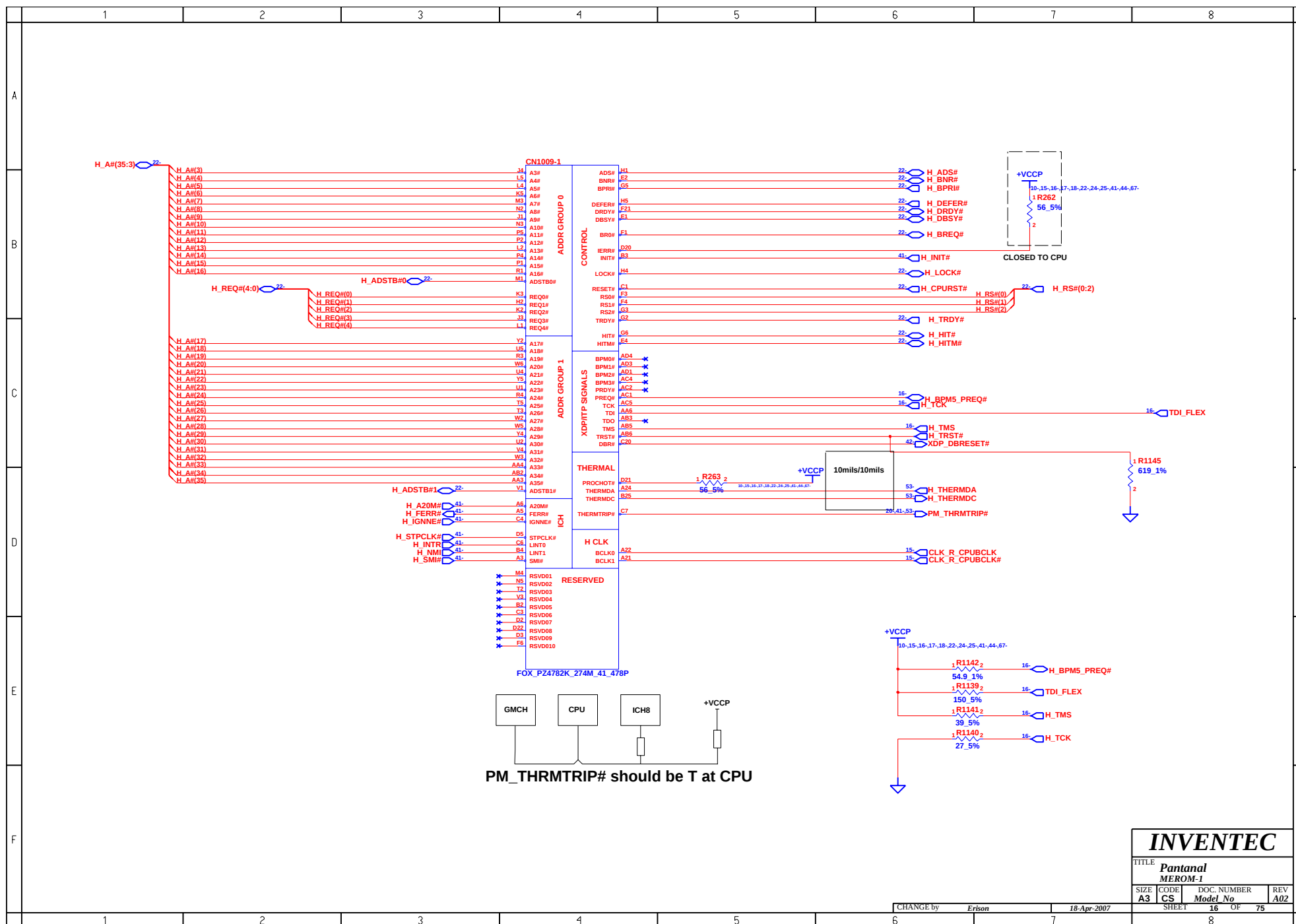
CR#_A	Byte5: bit6 =0(PWD)	Byte5: bit6 =1	CR#_B	Byte5: bit4 =0(PWD)	Byte5: bit4 =1
	SRC0	SRC2		SRC1	SRC4
	Byte5: bit7=0, disable CR#_A; 1,enable CR#_A			Byte5: bit5=0, disable CR#_B; 1,enable CR#_B	

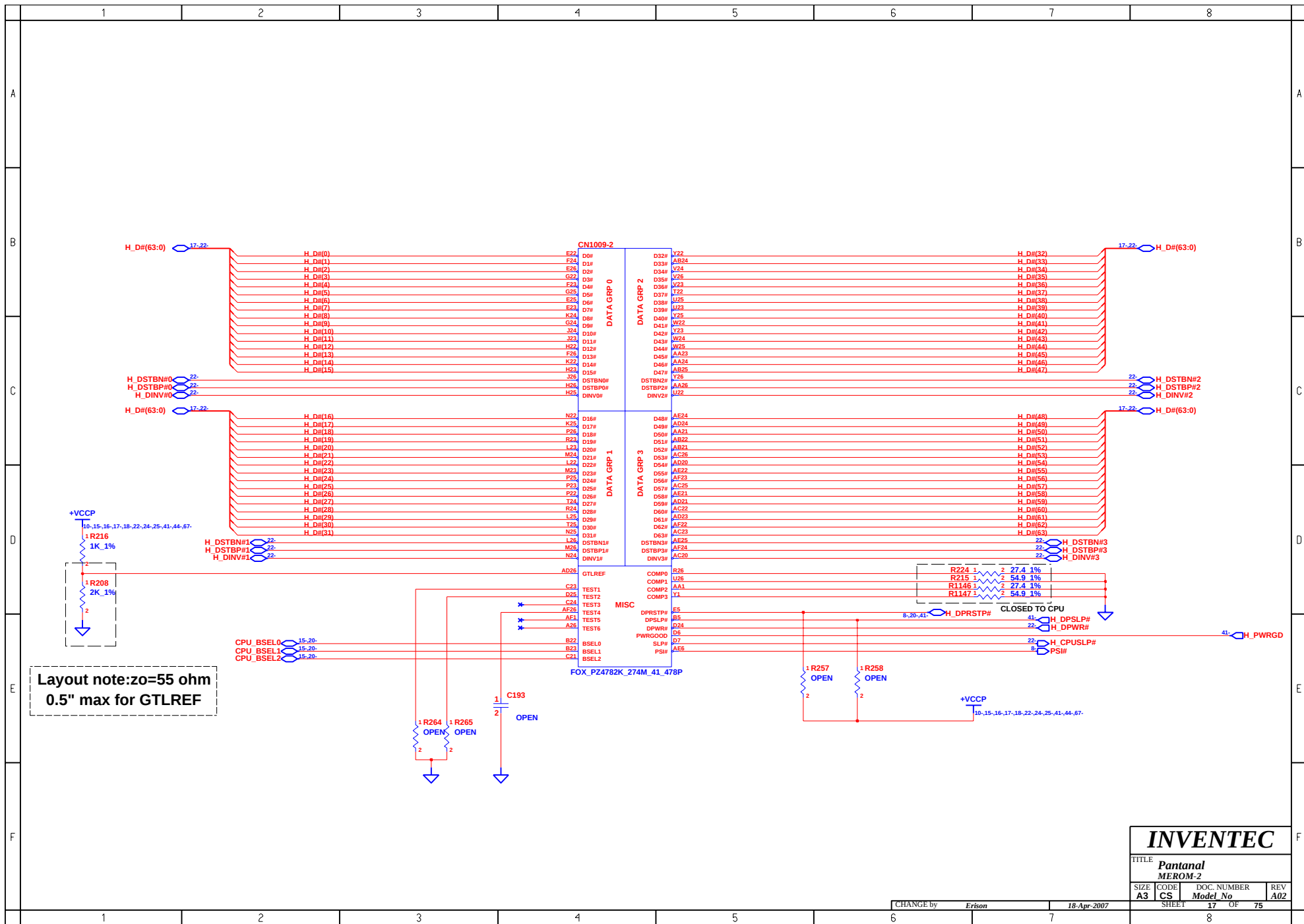
CR#_C	Byte5: bit2 =0(PWD)	Byte5: bit2 =1	CR#_D	Byte5: bit0 =0(PWD)	Byte5: bit0 =1
	SRC0	SRC2		SRC1	SRC4
	Byte5: bit3=0, disable CR#_C; 1,enable CR#_C			Byte5: bit1=0, disable CR#_D; 1,enable CR#_D	

CR#_E	Byte6: bit7=0, disable CR#_E; 1,enable CR#_E
SRC6	
CR#_F	Byte6: bit6=0, disable CR#_F; 1,enable CR#_F
SRC8	
CR#_G	Byte6: bit5=0, disable CR#_G; 1,enable CR#_G
SRC9	
CR#_H	Byte6: bit4=0, disable CR#_H; 1,enable CR#_H
SRC10	

INVENTEC

TITLE Pantanal CLOCK GENERATOR			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02





The drawing shows a grid of reinforcement bars (VSS) for a structure. The grid is labeled with letters A through F and numbers 1 through 8. The bars are arranged in a grid pattern, with labels for each bar. The labels are as follows:

Letter	1	2	3	4	5	6	7	8
A	VSS001	VSS002	VSS003	VSS004	VSS005	VSS006	VSS007	VSS008
B	VSS009	VSS010	VSS011	VSS012	VSS013	VSS014	VSS015	VSS016
C	VSS017	VSS018	VSS019	VSS020	VSS021	VSS022	VSS023	VSS024
D	VSS025	VSS026	VSS027	VSS028	VSS029	VSS030	VSS031	VSS032
E	VSS033	VSS034	VSS035	VSS036	VSS037	VSS038	VSS039	VSS040
F	VSS041	VSS042	VSS043	VSS044	VSS045	VSS046	VSS047	VSS048

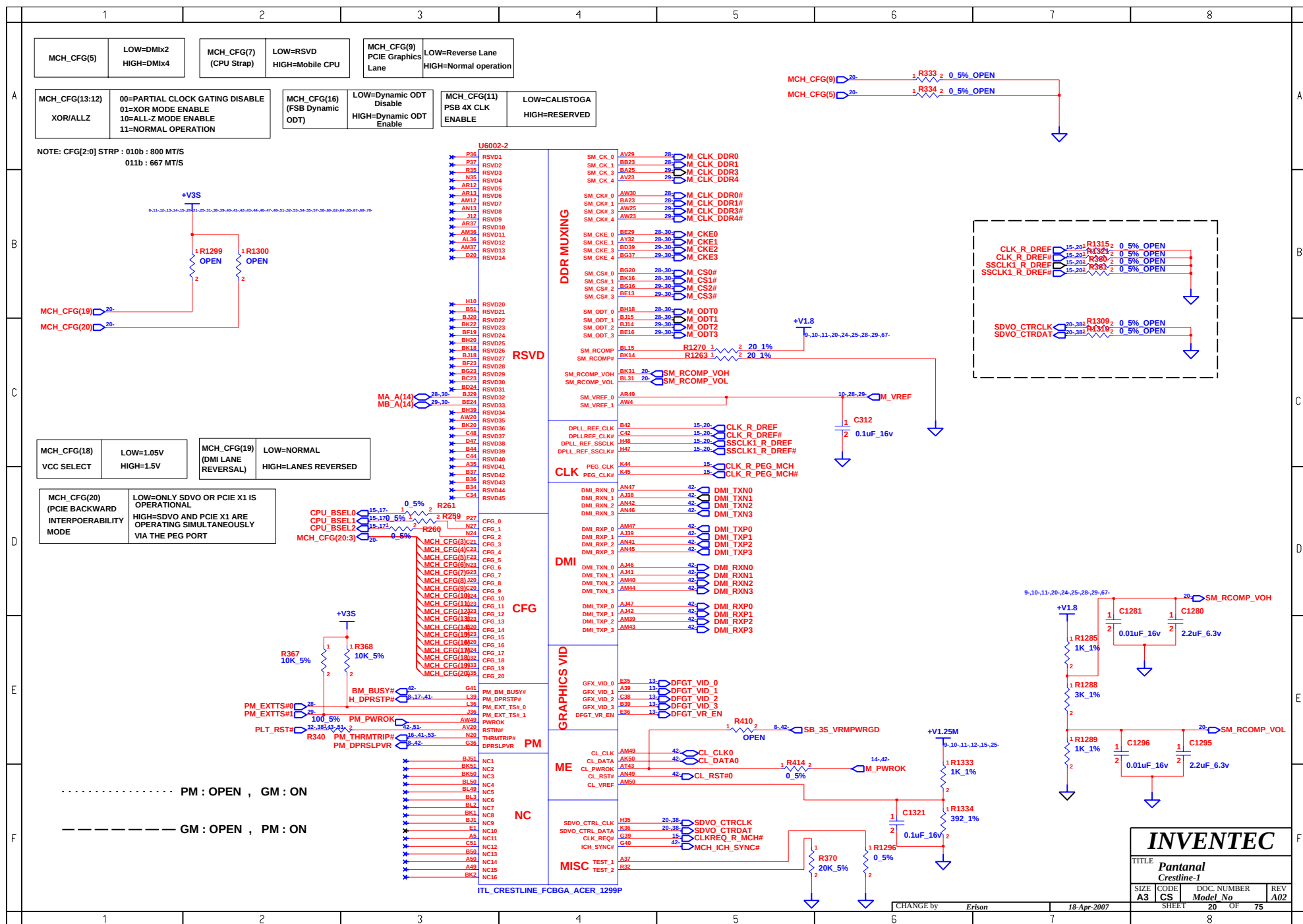
The drawing also includes a title block in the bottom right corner, which contains the following information:

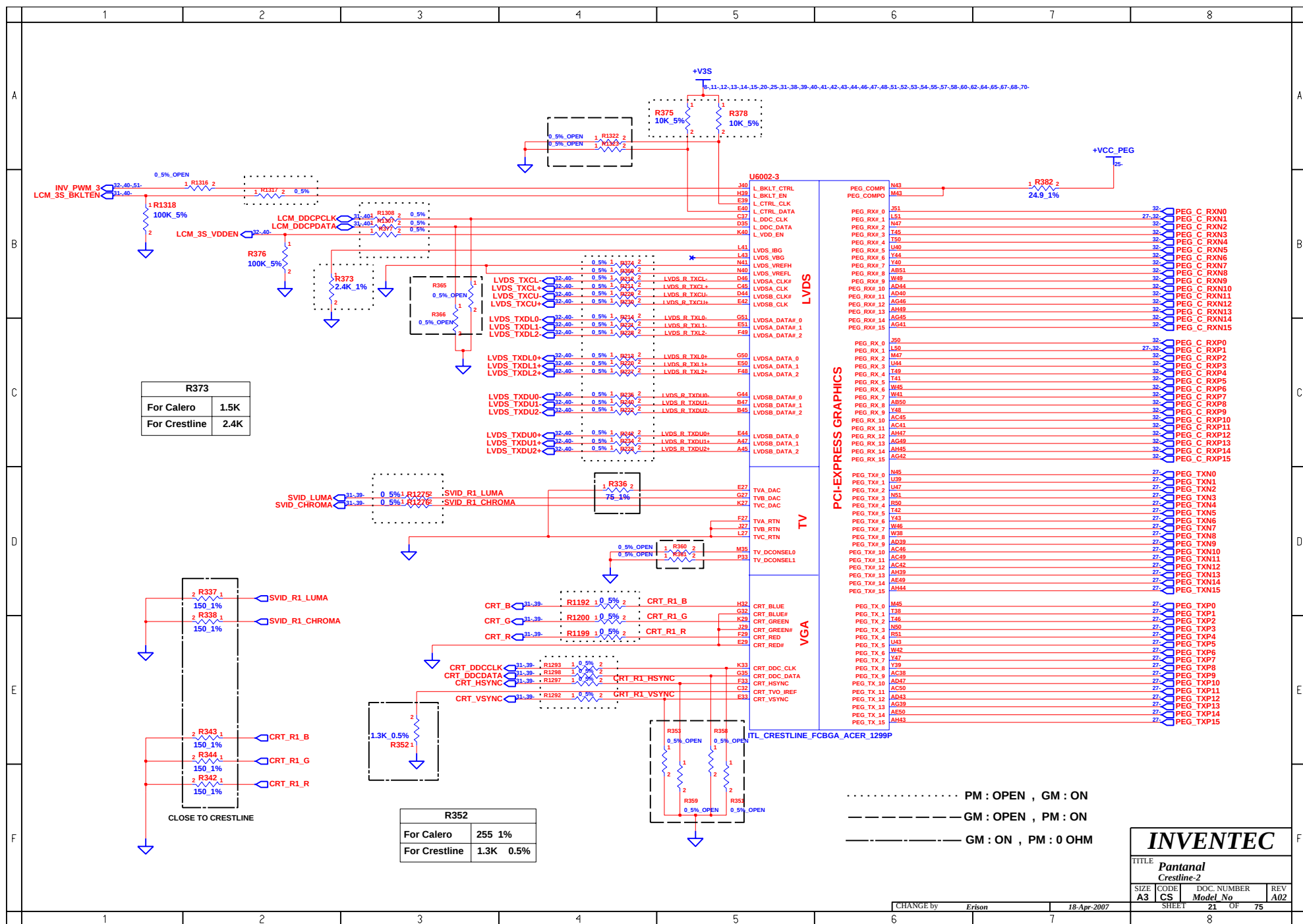
INVENTEC			
TITLE			
Pantanal			
MEROM-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

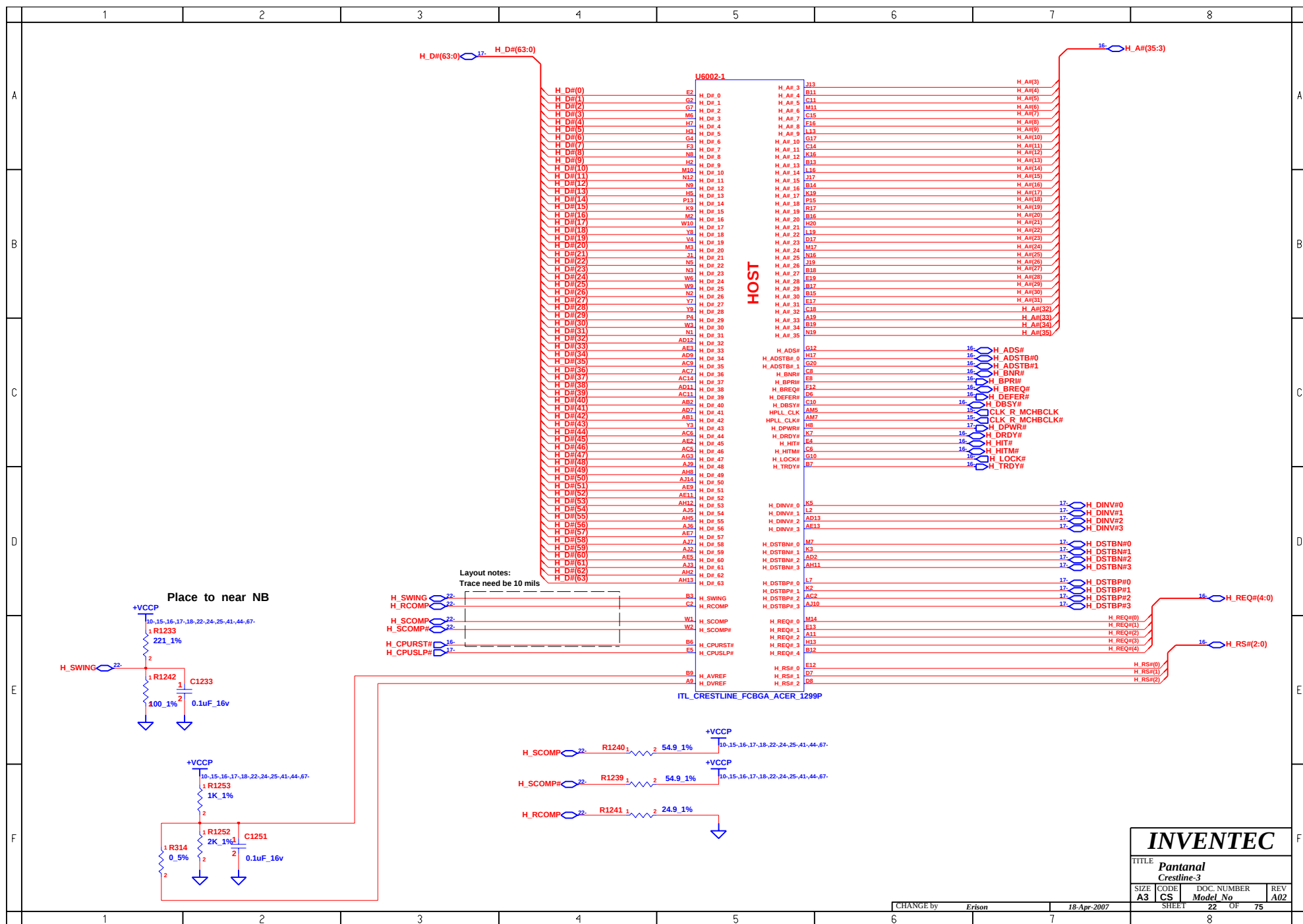
The drawing also includes a change log in the bottom right corner, which contains the following information:

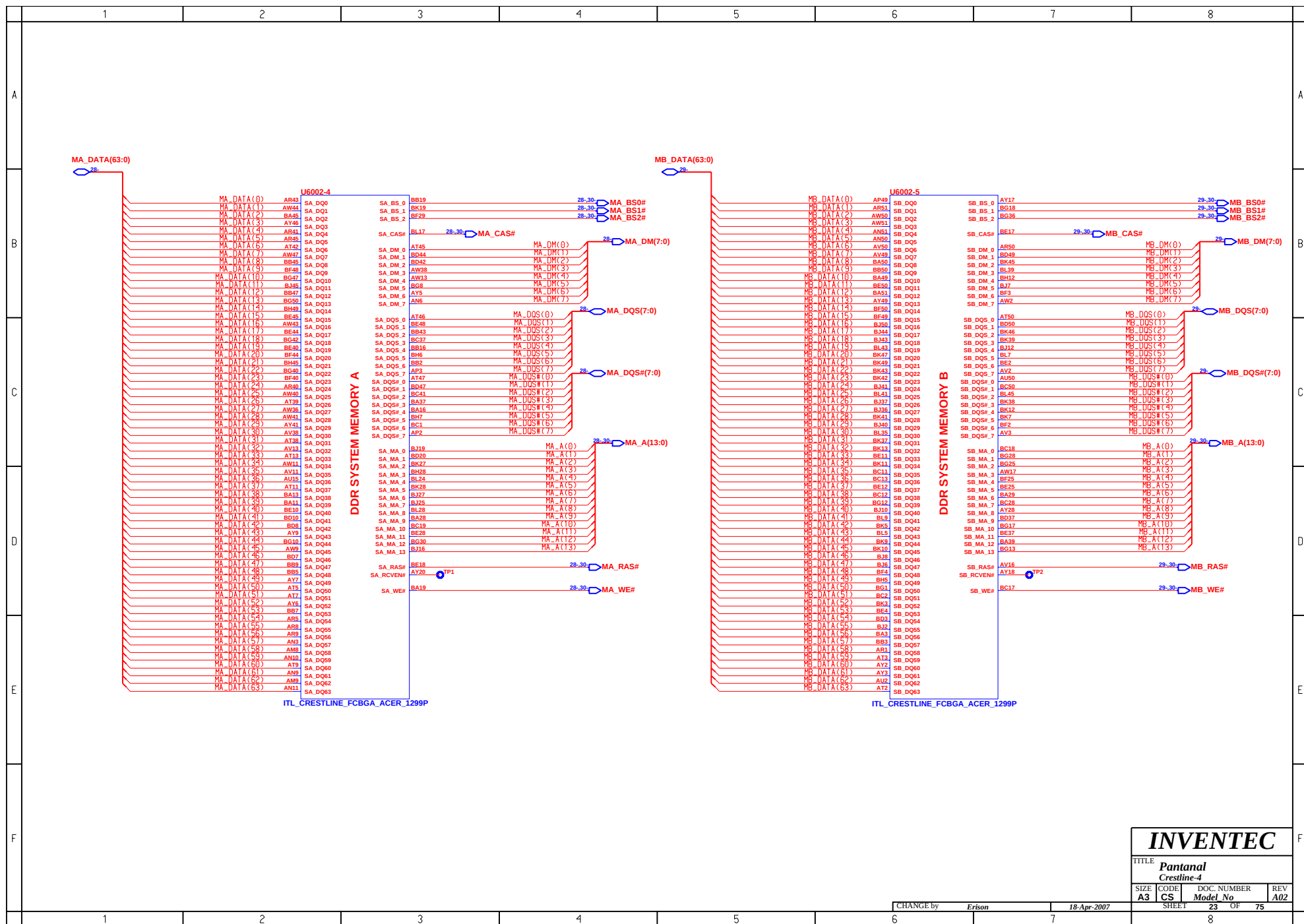
CHANGE by	DATE	DESCRIPTION
Erison	18-Apr-2007	

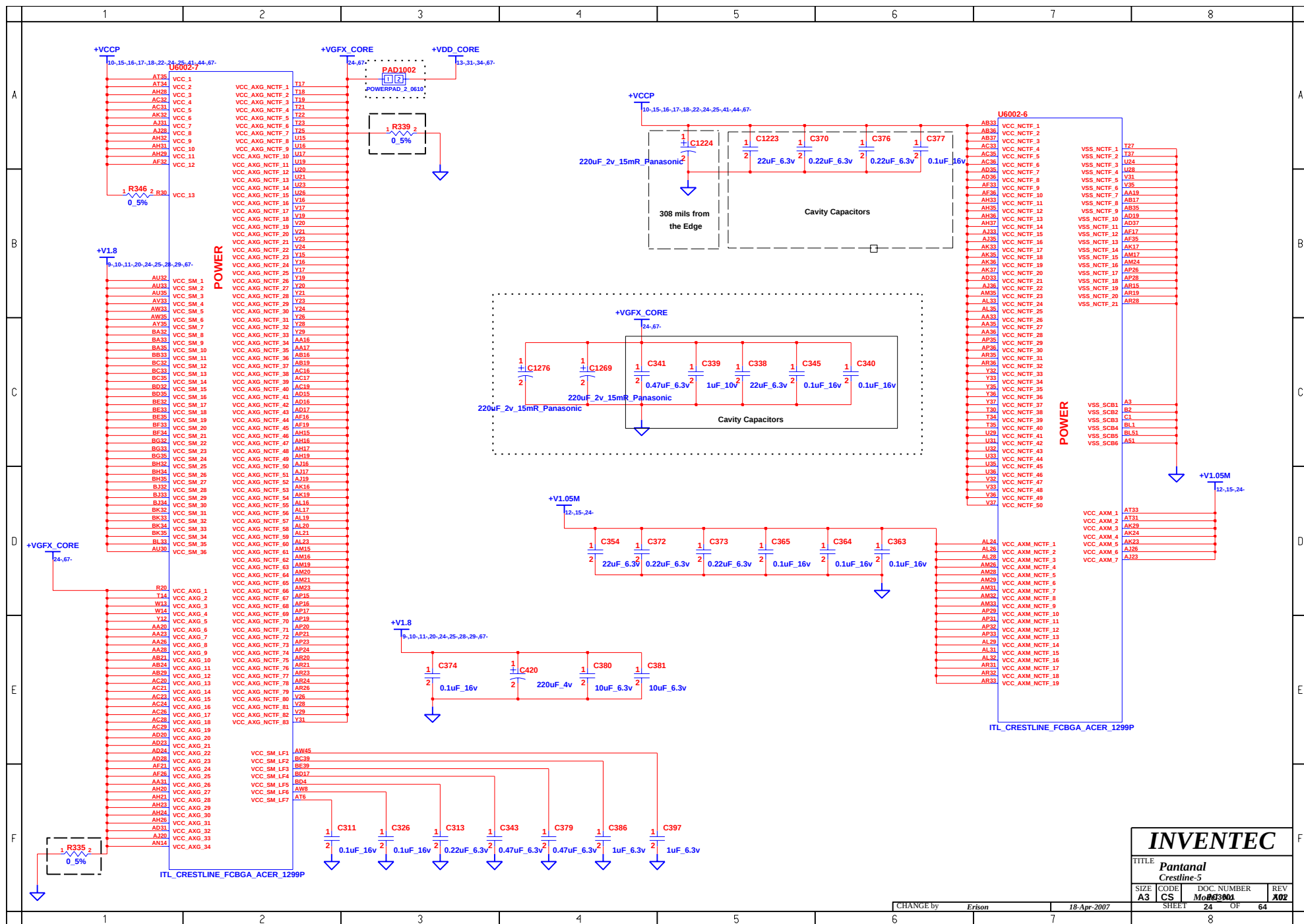
The drawing also includes a sheet number in the bottom right corner, which is 19 of 75.

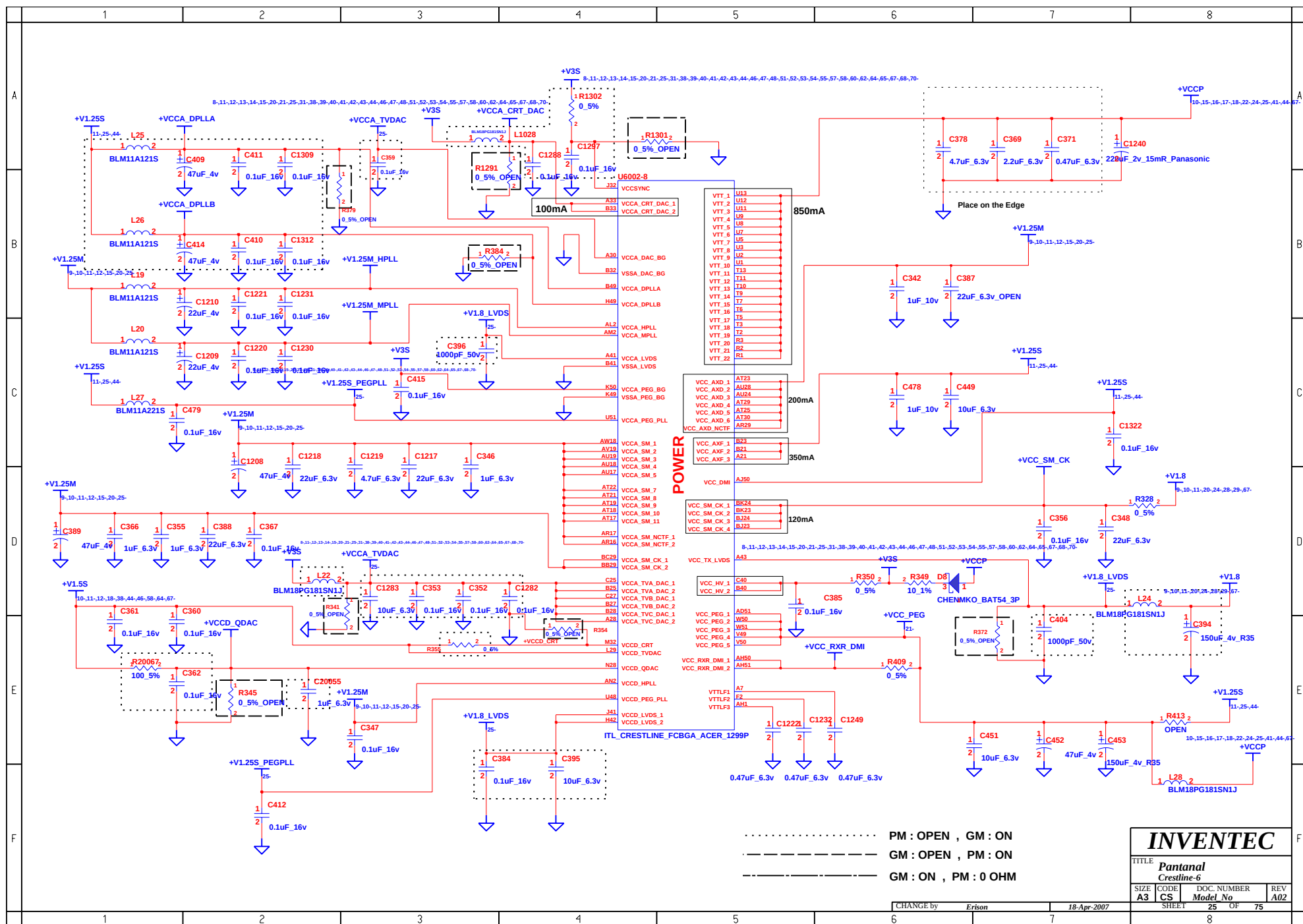


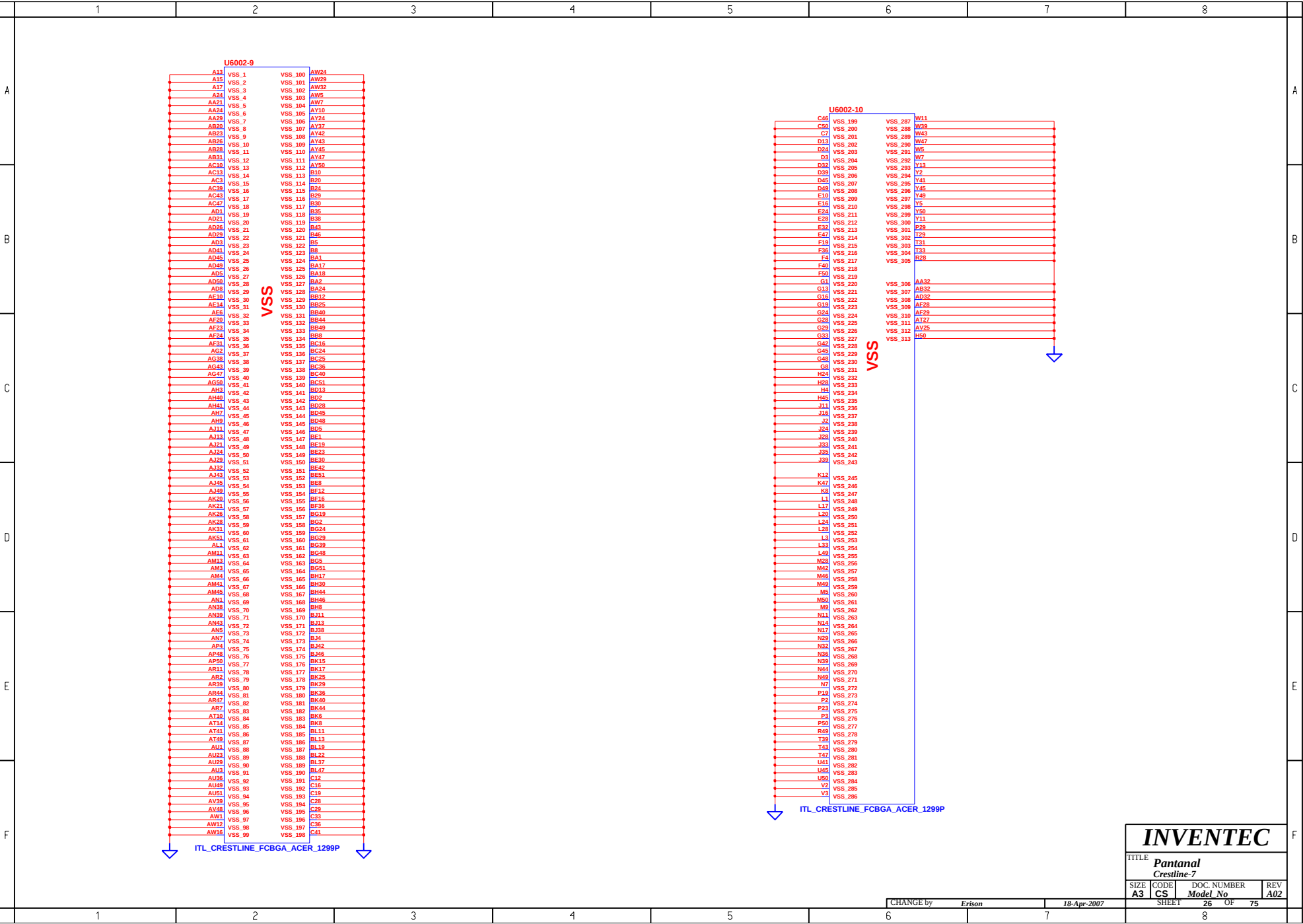


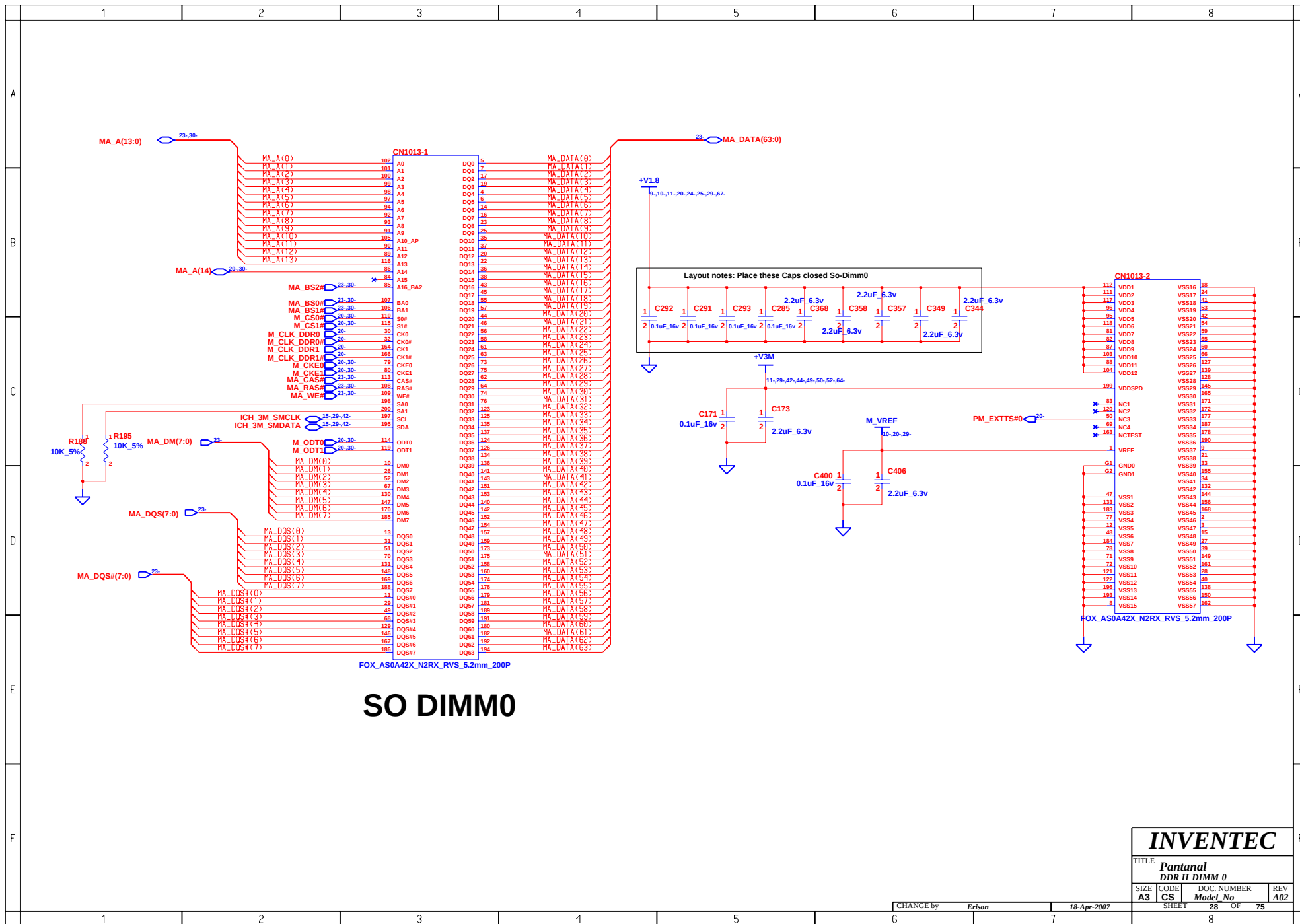


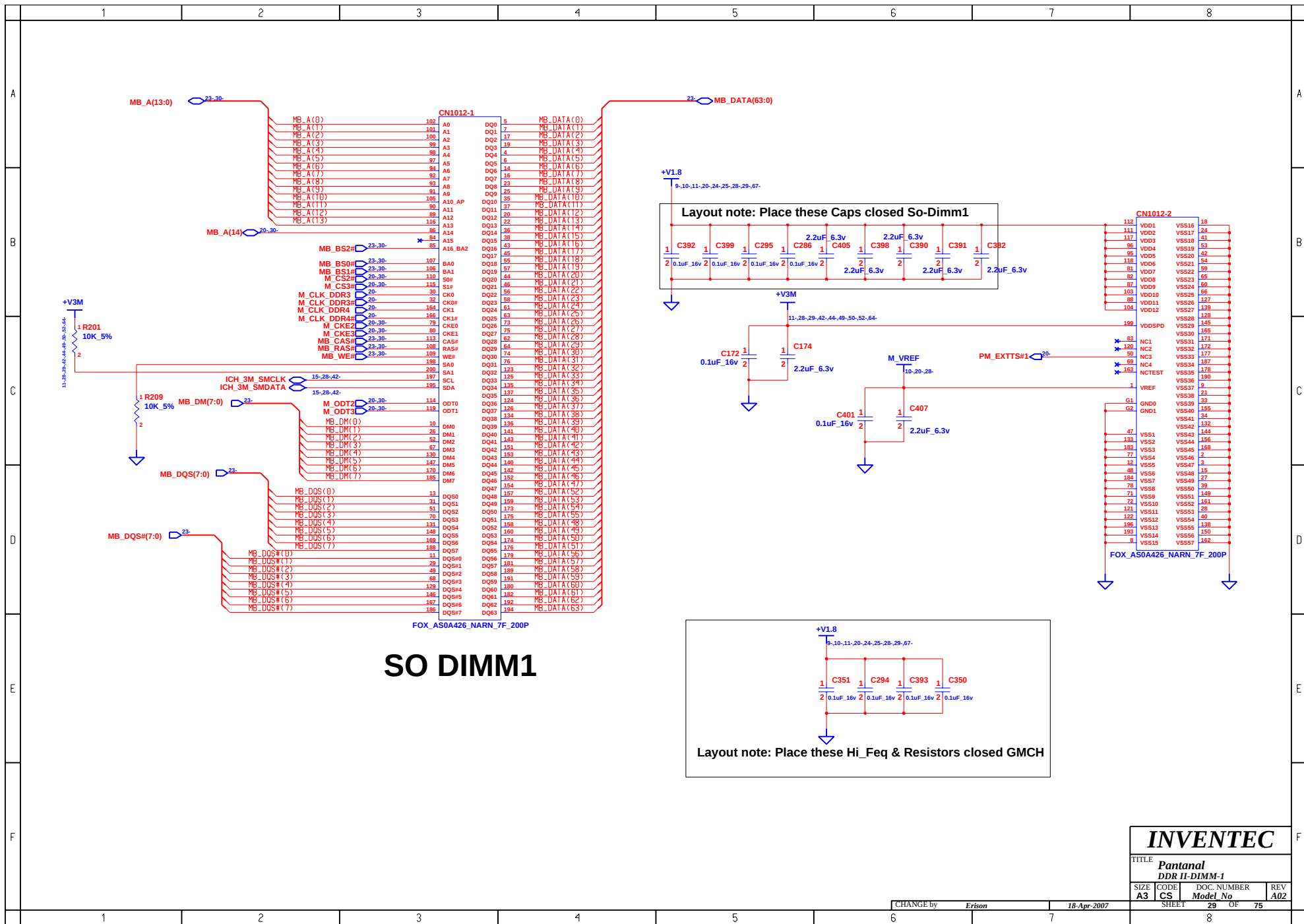












SO DIMM1

Layout note: Place these Hi_Feq & Resistors closed GMCH

+V1.8
9,10,11,20,24,25,28,29,67.

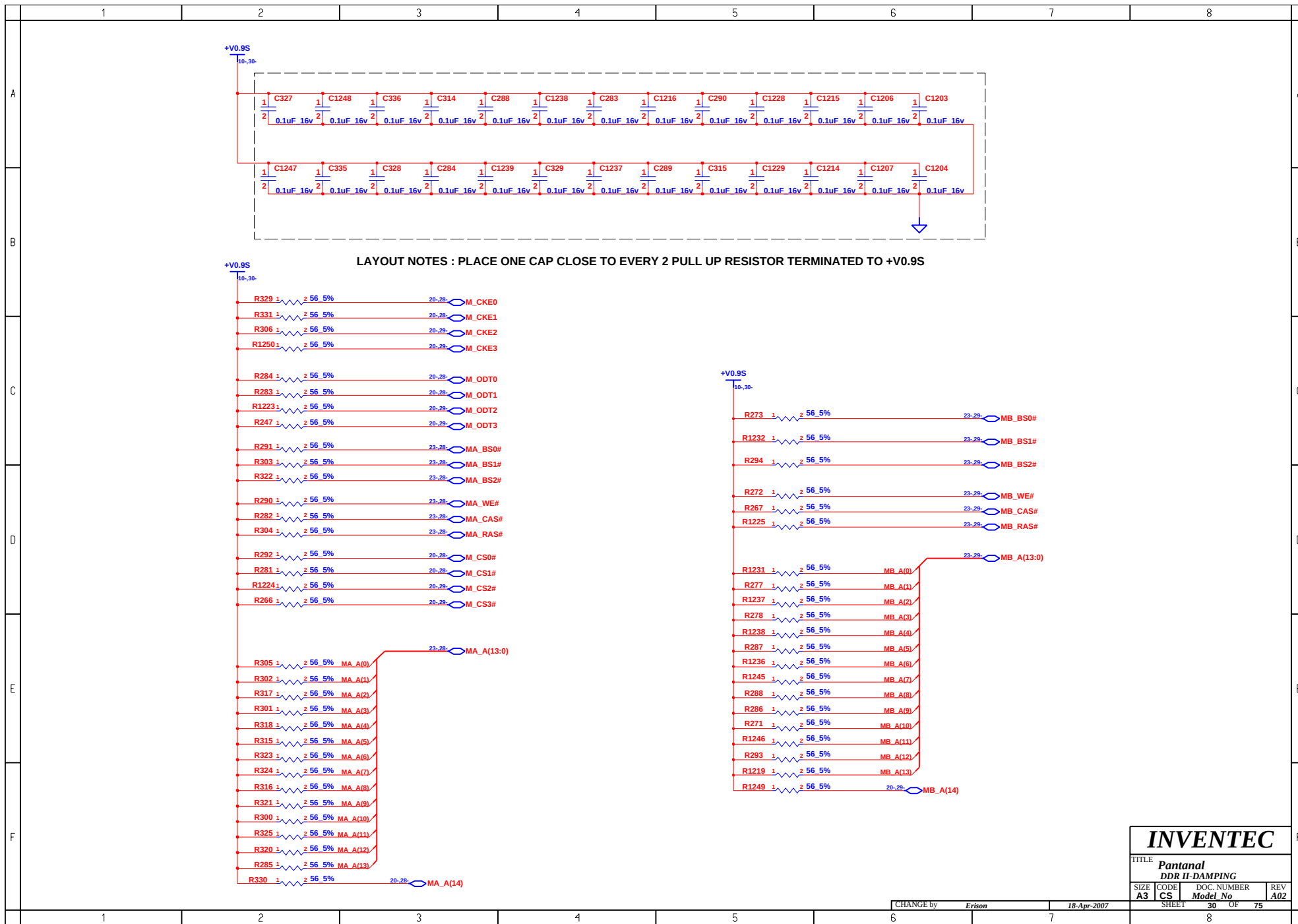
C351 1 2 0.1uF_16v 2
C294 1 2 0.1uF_16v 2
C393 1 2 0.1uF_16v 2
C350 1 2 0.1uF_16v 2

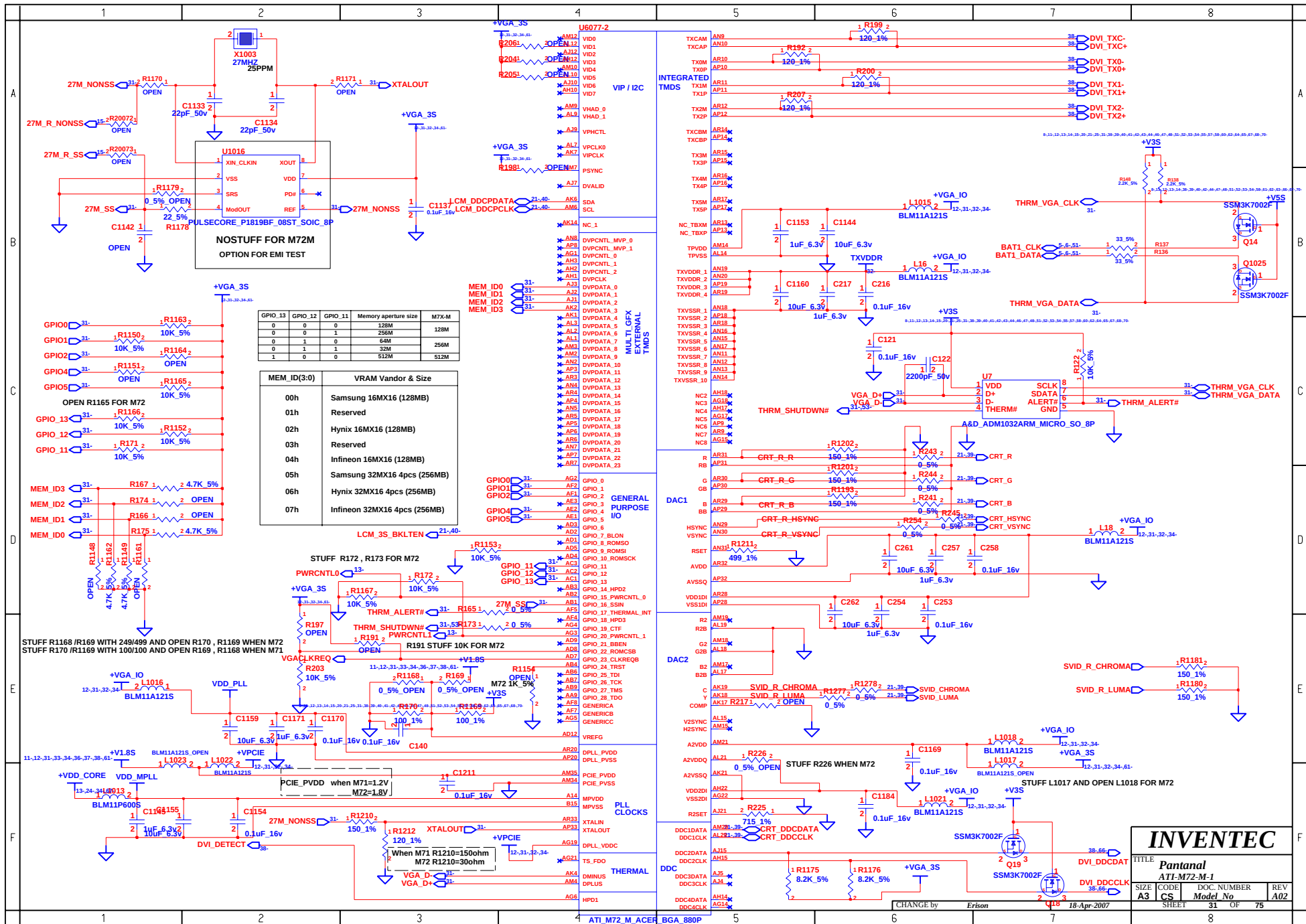
INVENTEC

TITLE			
Pantanal			
DDR II-DIMM-1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

SHEET 29 OF 75





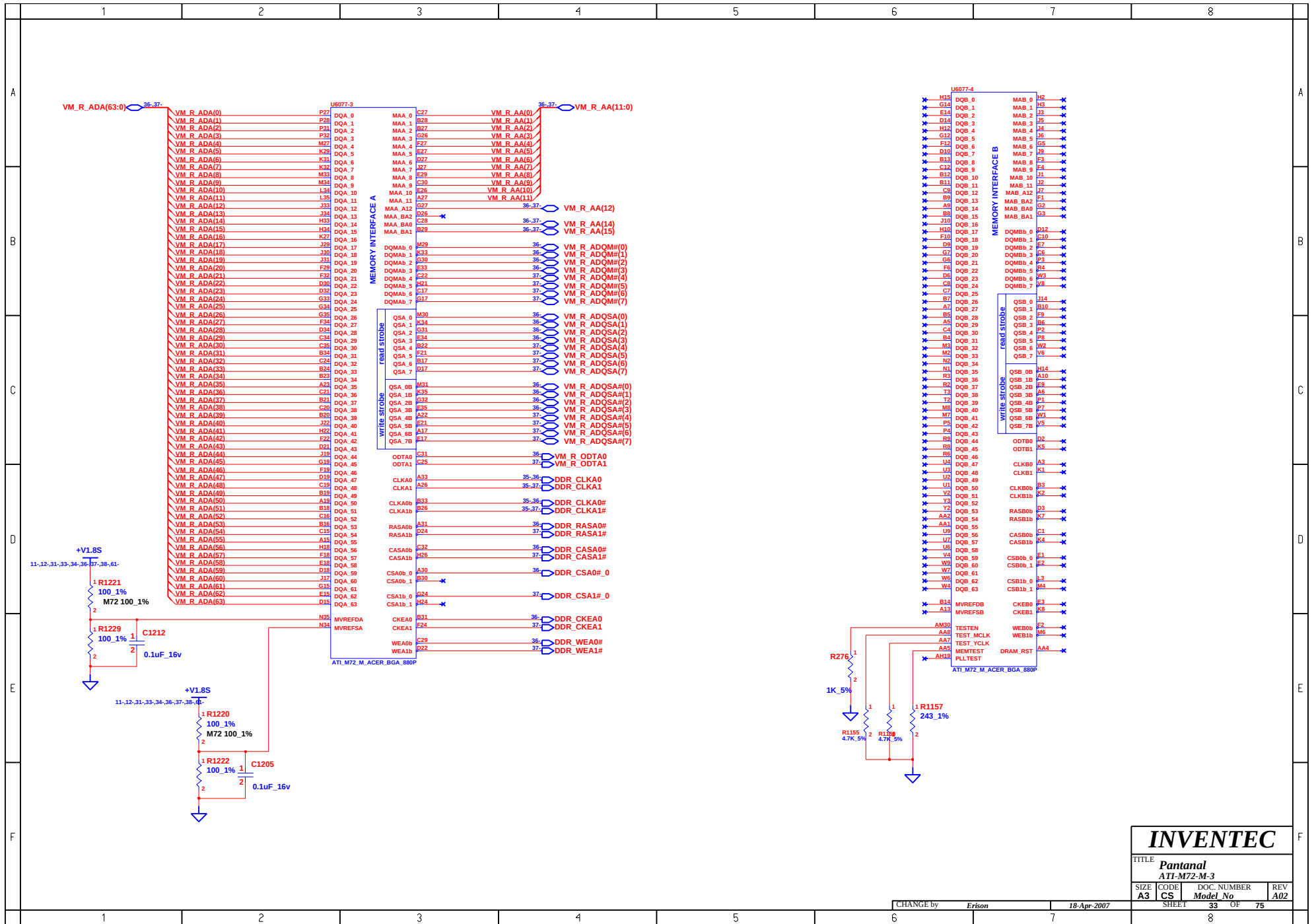
INVENTEC

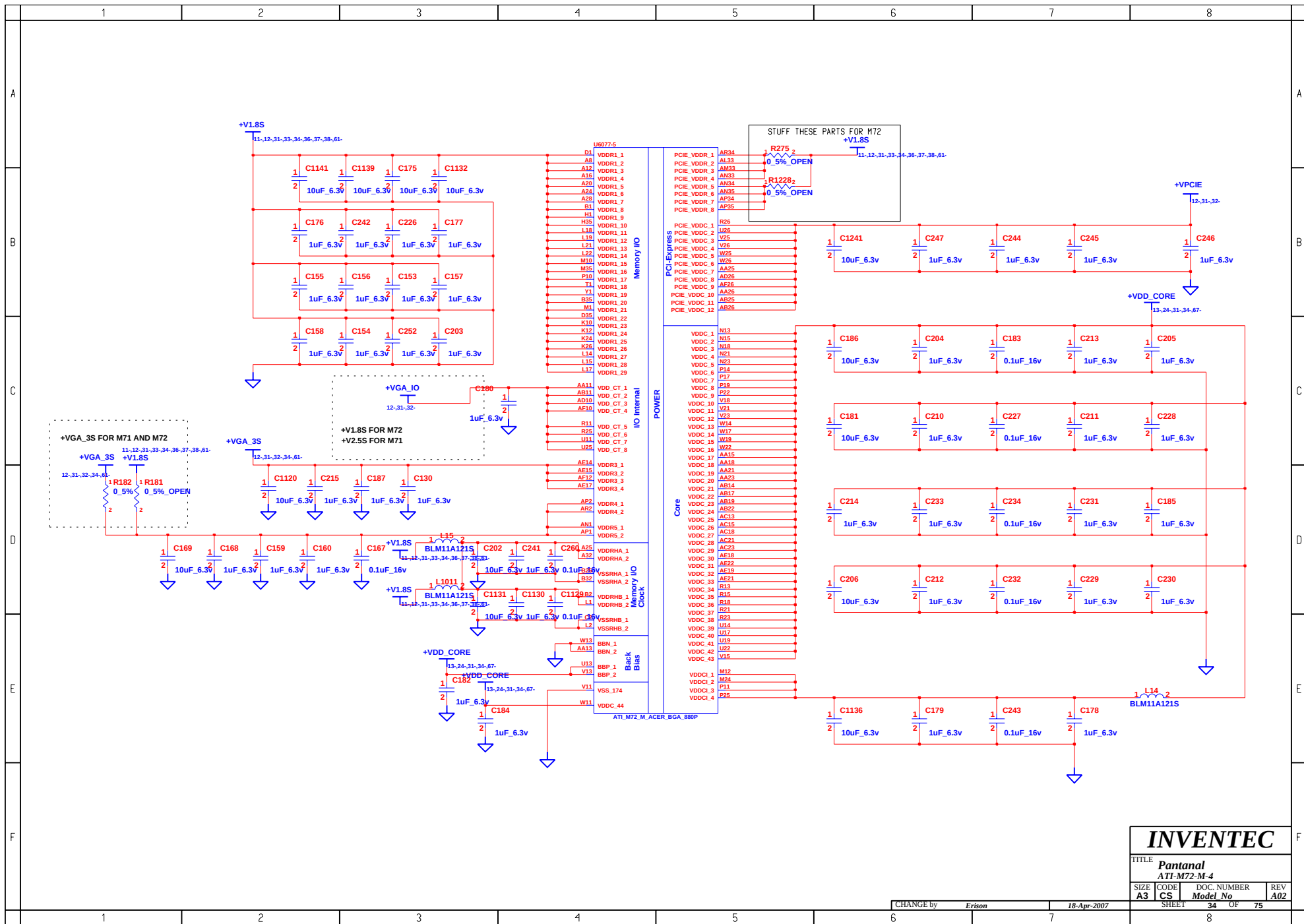
Pantanal
ATI-M72-M-1

SIZE CODE DOC. NUMBER REV
A3 CS Model No

SHEET 1 Of 75

CHANGE by Erikon 18-Apr-2007



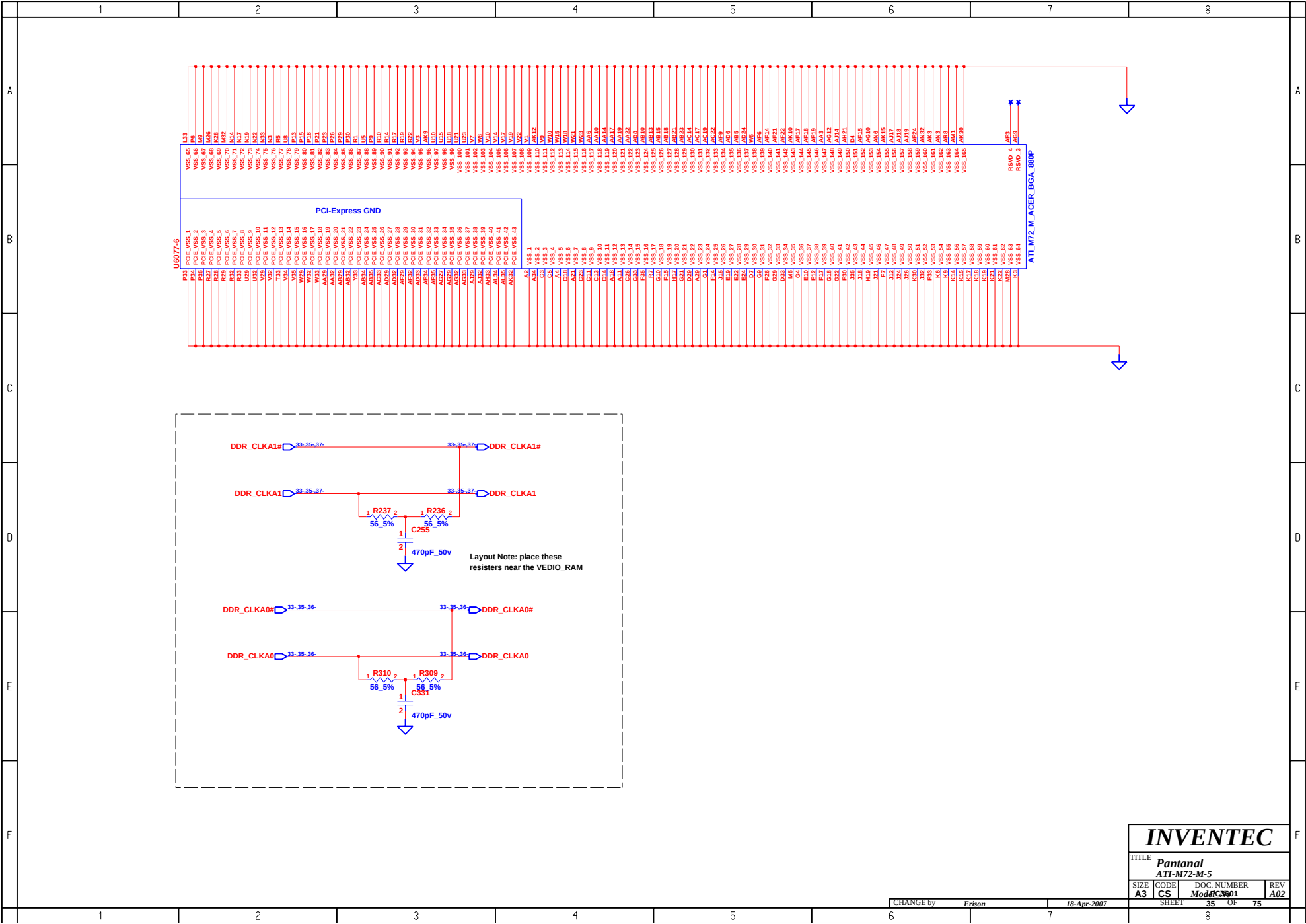


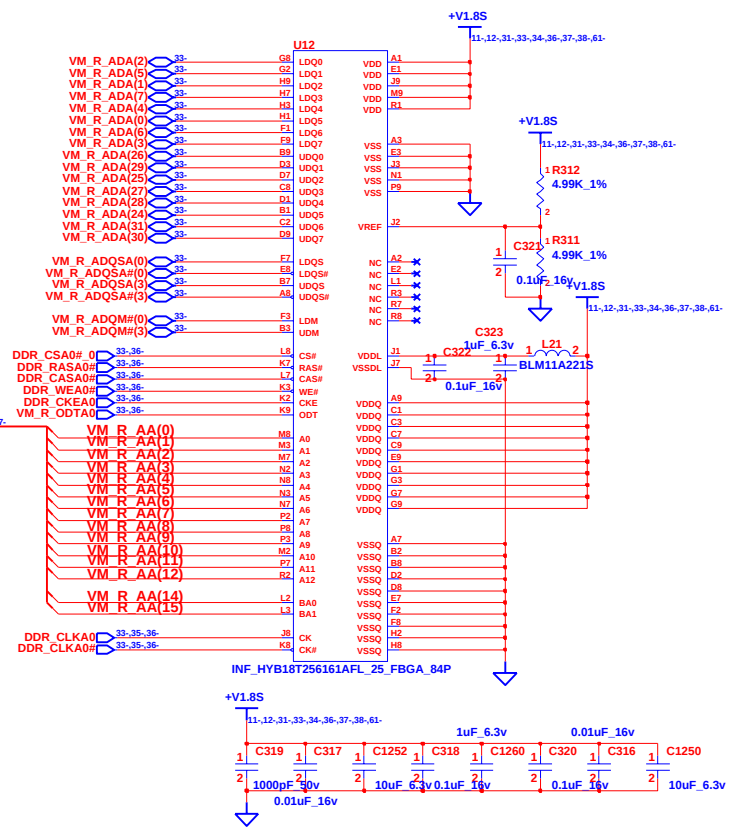
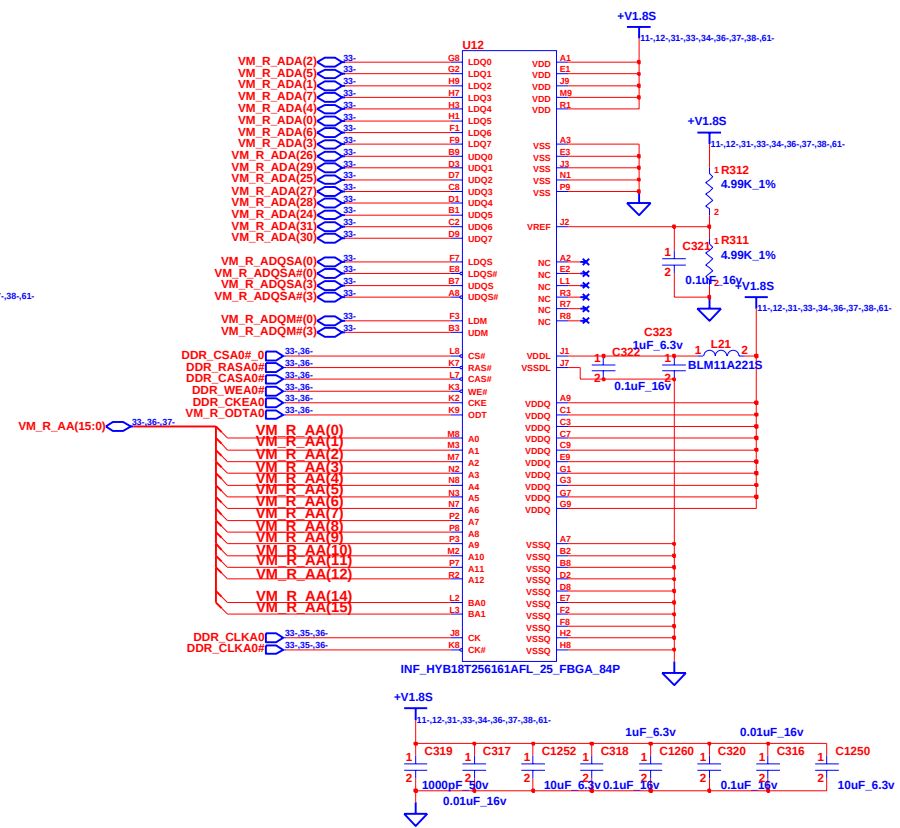
INVENTEC

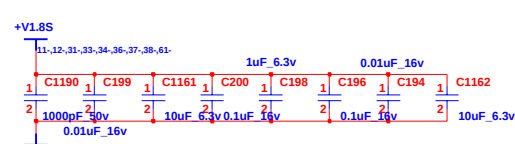
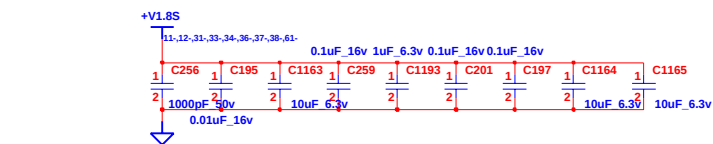
TITLE			
Pantanal			
ATI-M72-M-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

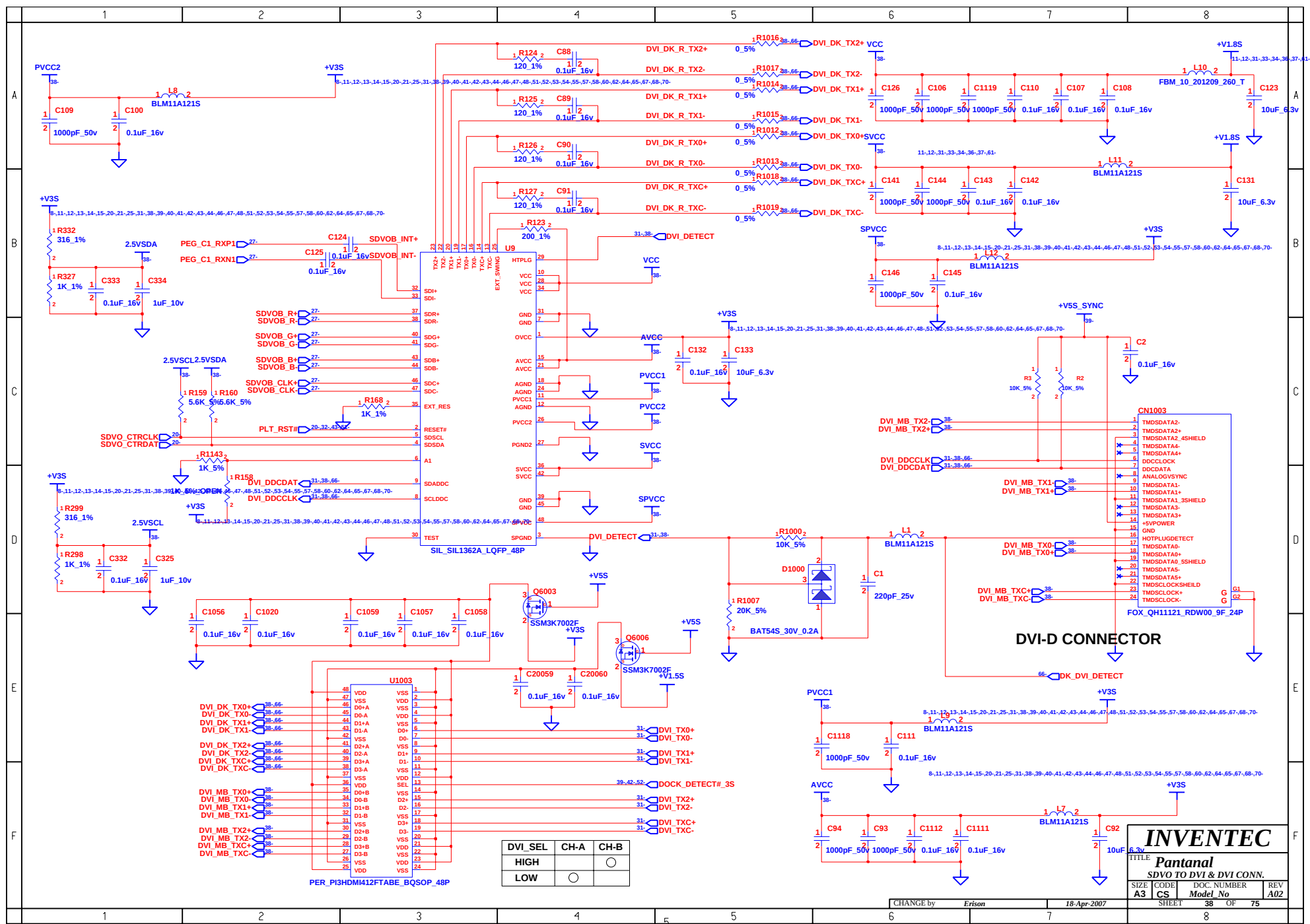
SHEET 34 OF 75

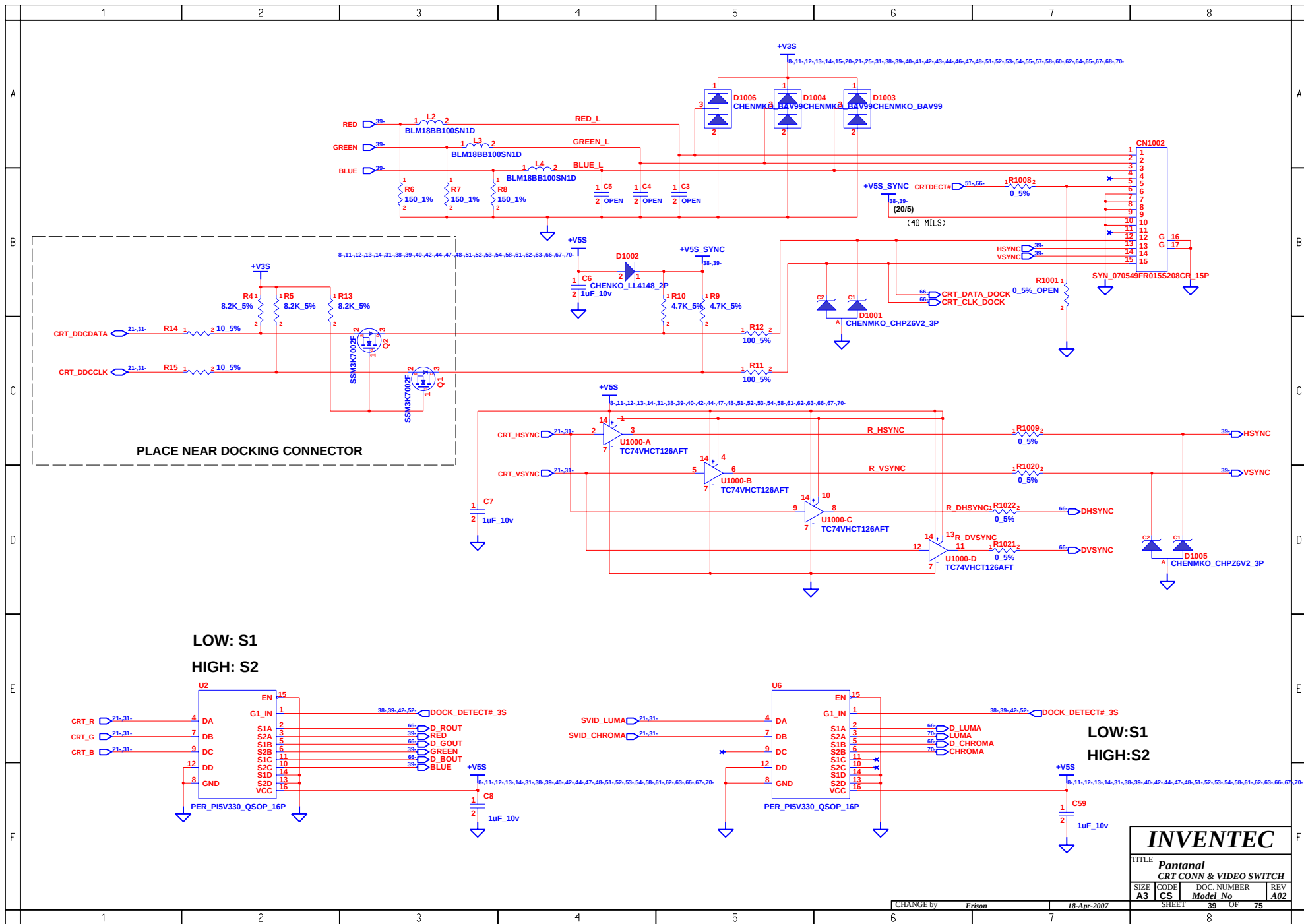






TITLE			
<i>Pantanal</i> VRAM GDDR2-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	A02



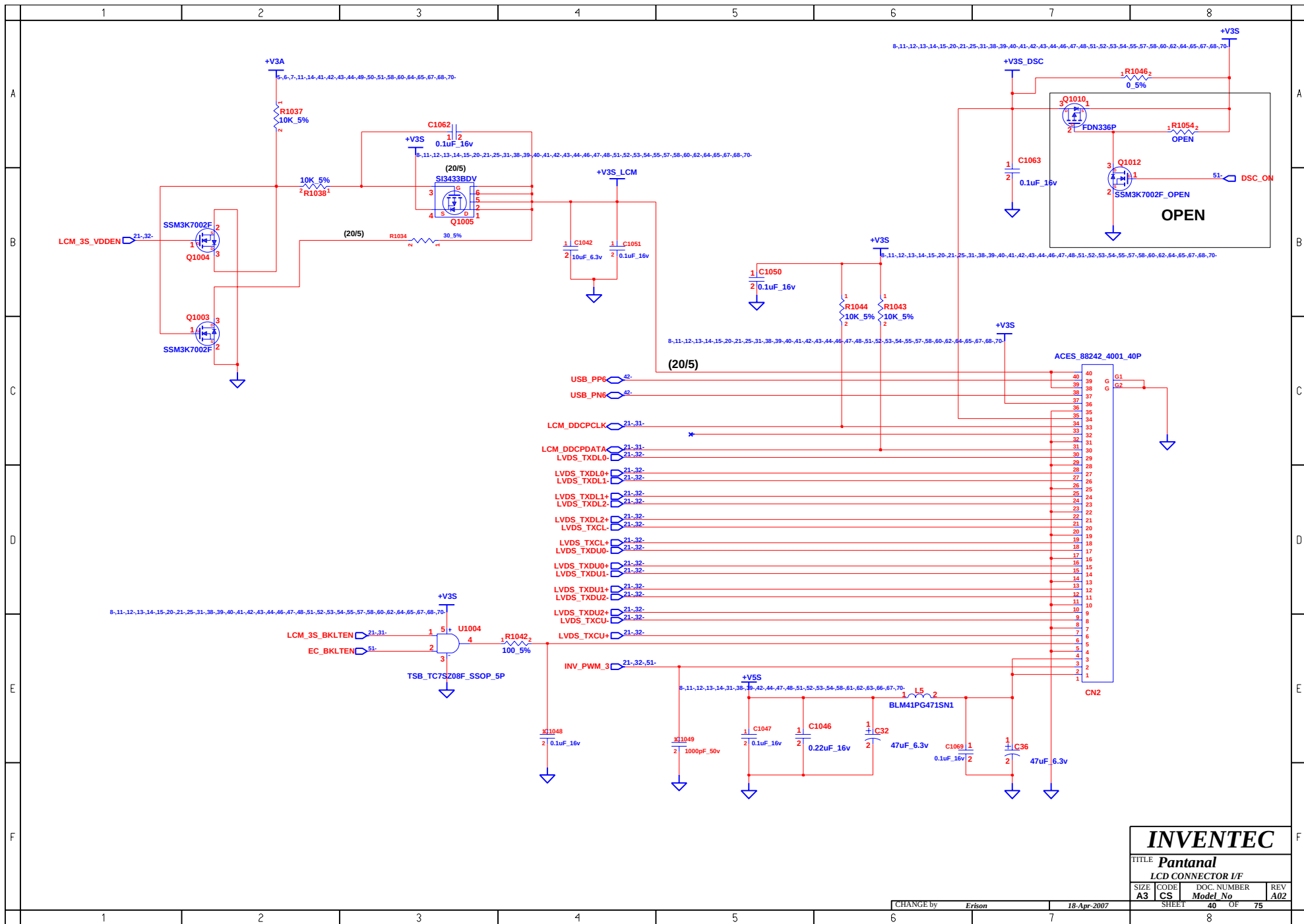


INVENTEC

TITLE			
Pantanal			
CRT CONN & VIDEO SWITCH			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

SHEET 39 OF 75



INVENTEC

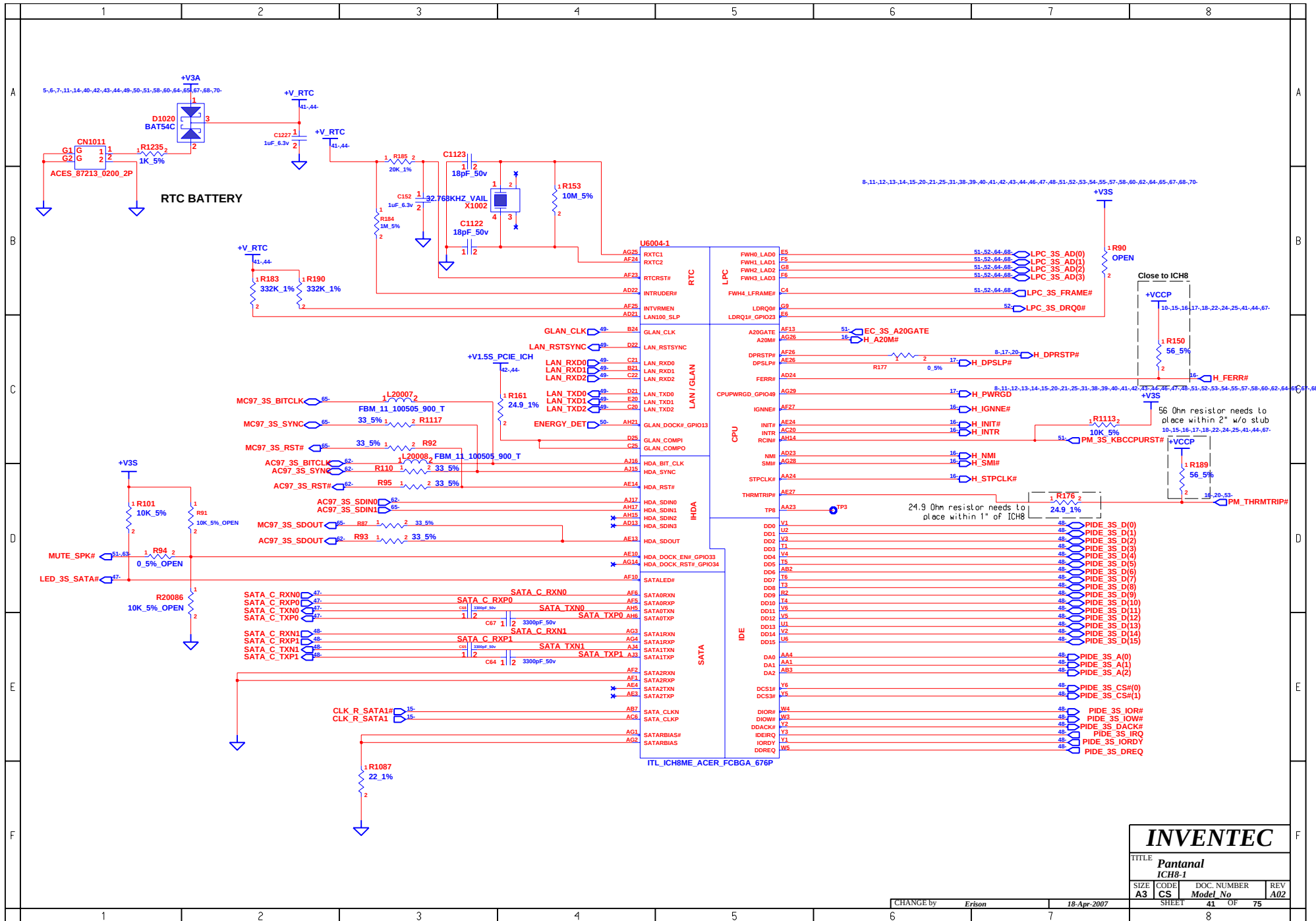
TITLE **Pantanal**

LCD CONNECTOR I/F

SIZE CODE CS DOC. NUMBER Model No. REV A02

CHANGE by Erison 18-Apr-2007

SHEET 40 OF 75

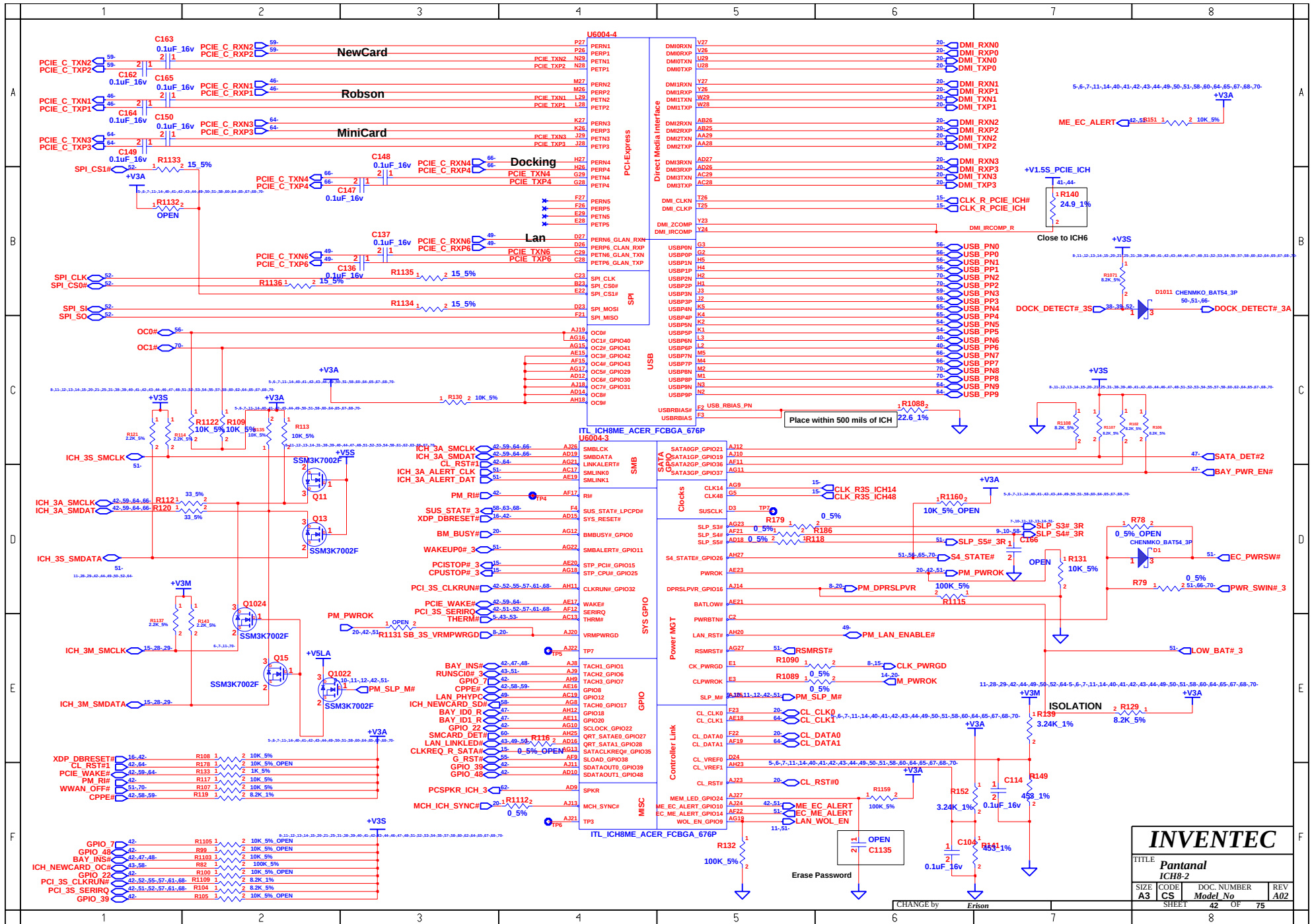


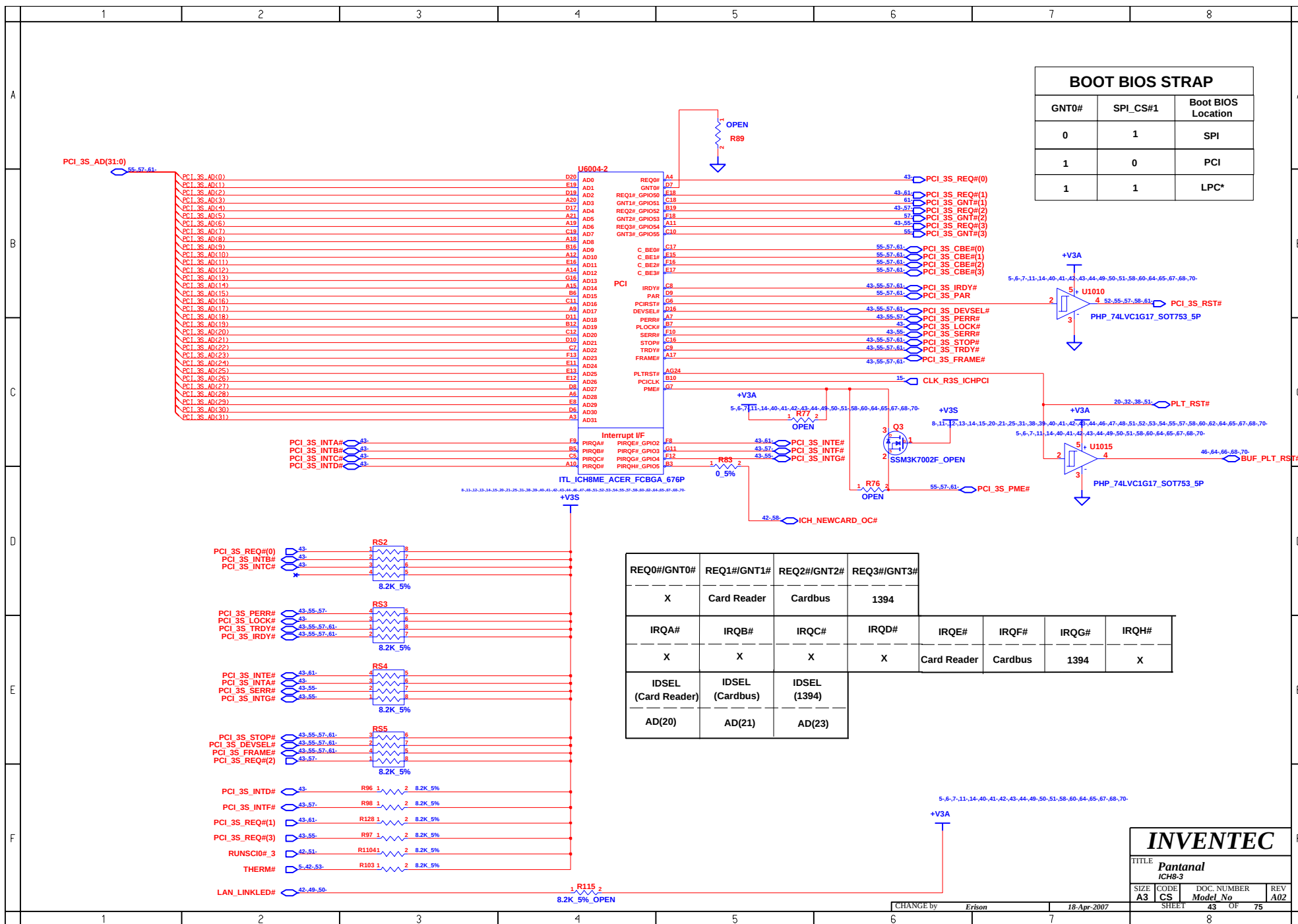
INVENTEC

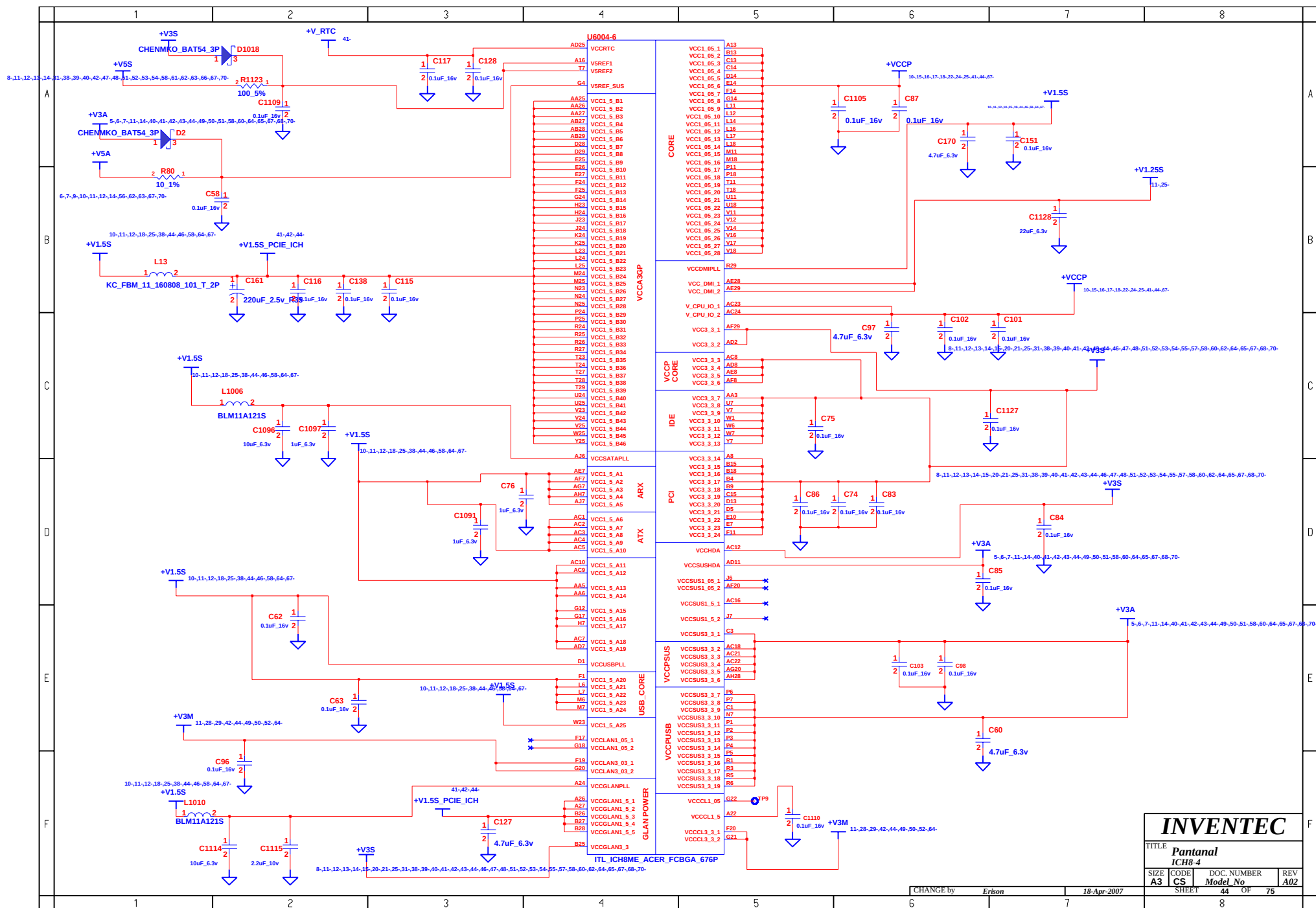
TITLE			
Pantanal			
ICH8-1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

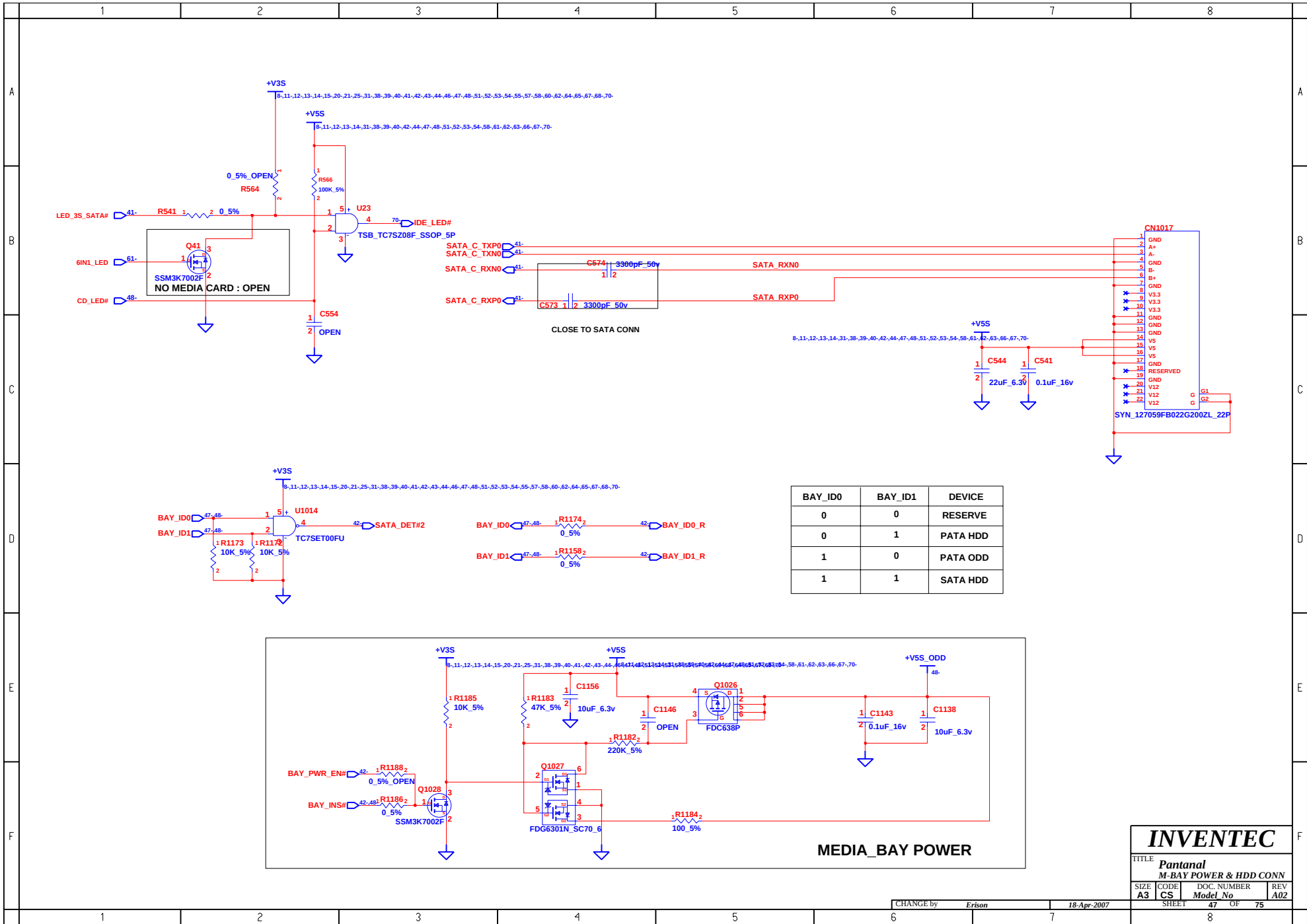
CHANGE by Erison 18-Apr-2007

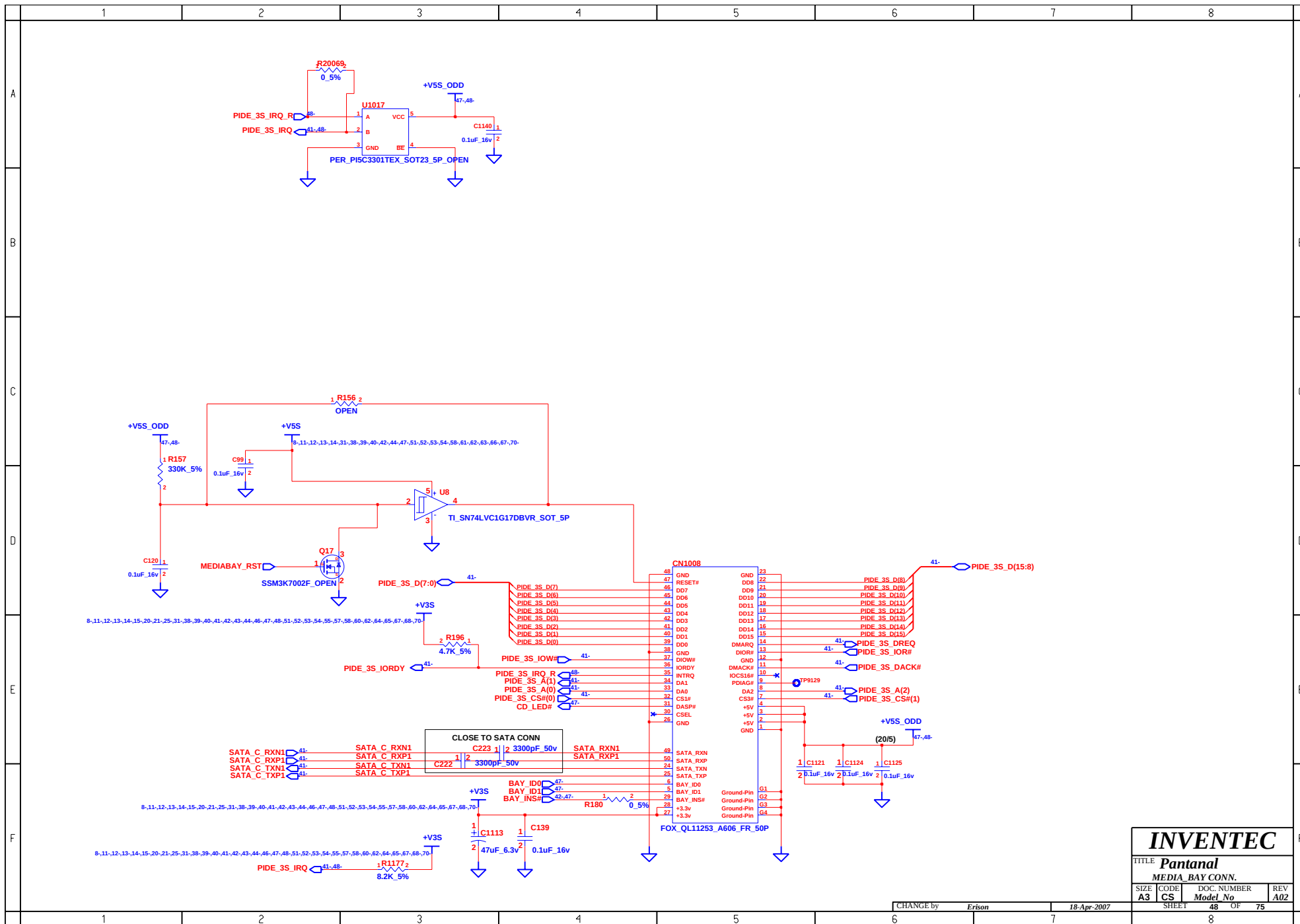
SHEET 41 OF 75



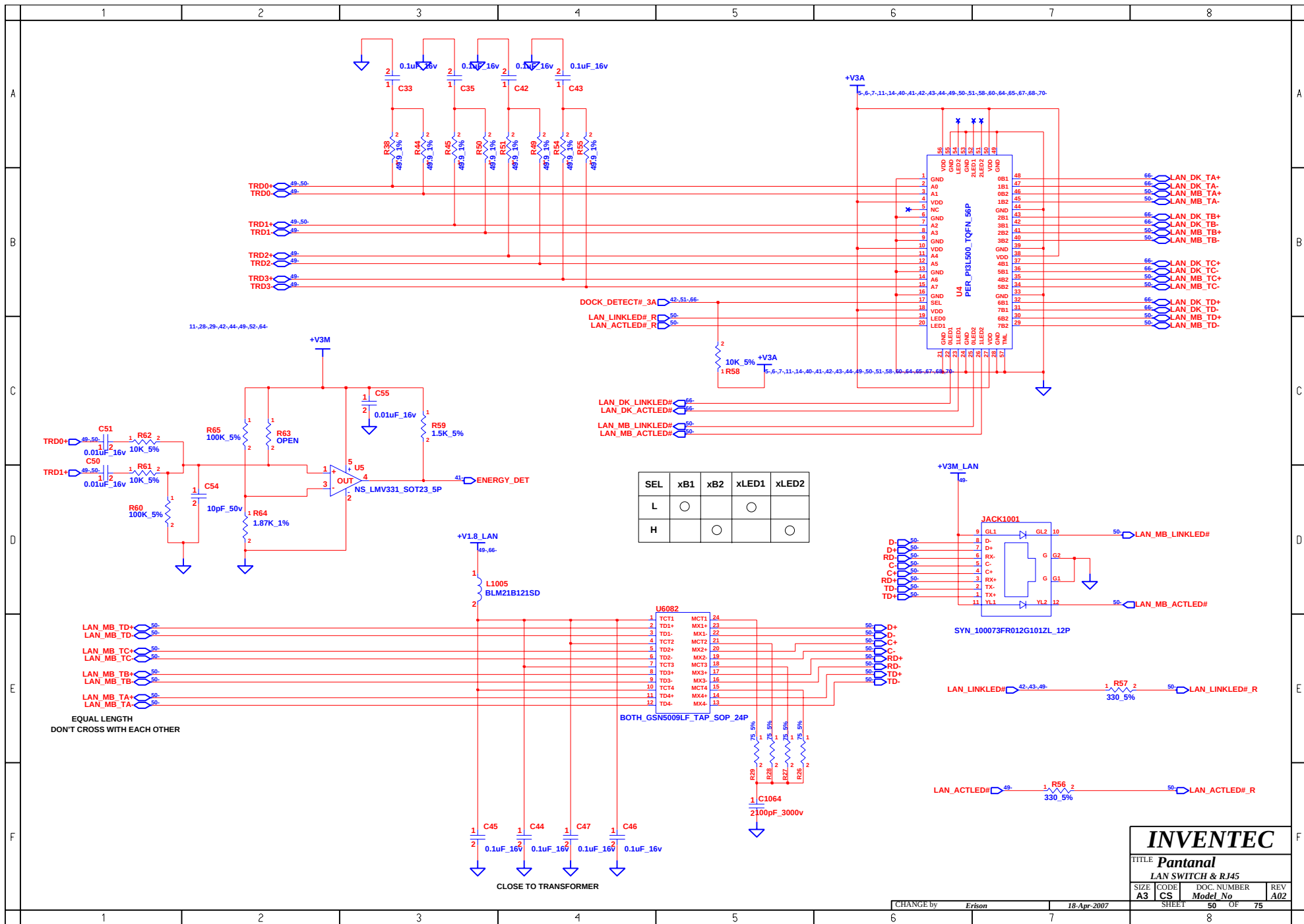


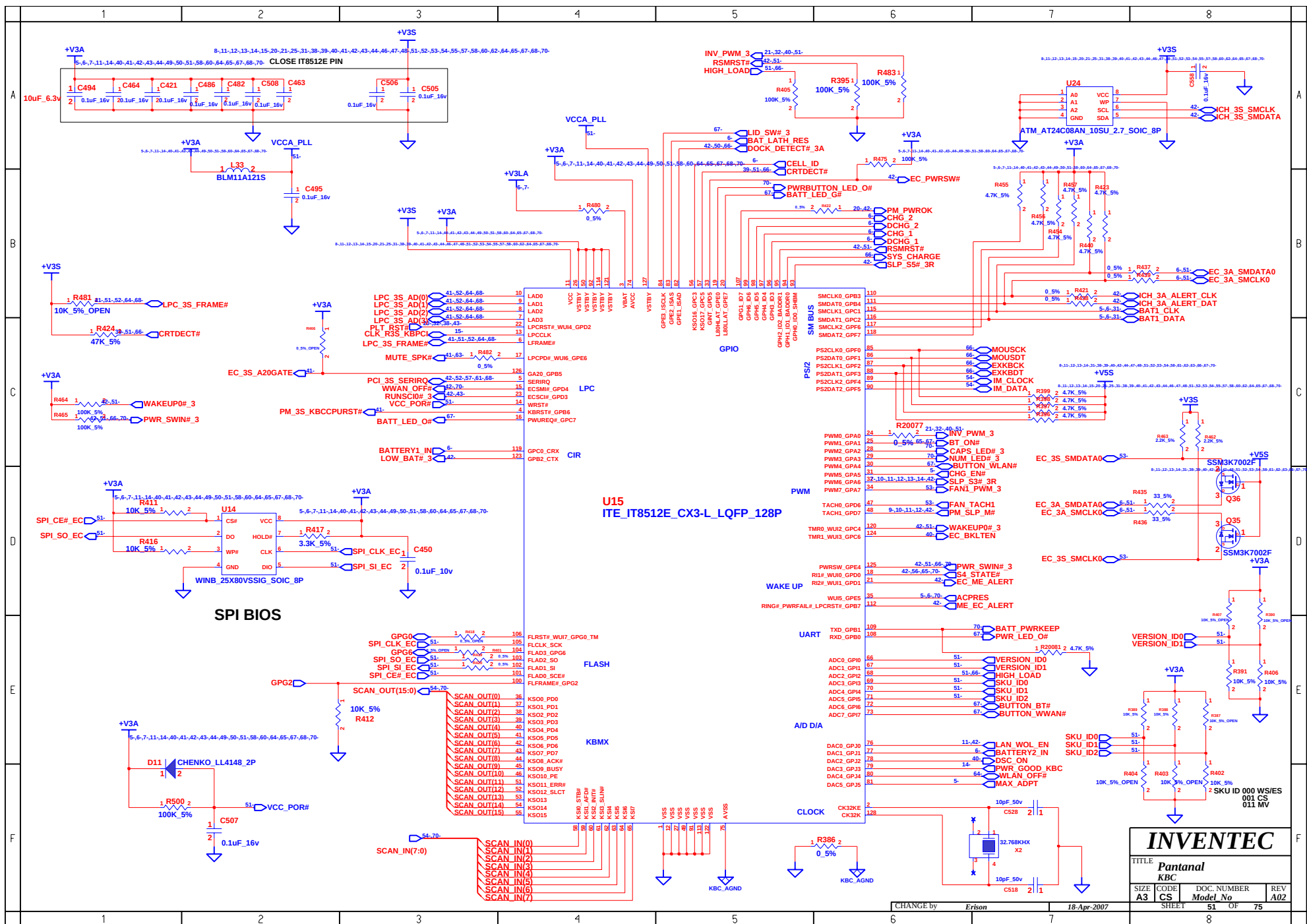


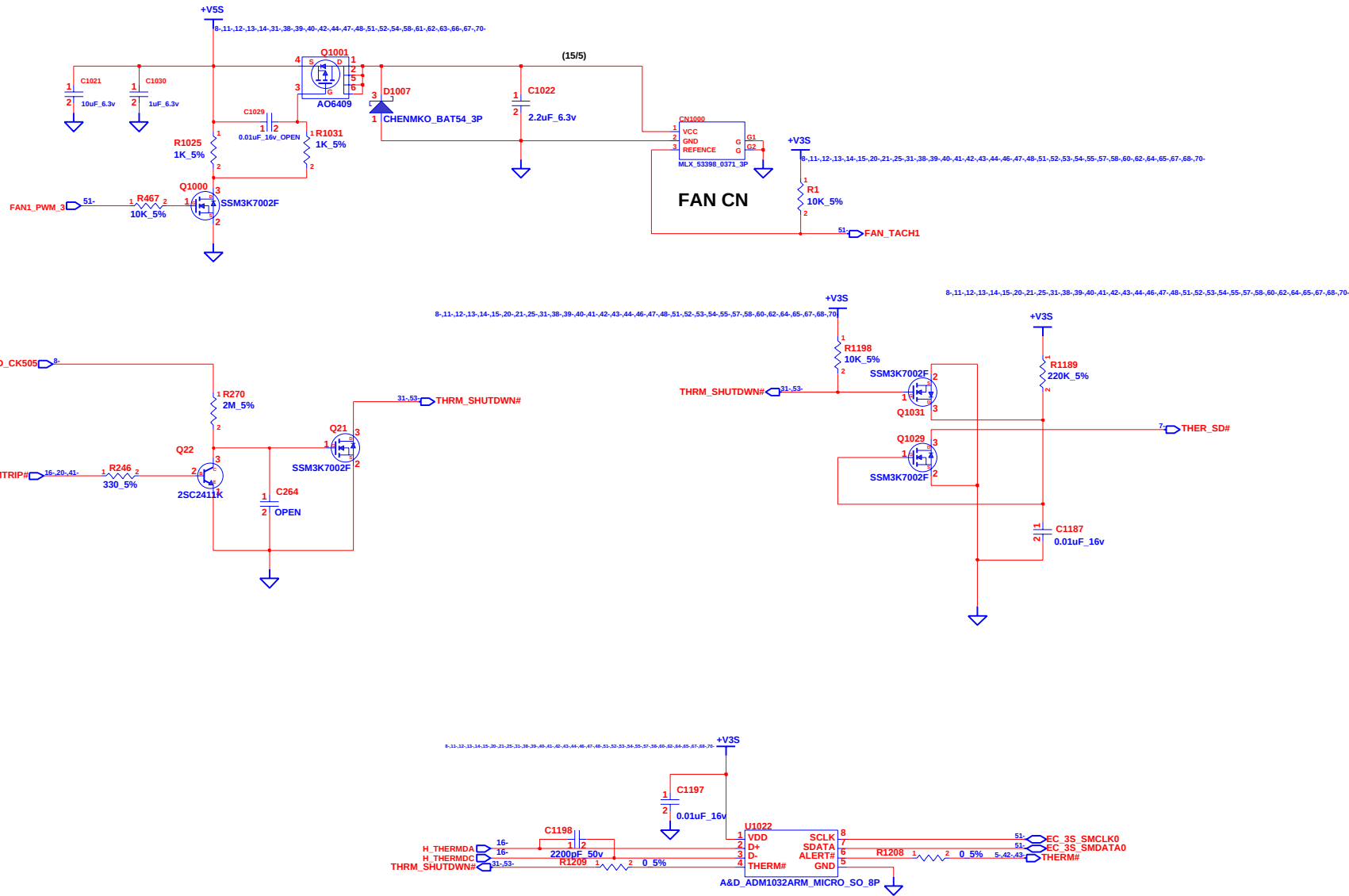






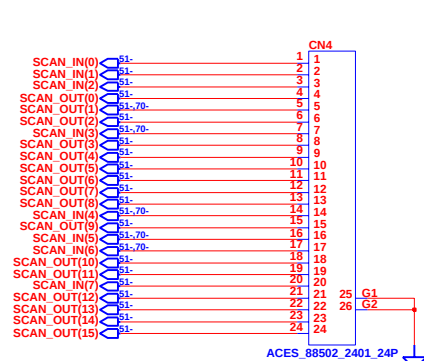




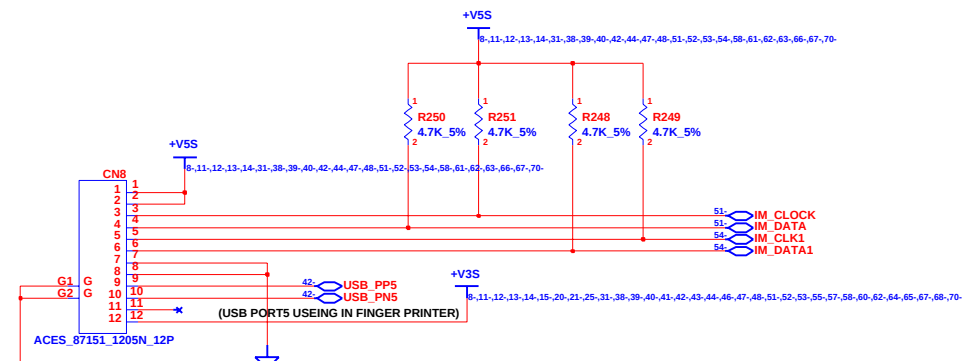


THERMAL SENSOR FOR CPU.

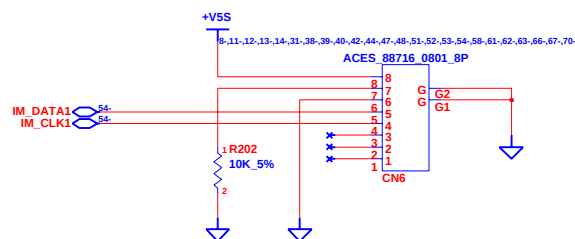
INVENTEC			
TITLE Pantanal			
THERMAL SENSOR & FAN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02
CHANGE by Erison		18-Apr-2007	
SHEET 53		OF 75	



KEYBOARD CONNECTOR



TOUCHPAD CONNECTOR

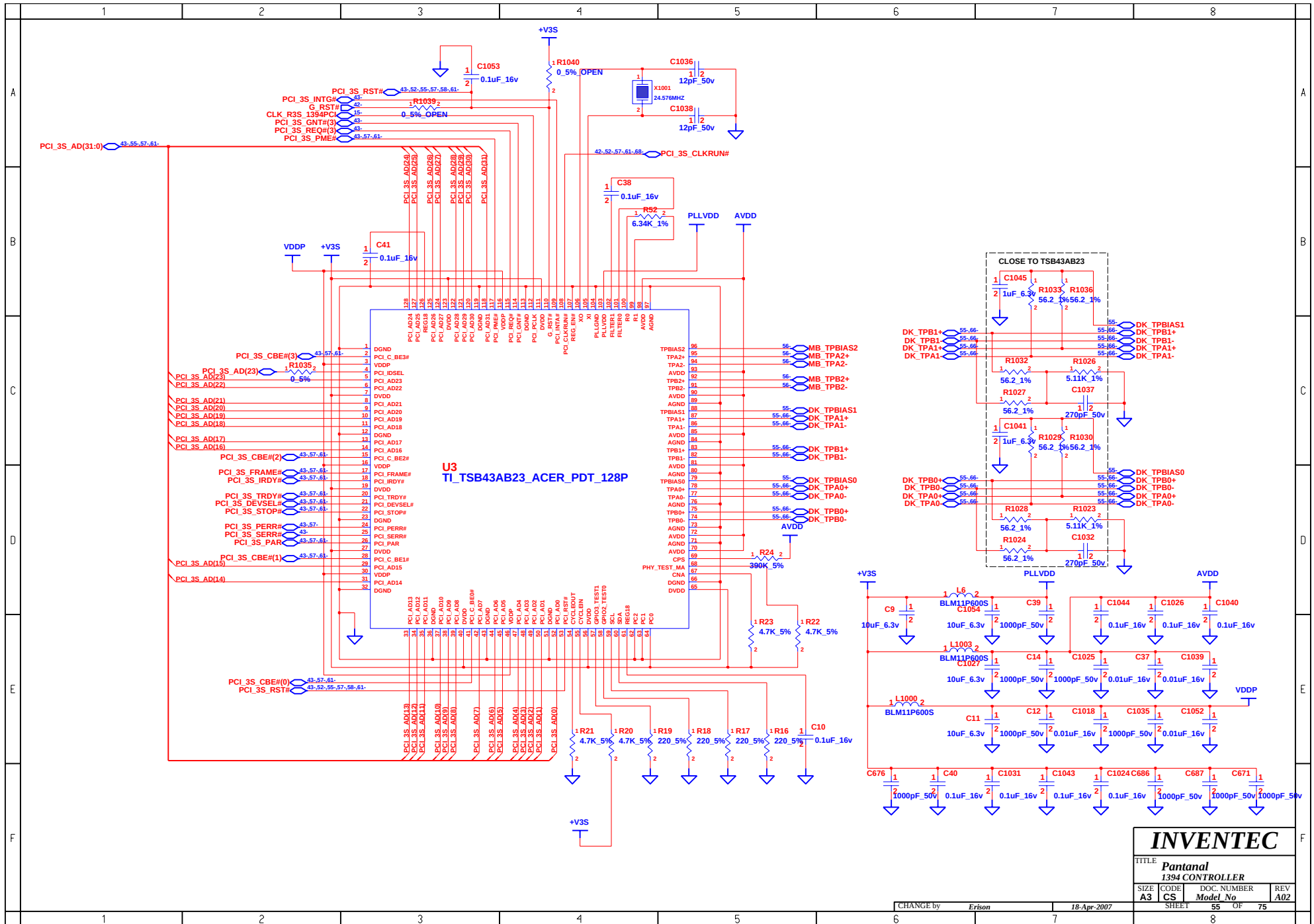


TRACK POINT CONNECTOR

INVENTEC			
TITLE Pantanal			
KB, TP & SP I/F			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by *Erison* 18-Apr-2007

SHEET 54 OF 75

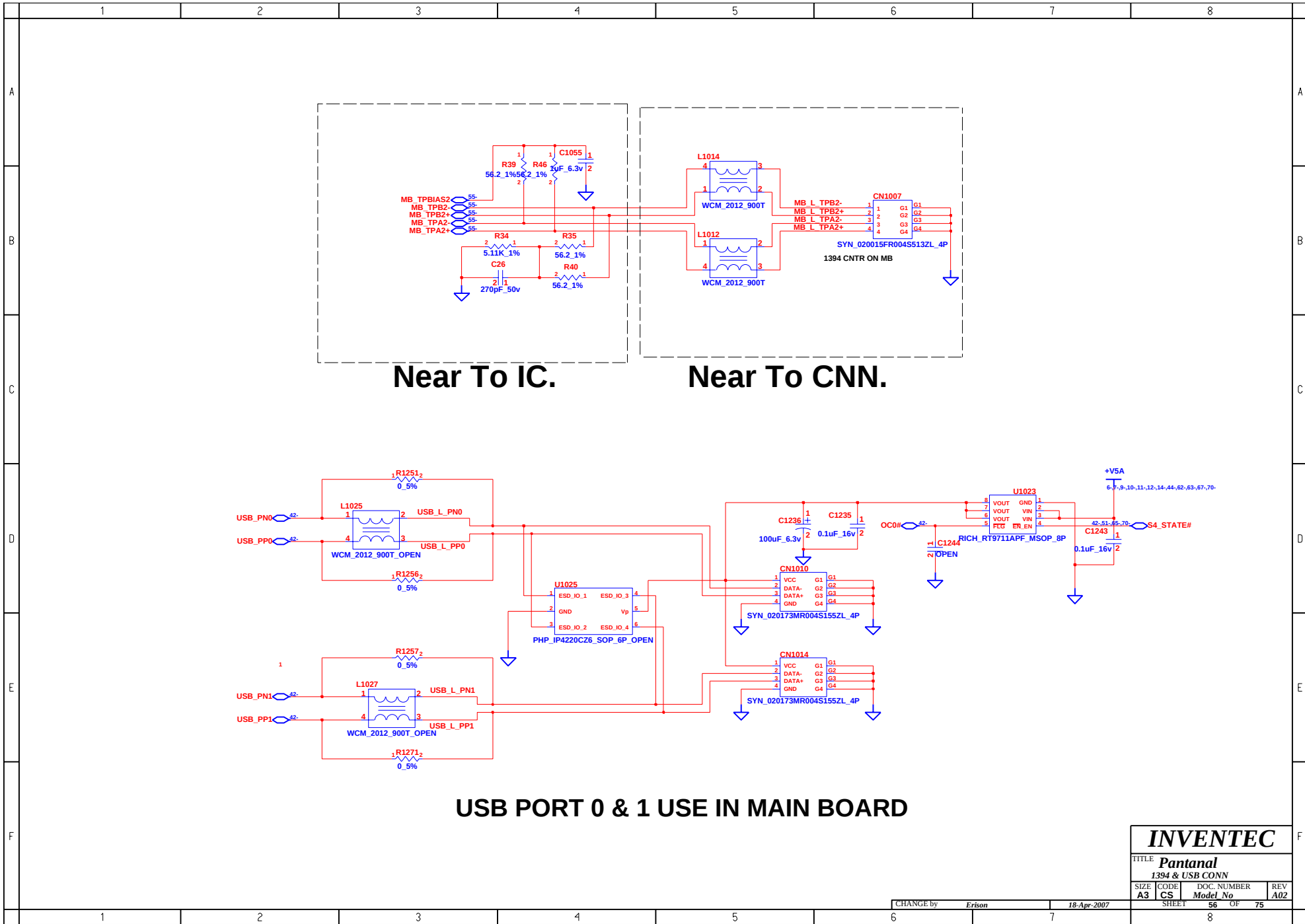


INVENTEC

TITLE			
Pantanal			
1394 CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

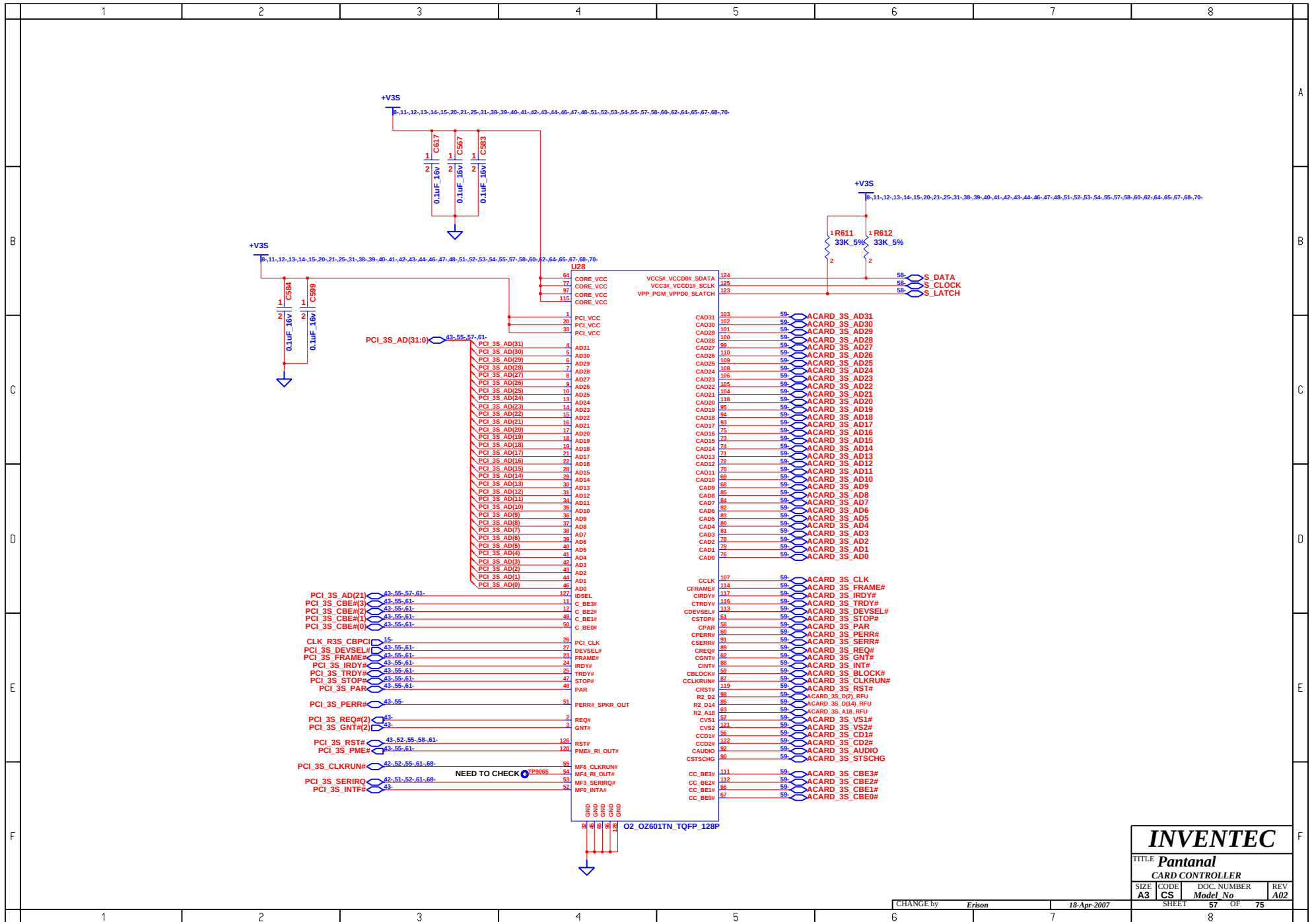
SHEET 55 OF 75



INVENTEC

TITLE Pantanal			
1394 & USB CONN			
SIZE A3	CODE CS	DOC. NUMBER	REV A02
Model No		SHEET 56 OF 75	

CHANGE by *Erison* 18-Apr-2007

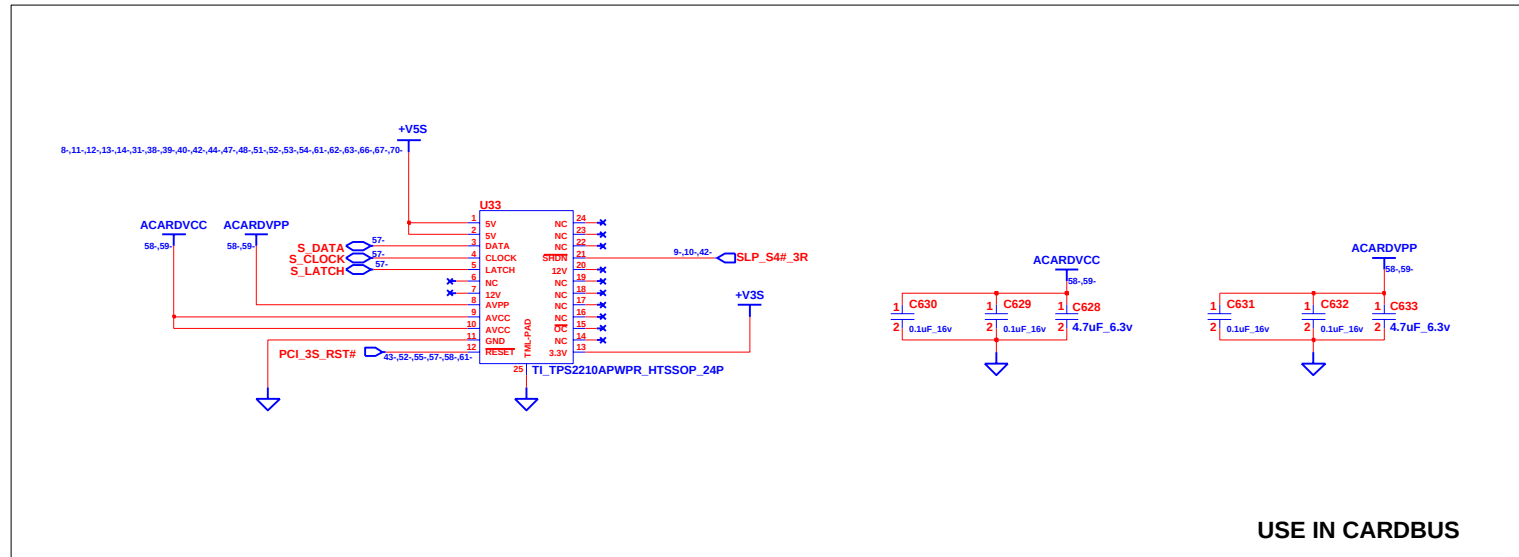
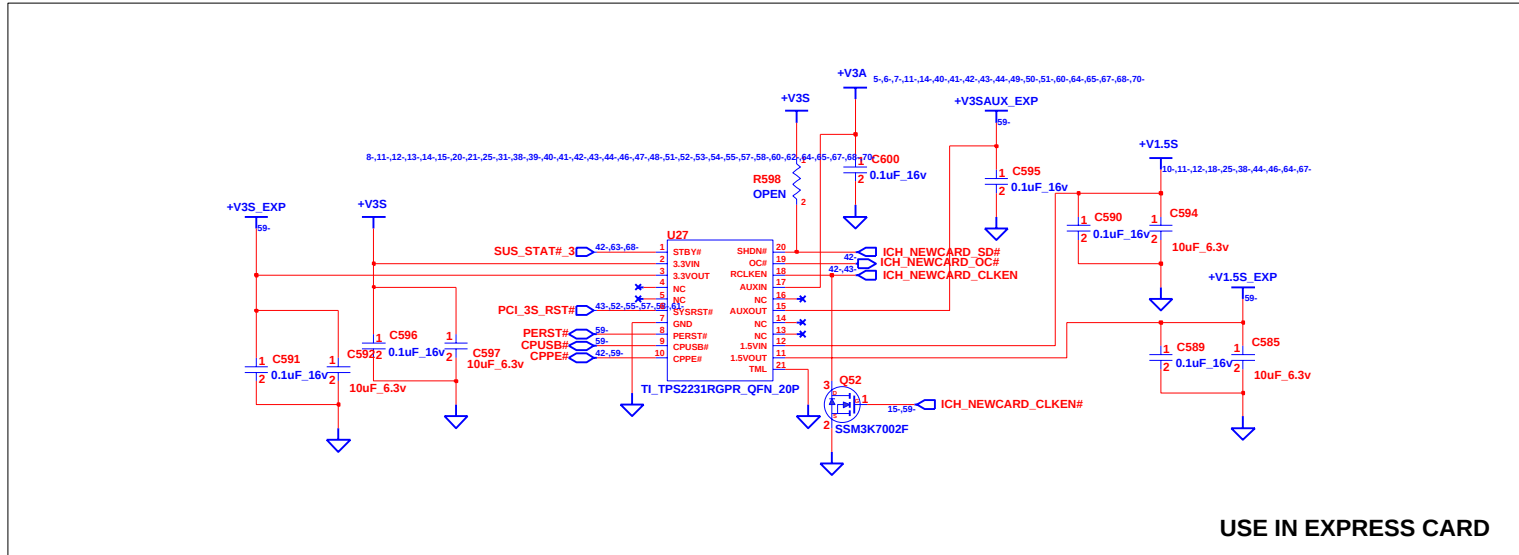


INVENTEC

TITLE Pantanal
CARD CONTROLLER

SIZE A3 CODE CS DOC. NUMBER Model No REV A02

SHEET 57 OF 75

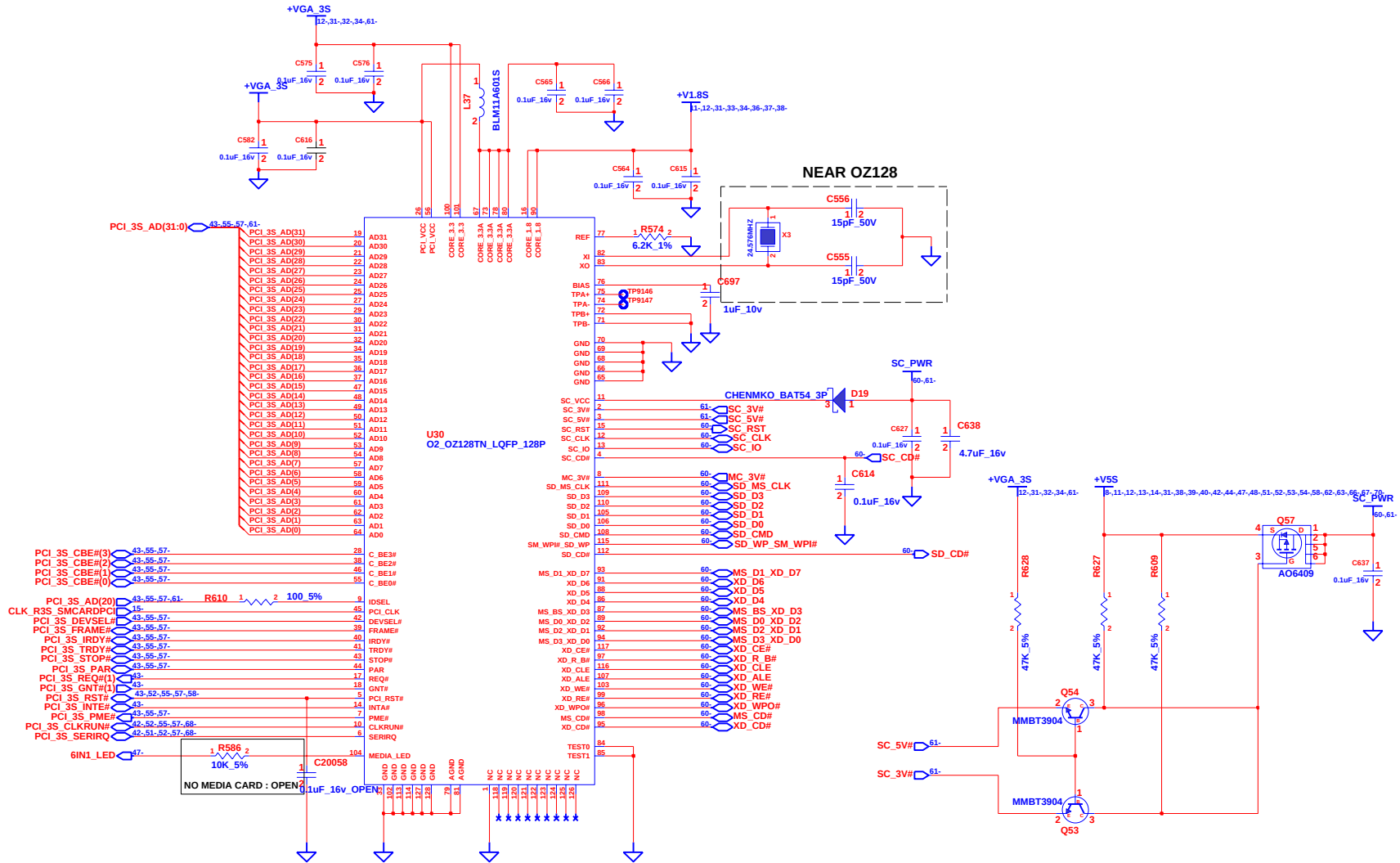


INVENTEC			
TITLE Pantanal			
PC CARD & EXPRESS CARD POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by *Erison* 18-Apr-2007

SHEET 58 OF 75

NOTE: +V1.8S NEED POWER ON FASTER THAN +VGA_3S

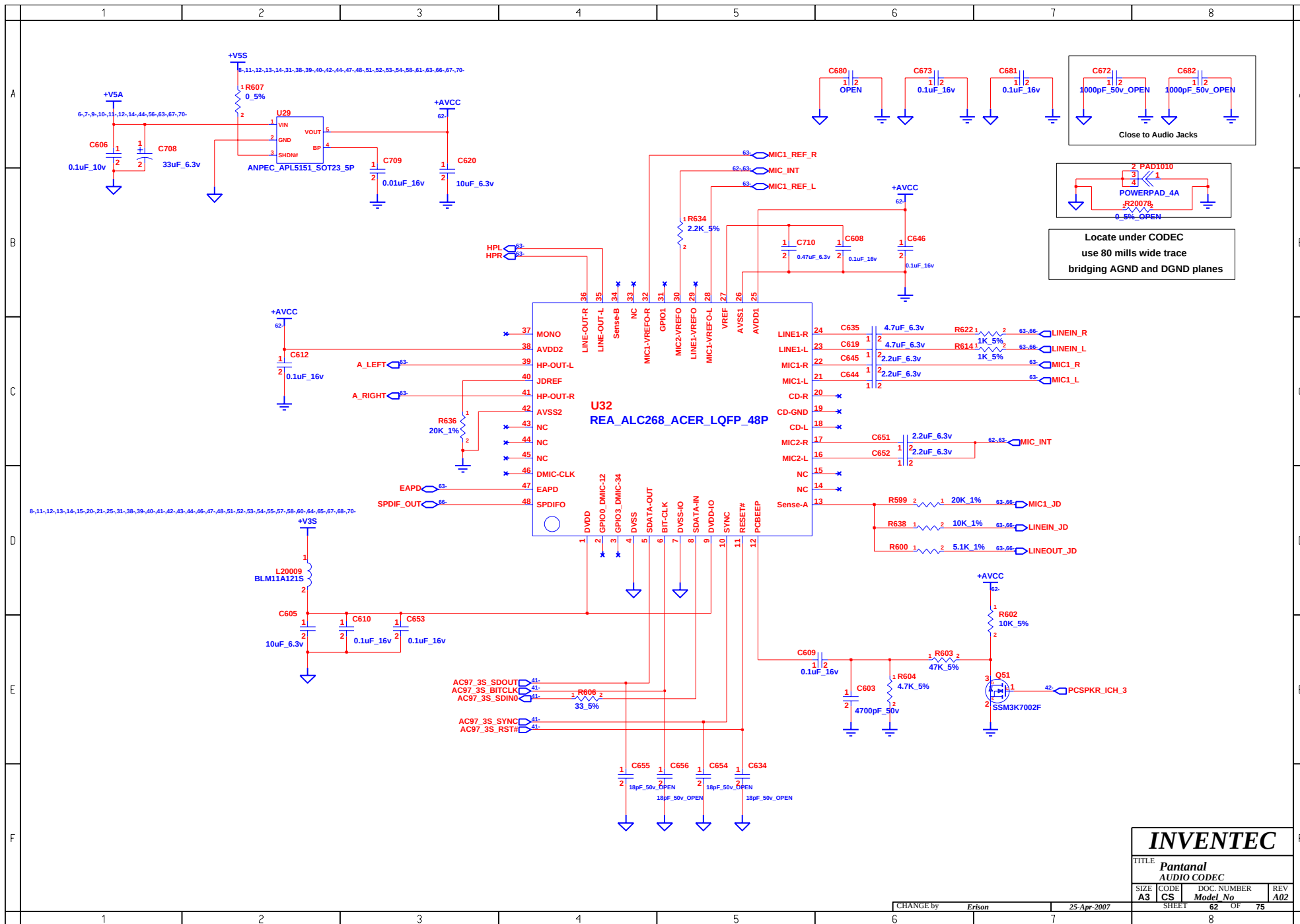


INVENTEC

TITLE			
Pantanal SMART CARD IC & SC POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 18-Apr-2007

SHEET 61 OF 75

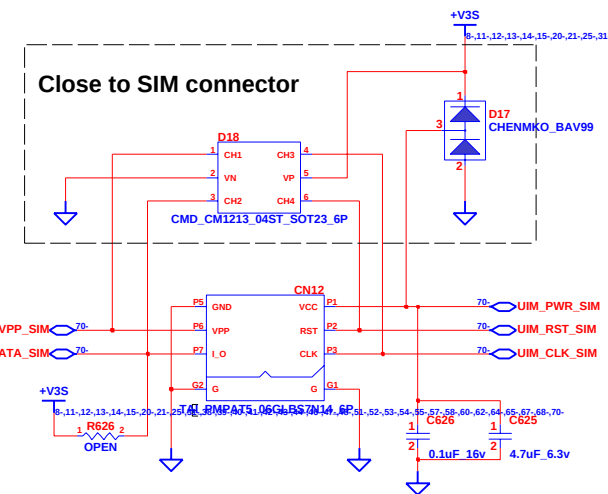
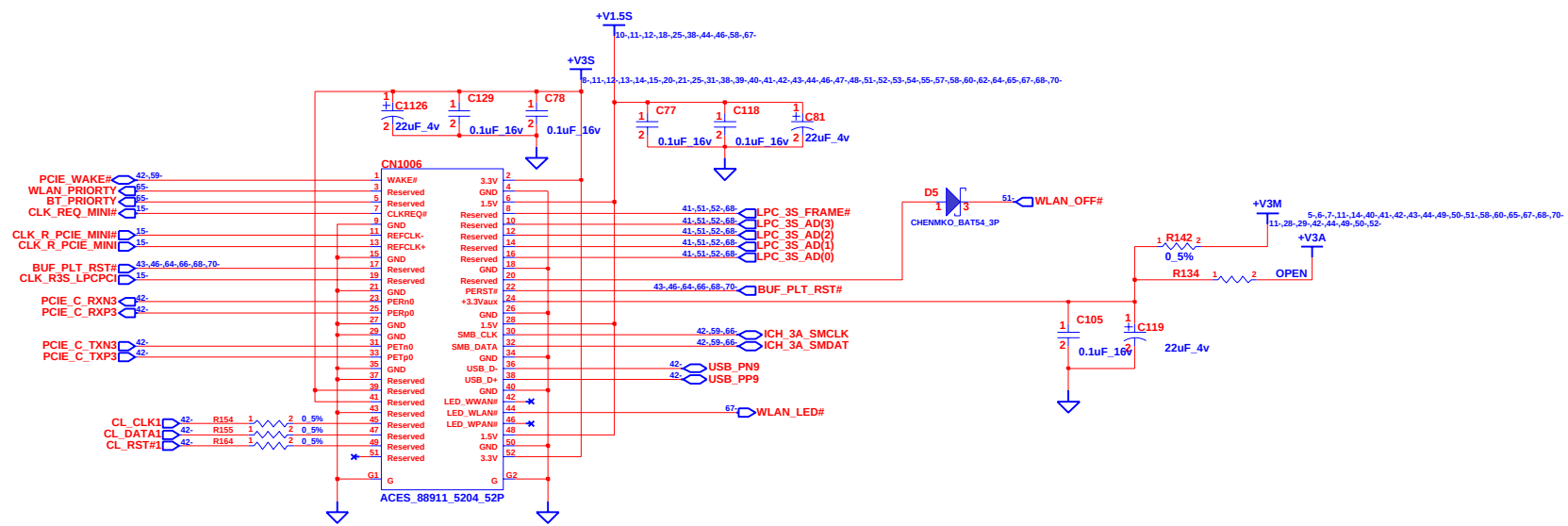


INVENTEC

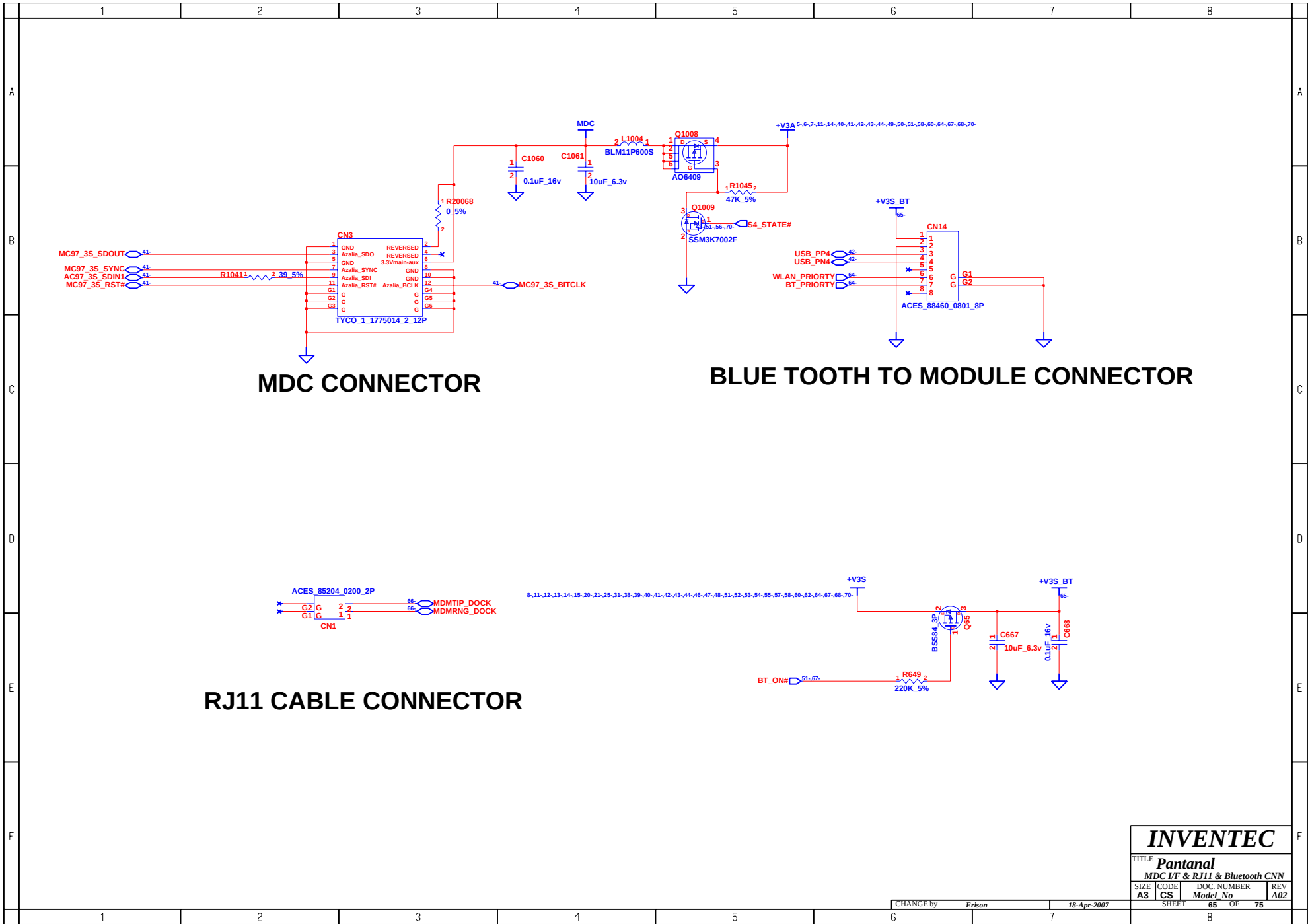
TITLE			
Pantanal AUDIO CODEC			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

CHANGE by Erison 25-Apr-2007

SHEET 62 OF 75



INVENTEC			
TITLE Pantanal WLAN & SIM SOCKET			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02
CHANGE by Erison		18-Apr-2007	
SHEET 64		OF 75	

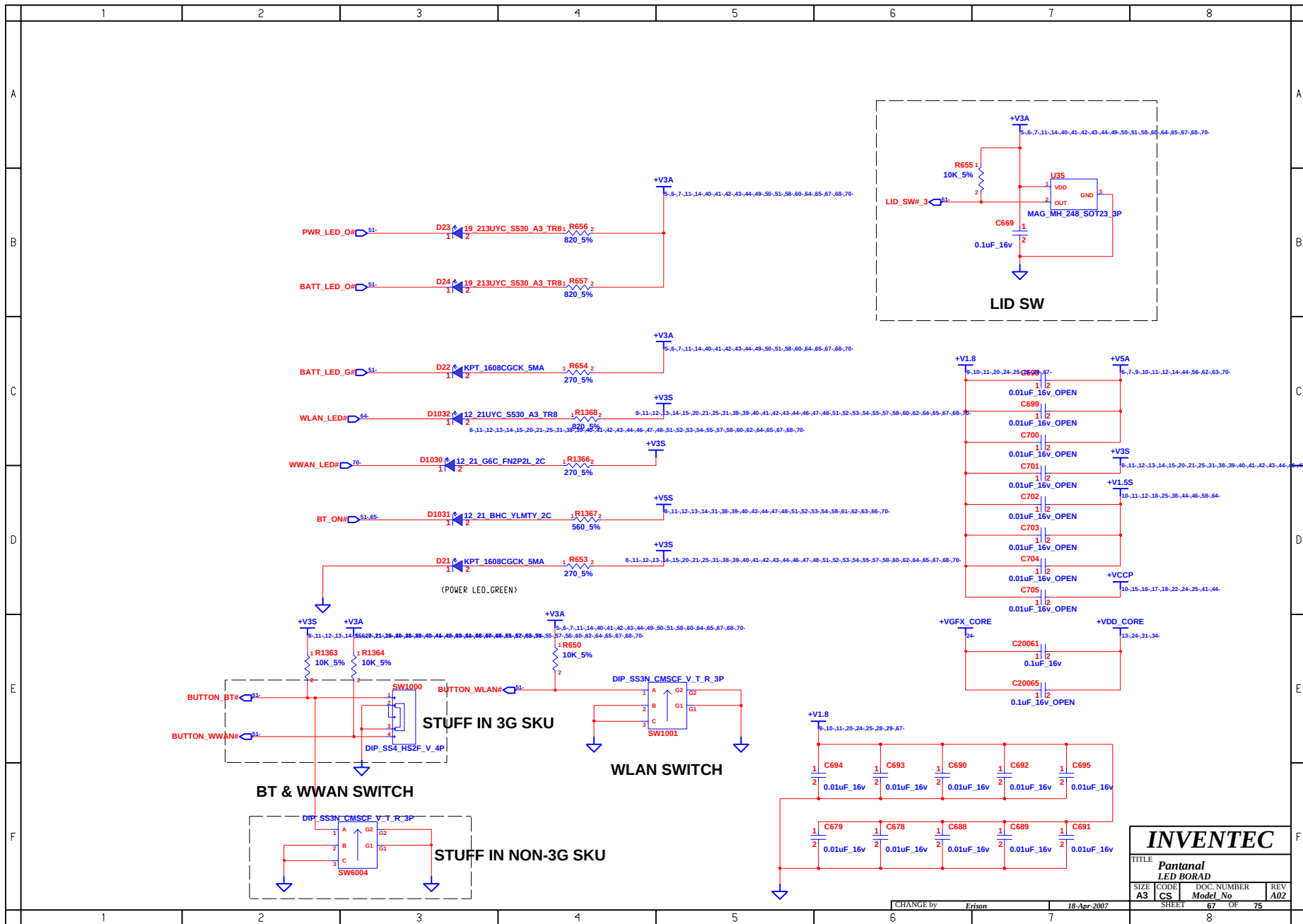


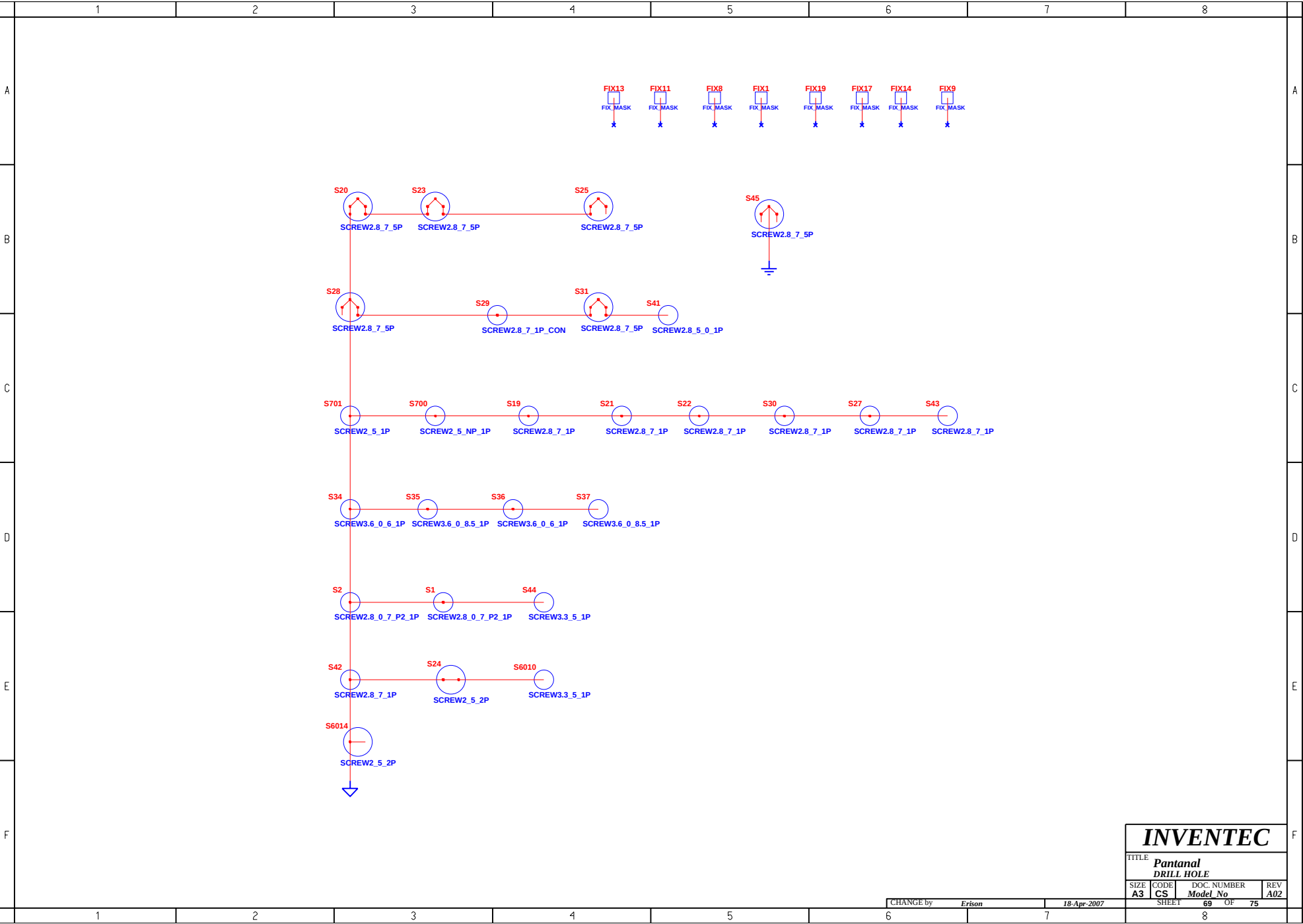
INVENTEC

TITLE Pantanal			
MDC I/F & RJ11 & Bluetooth CNN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A02

CHANGE by *Erison* 18-Apr-2007

SHEET 65 OF 75



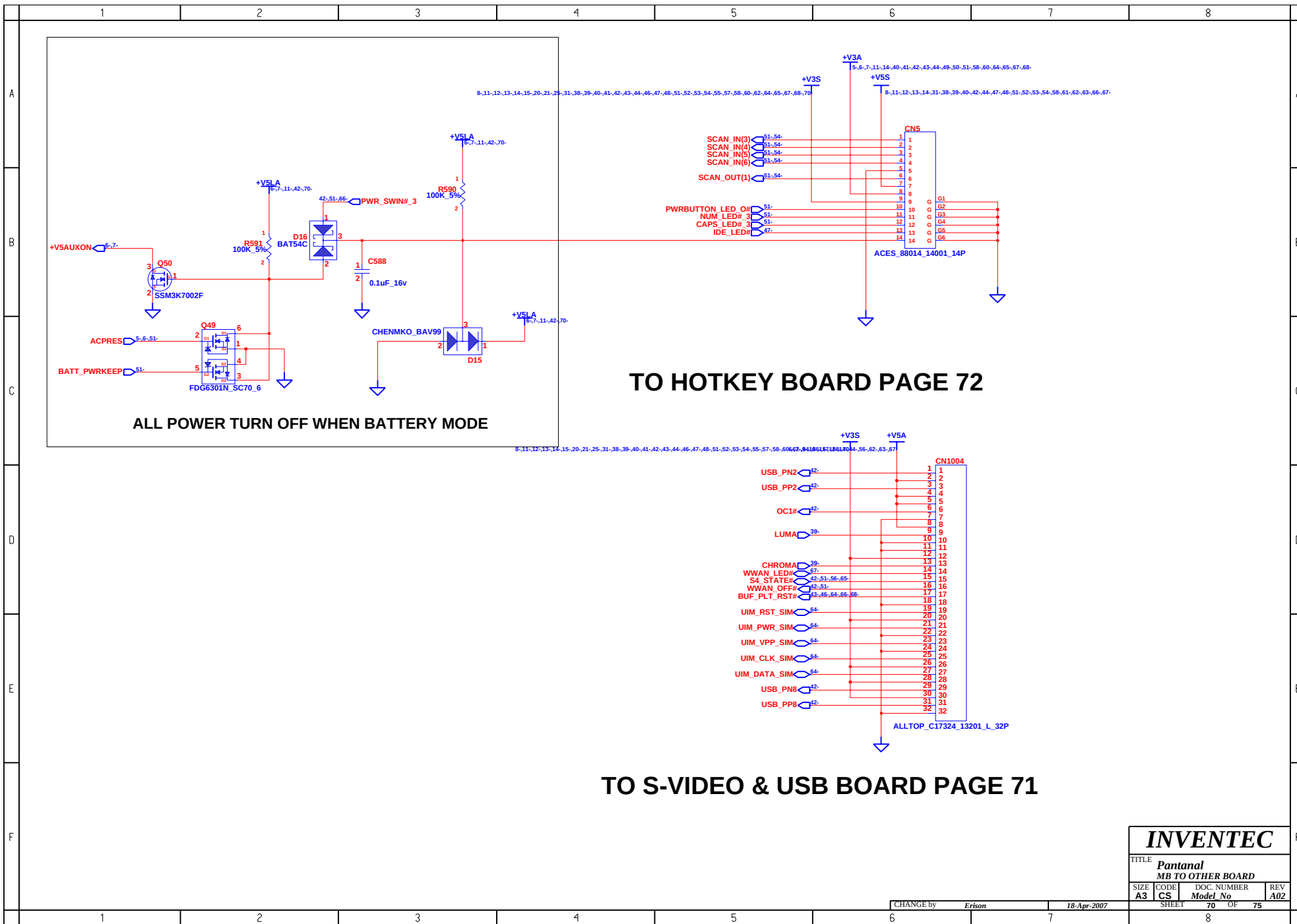


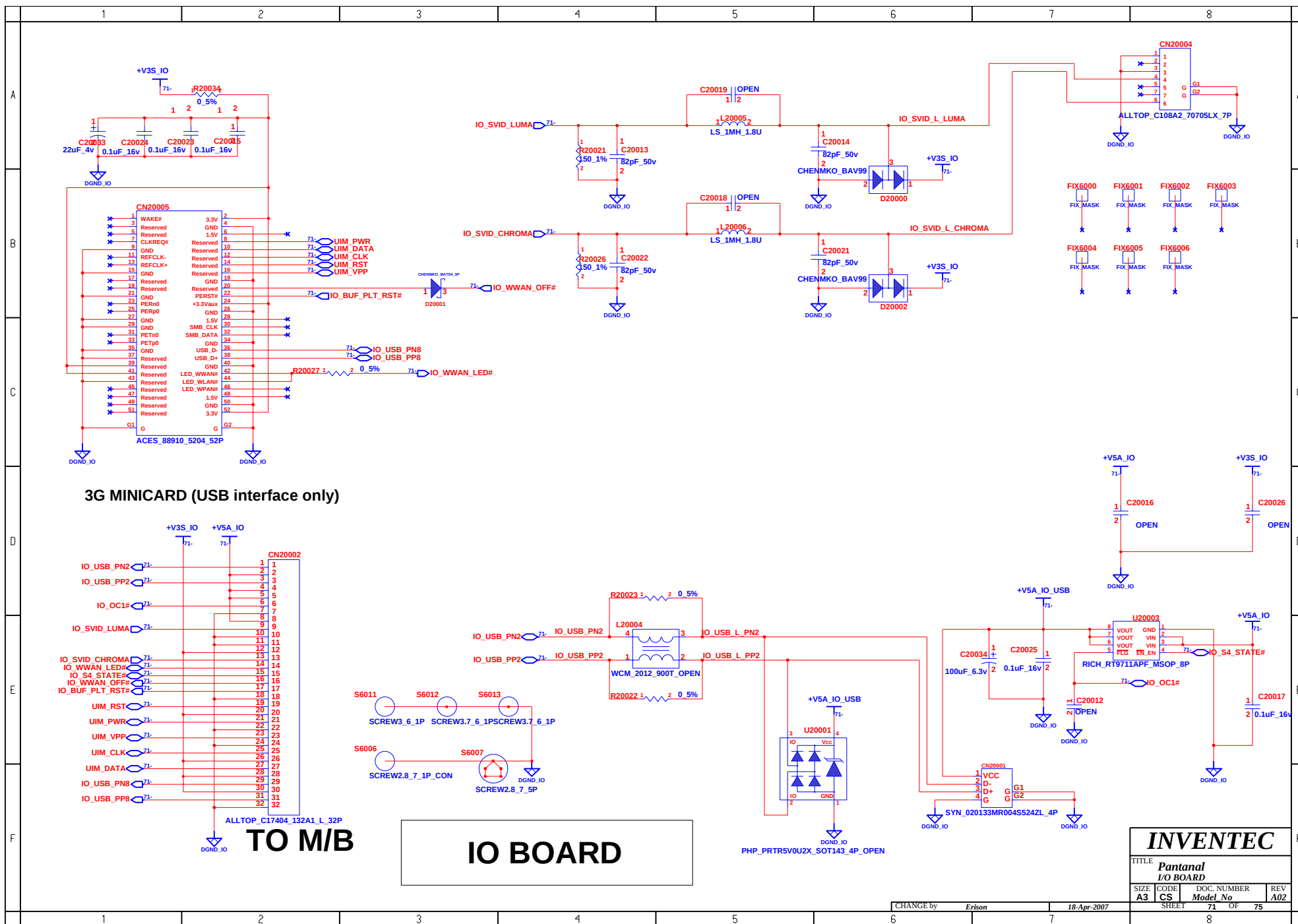
INVENTEC

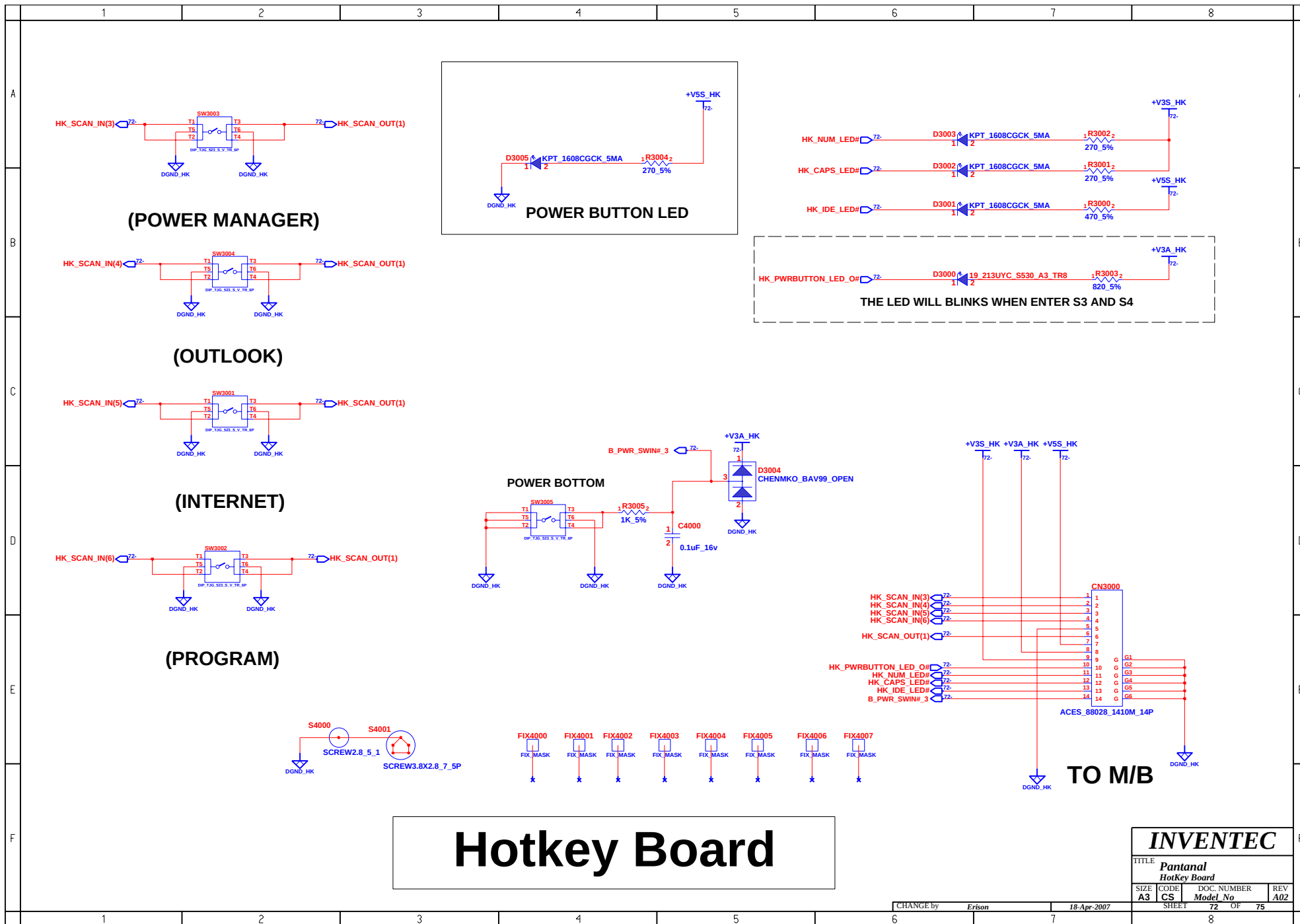
TITLE			
Pantanal DRILL HOLE			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

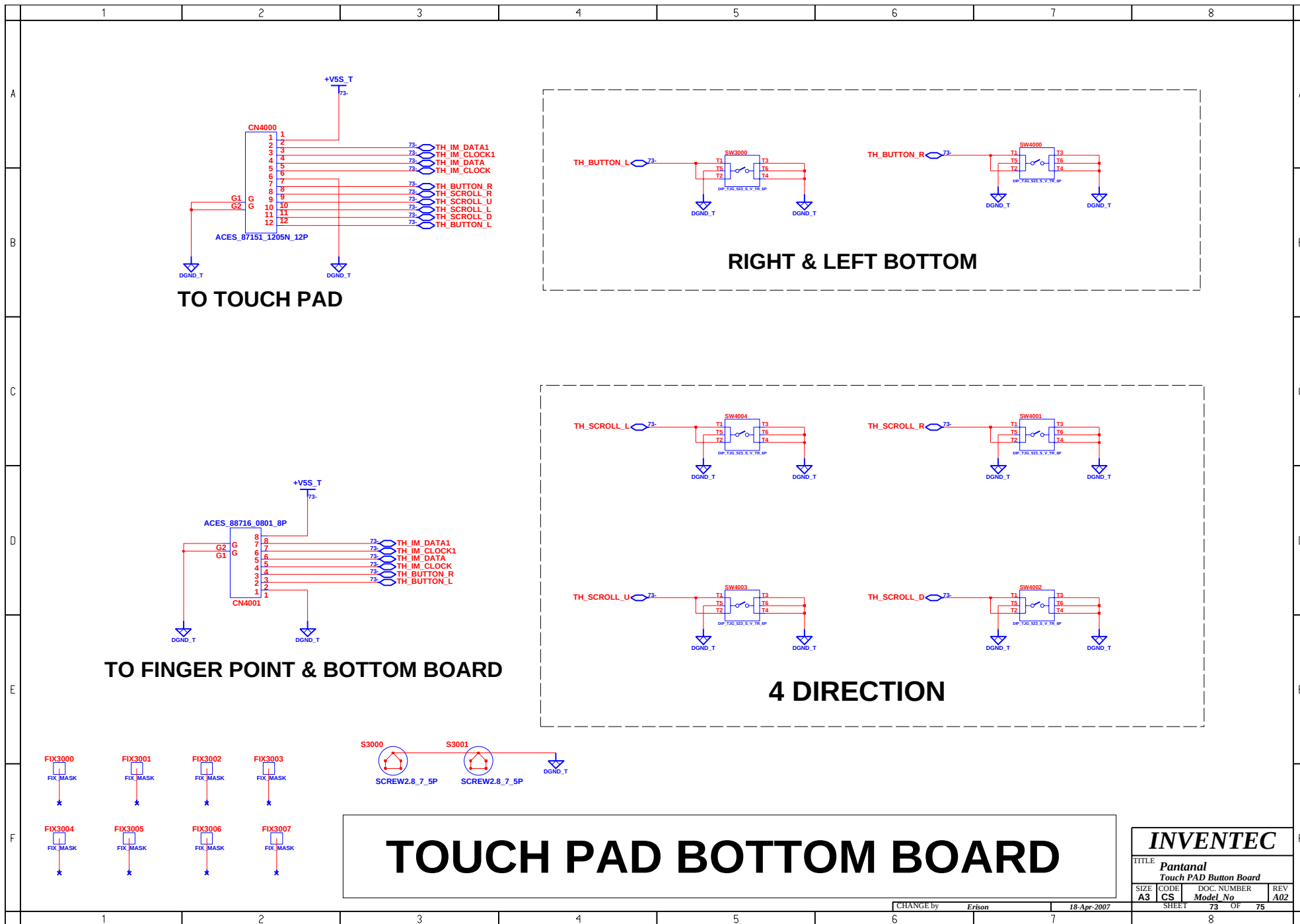
CHANGE by Erison 18-Apr-2007

SHEET 69 OF 75









BAY_ID Table:

BAY_ID0	BAY_ID1	DEVICE
0	0	RESERVE
0	1	PATA HDD
1	0	PATA ODD
1	1	SATA HDD

Key Components and Connections:

- Connectors:** CN5000, CN5001, S6002, S6003.
- ICs:** FOX_QL00253_A607_9F_50P, SYN_800170MA050S100ZR_50P.
- Power:** +V3S_ODD_B, +V5S_ODD_B, ODD_GND.
- Signals:** ODD_PIDE_3S_D(0-15), ODD_PIDE_3S_DREQ, ODD_PIDE_3S_IOR#, ODD_PIDE_3S_DACK#, ODD_PDIAG#, ODD_PIDE_3S_A(0-2), ODD_PIDE_3S_CS(0-1), ODD_CD_LED#, ODD_PIDE_3S_CS(0-1), ODD_PIDE_3S_A(0-1), ODD_PIDE_3S_IQDY, ODD_PIDE_3S_IORDY, ODD_PIDE_3S_A(0), ODD_PIDE_3S_CS(0), ODD_CD_LED#.

Notes:

- WHEN SYSTEM "BAY_INS#" GET A "LOW" SYSTEM WILL TURN ON ODD POWER.
- SCREW2.2_5_0_1P and SCREW2.2_4V2.4_0_1P are connected to ODD_GND.

INVENTEC TITLE

Pantanal
Multi-Bay ODD Board

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A02

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TITLE			
<i>Pantanal</i> <i>Multi-Bay ODD Board</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	A02

